TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOS IV)

TPCF8302

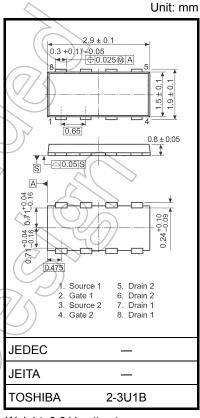
Notebook PC Applications Portable Equipment Applications

- Low drain-source ON resistance: $R_{DS (ON)} = 44 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance: $|Y_{fs}| = 6.2 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = -10 \mu A \text{ (max) (V}_{DS} = -20 \text{ V)}$
- Enhancement mode: $V_{th} = -0.5 \text{ to } -1.2 \text{ V}$

 $(V_{DS} = -10 \text{ V}, I_D = -200 \mu\text{A})$

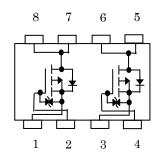
Absolute Maximum Ratings (Ta = 25°C)

Cha	racteristics	Symbol	Rating	Unit
Drain-source voltage	ge	V _{DSS}	-20	V
Drain-gate voltage	(R _{GS} = 20 kΩ)	V _{DGR}	-20	/V-
Gate-source voltage	ge	VGSS	±10	V
Drain current	DC (Note 1)	((I _D))	-3.0	A
Drain current	Pulse (Note 1)	IDP	-12	
Drain power	Single-device operation (Note 3a)	P _D (1)	1.35	
dissipation (t = 5 s) (Note 2a)	Single-device value at dual operation (Note 3b)	P _{D (2)}	1.12	\searrow
Drain power	Single-device operation (Note 3a)	P _D (1)	0,53	VV
(t = 5 s) (Note 2b)	Single-device value at dual operation (Note 3b)	P _D (2)	0.33	
Single pulse avalar	nche energy (Note 4)	Eas	0.58	mJ
Avalanche current		I _{AR}	-1.5	Α
Repetitive avalanche energy Single-device value at dual operation (Note 2a, 3b, 5)		E _{AR}	0.11	mJ
Channel temperatu	ire	T _{ch}	150	°C
Storage temperature range) T _{stg}	-55 to 150	°C



Weight: 0.011 g (typ.)

Circuit Configuration



Note: (Note 1), (Note 2), (Note 3), (Note 4) and (Note 5): See the next page.

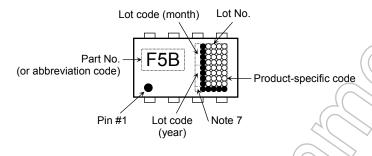
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Please handle with caution.

Thermal Characteristics

Characteristics		Symbol	Max	Unit
Thermal resistance, channel to ambient	Single-device operation (Note 3a) Rth (ch-a) (1)		92.6	°C/W
(t = 5 s) (Note 2a)	Single-device value at dual operation (Note 3b) Rth (ch-a) (2) 1		111.6	
Thermal resistance,	Single-device operation (Note 3a)	R _{th (ch-a) (1)}	235.8	°C/W
channel to ambient (t = 5 s) (Note 2b)	Single-device value at dual operation (Note 3b)	R _{th (ch-a) (2)}	378.8	G/VV

Marking (Note 6)



- Note 1: Ensure that the channel temperature does not exceed 150°C.
- Note 2: (a) Device mounted on a glass-epoxy board (b) Device mounted on a glass-epoxy board (b)



- Note 3: a) The power dissipation and thermal resistance values are shown for a single device (During single-device operation, power is only applied to one device.).
 - b) The power dissipation and thermal resistance values are shown for a single device (During dual operation, power is evenly applied to both devices.).
- Note 4: $V_{DD} = -16 \text{ V}$, $T_{ch} = 25 ^{\circ}\text{C}$ (initial), L = 0.5 mH, $R_G = 25 \Omega$, $I_{AR} = -1.5 \text{ A}$
- Note 5: Repetitive rating: pulse width limited by maximum channel temperature
- Note 6: on the lower left of the marking indicates Pin 1.
- Note 7: A dot marking identifies the indication of product Labels.

Without a dot: [[Pb]]/INCLUDES > MCV

With a dot: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

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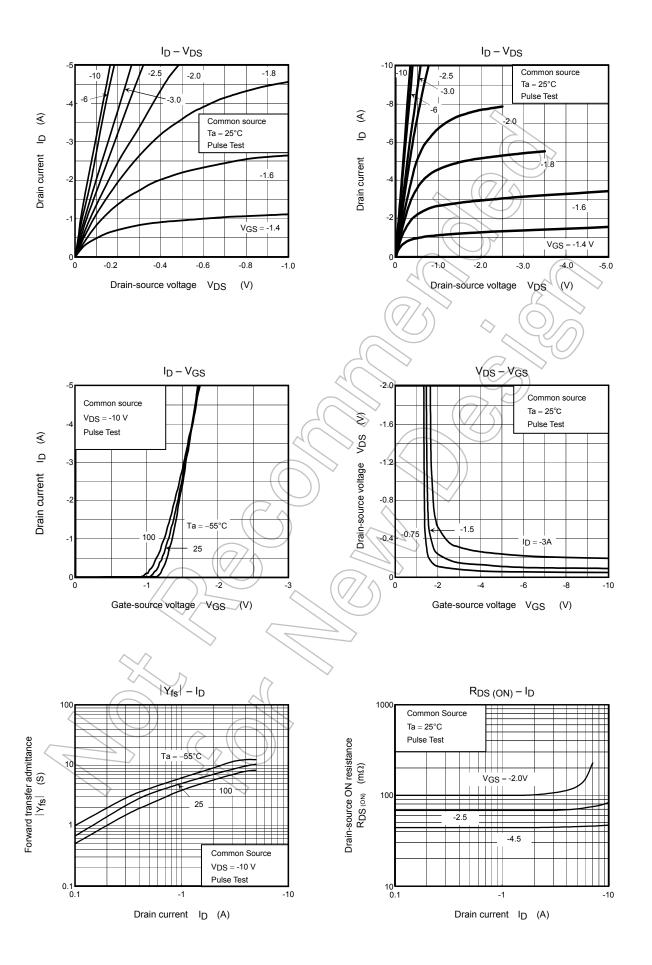
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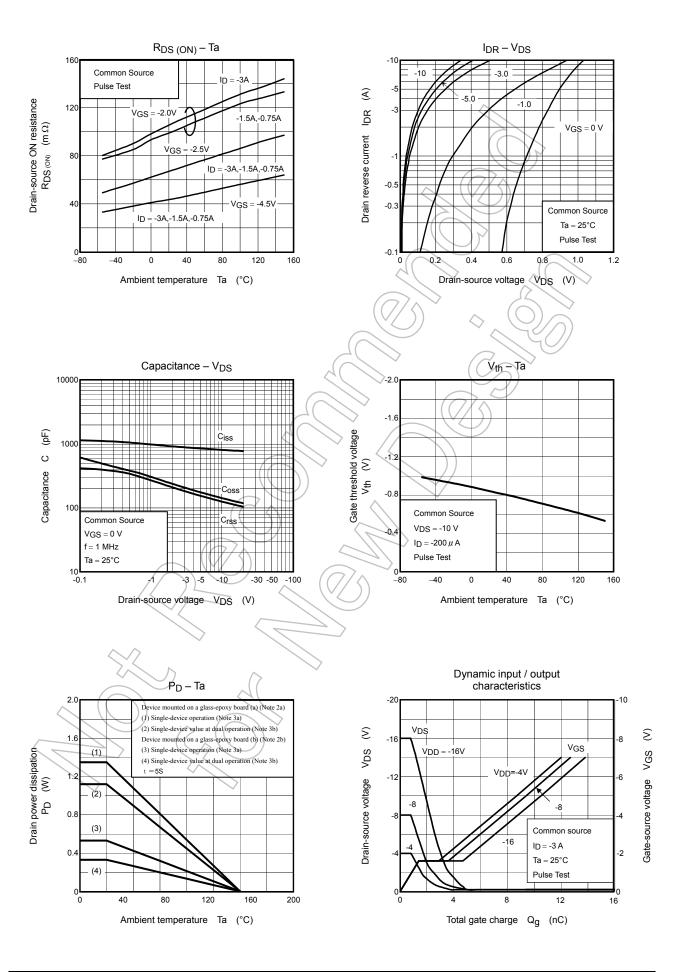
Electrical Characteristics (Ta = 25°C)

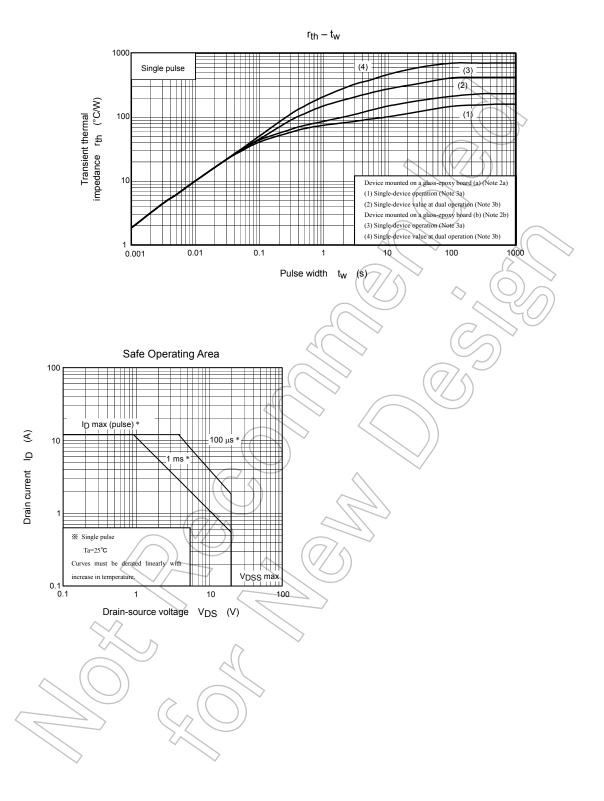
Ch	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cui	rrent	I _{GSS}	$V_{GS} = \pm 10V, V_{DS} = 0 V$	_	_	±10	μА
Drain cut-off curr	ent	I _{DSS}	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μА
Drain source breekdown veltege		V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-20	_	_	V
Dialii-source bre	Orain-source breakdown voltage		$I_D = -10 \text{ mA}, V_{GS} = 10 \text{ V}$	10		_	V
Gate threshold v	oltage	V _{th}	$V_{DS} = -10 \text{ V}, I_D = -200 \mu\text{A}$	0.5	4	-1.2	٧
		R _{DS} (ON)	$V_{GS} = -2.0 \text{ V}, I_D = -1.5 \text{ A}$		100	200	
Drain-source ON	l resistance	R _{DS} (ON)	$V_{GS} = -2.5 \text{ V}, I_D = -1.5 \text{ A}$		68	95	mΩ
		R _{DS} (ON)	$V_{GS} = -4.5 \text{ V}, I_D = -1.5 \text{ A}$	7)	44	59	
Forward transfer	admittance	Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -1.5 \text{ A}$	3.1	6.2	_	S
Input capacitance		C _{iss}			800	_	
Reverse transfer capacitance		C _{rss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		120	/-	pF
Output capacitan	nce	C _{oss}	, v		160	\geq	
	Rise time	t _r	V _{GS} -5 V I _D = -1.5 A O V _{OUT} C _C O V _{OUT} C _C O V _{OUT} O	-(6.2	> -	
Consideration on Alice of	Turn-on time	t _{on}			15	_	ns
Switching time	Fall time	t _f		(\mathcal{E})	17	_	
	Turn-off time	t _{off}	$V_{DD} \simeq -10 \text{ V}$ Duty \leq 1%, $t_W = 10 \mu\text{s}$) —	51	_	
Total gate charge (gate-source plus	tal gate charge ate-source plus gate-drain) $Q_{g} V_{DD} \simeq -16 \text{ V}, V_{GS} = -5 \text{ V},$		_				
Gate-source charge1		Q _{gs1}	$I_D = -3 \text{ A}$	_	1.1	_	nC
Gate-drain ("miller") charge		Q _{gd}		_	3.3	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characterist	ics	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse (Note 1)	IDRP	_	_	_	-12	Α
Forward voltage (diode)		VDSF	$I_{DR} = -3.0 \text{ A}, V_{GS} = 0 \text{ V}$	_		1.2	V







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