TOSHIBA Field Effect Transistor Silicon P-Channel MOS Type (U-MOS V)

TPCC8103

Notebook PC Applications Portable Equipment Applications

- Small footprint due to a small and thin package
- Low drain-source ON-resistance:
 - $R_{DS (ON)}$ = 9.4 m Ω (typ.) (V_{GS} = -10 V)
- Low leakage current: I_{DSS} = -10 μ A (max) (V_{DS} = -30 V)
- Enhancement mode: V_{th} = -0.8 to -2.0 V (V_{DS} = -10 V, I_D = -1.0 mA)

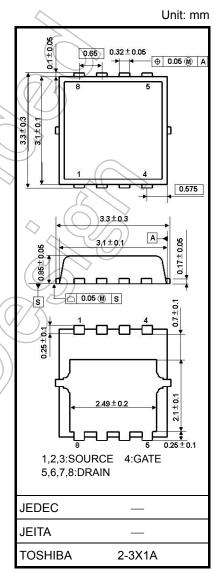
Absolute Maximum Ratings (Ta = 25°C)

				$\left(\bigcap \right) $	
Characteristic		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	-30	y	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	-30	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	۱ _D	-18	A	
Drain current	Pulsed (Note 1)	I _{DP}	-54		
Drain power dissipation $(Tc = 25^{\circ}C)$		PD	27	$\langle \langle w \rangle$	
Drain power dissipation (t = 10 s) (Note 2a)		PD	1.9	×	
Drain power dissipation (t = 10 s) (Note 2b)		PD	0.7	w	
Single-pulse avalanche energy (Note 3)		EAS	84	mJ	
Avalanche current			-18	А	
Repetitive avalanche energy (Tc = 25°C) (Note 4)		Ear	1.59	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	
		A			

Note: For Notes 1 to 4, refer to the next page.

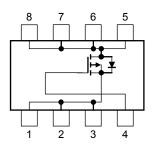
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.



Weight: 0.02 g (typ.)

Circuit Configuration



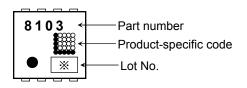
Start of commercial production 2009-06

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Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case (Tc = 25°C)	R _{th (ch-c)}	4.7	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	66	°C/W
Thermal resistance, channel to ambient $(t = 10 \text{ s})$ (Note 2b)	R _{th (ch-a)}	180	°C/W

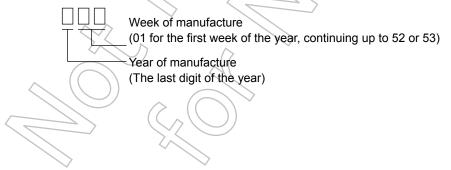
Marking (Note 5)



- Note 1: Ensure that the channel temperature does not exceed 150°C.
- Note 2: (a) Device mounted on a glass-epoxy board (a)
 - (a) FR-4 25.4 × 25.4 × 0.8 (Unit: mm) (b) FR-4 25.4 × 25.4 × 0.8 (Unit: mm)

(b) Device mounted on a glass-epoxy board (b)

- Note 3: $V_{DD} = -24 \text{ V}, T_{ch} = 25^{\circ}\text{C}$ (initial), L = 200 μ H, R_G = 25 Ω , I_{AR} = -18 A
- Note 4: Repetitive rating: pulse width limited by maximum channel temperature
- Note 5: * Weekly code: (Three digits)

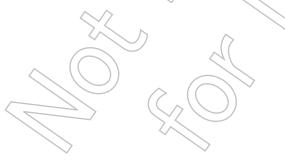


Electrical Characteristics (Ta = 25°C)

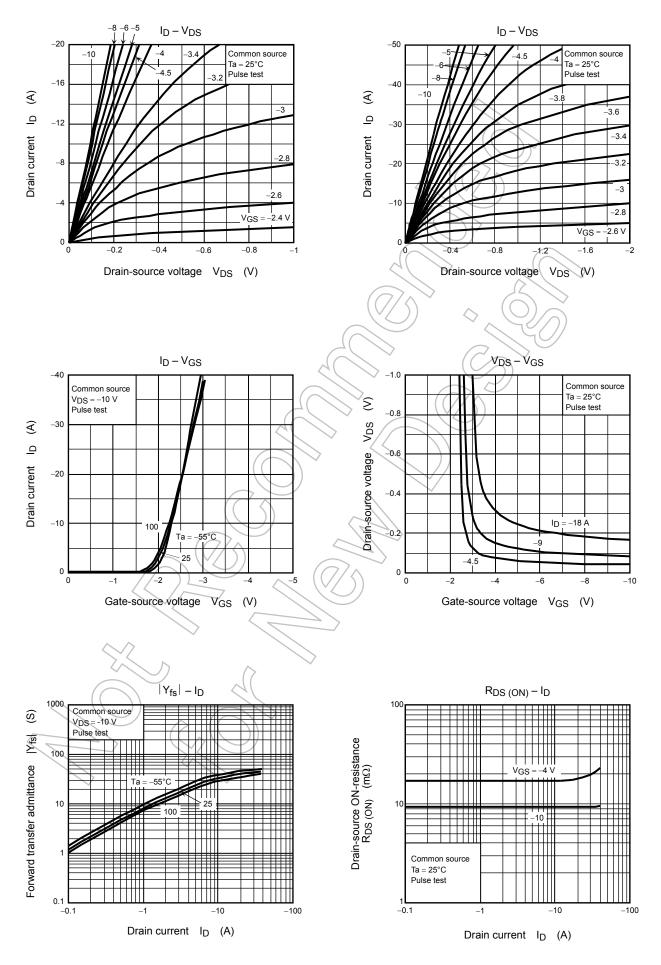
Cł	aracteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	$V_{GS}=\pm 20~V,~V_{DS}=0~V$	_	_	±100	nA
Drain cutoff curre	ent	I _{DSS}	$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	_		-10	μA
Drain-source breakdown voltage		V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-30	_	_	V
		V (BR) DSX	$I_D = -10$ mA, $V_{GS} = 20$ V	-13	1	_	v
Gate threshold v	oltage	V _{th}	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1.0 \text{ mA}$	-0.8)/	-2.0	V
Drain-source ON-resistance		R _{DS (ON)}	$V_{GS} = -4 V, I_D = -9 A$		17	25	mΩ
			$V_{GS} = -10 \text{ V}, \text{ I}_{D} = -9 \text{ A}$	ð	9.4	12	
Forward transfer	admittance	Y _{fs}	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -9 \text{ A}$	15	30		S
Input capacitance		C _{iss}		_	1600	_	
Reverse transfer capacitance		C _{rss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		340	1	pF
Output capacitance		C _{oss}		_	490	7	
Switching time	Rise time	tr			9.3		
	Turn-on time	t _{on}			16	/	
	Fall time	t _f	v _{DD} ≈-15V	Ð	68	_	- ns
	Turn-off time	t _{off}	Duty $\leq 1\%$, t _w = 10 μ s) —	175		
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \approx -24 \text{ V}, \text{ V}_{GS} = -10 \text{ V},$	_	38		
Gate-source charge 1		Q _{gs1}	I _D = -18 A	_	4.5	_	nC
Gate-drain ("Miller") charge		Qgd	\wedge	_	11	_	

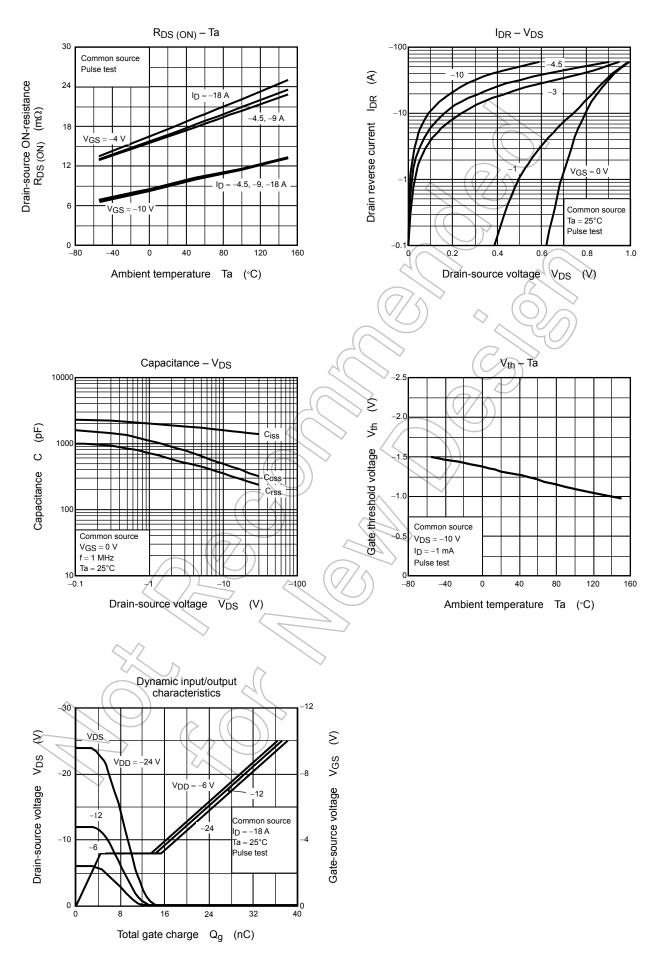
Source-Drain Ratings and Characteristics (Ta = 25°C)

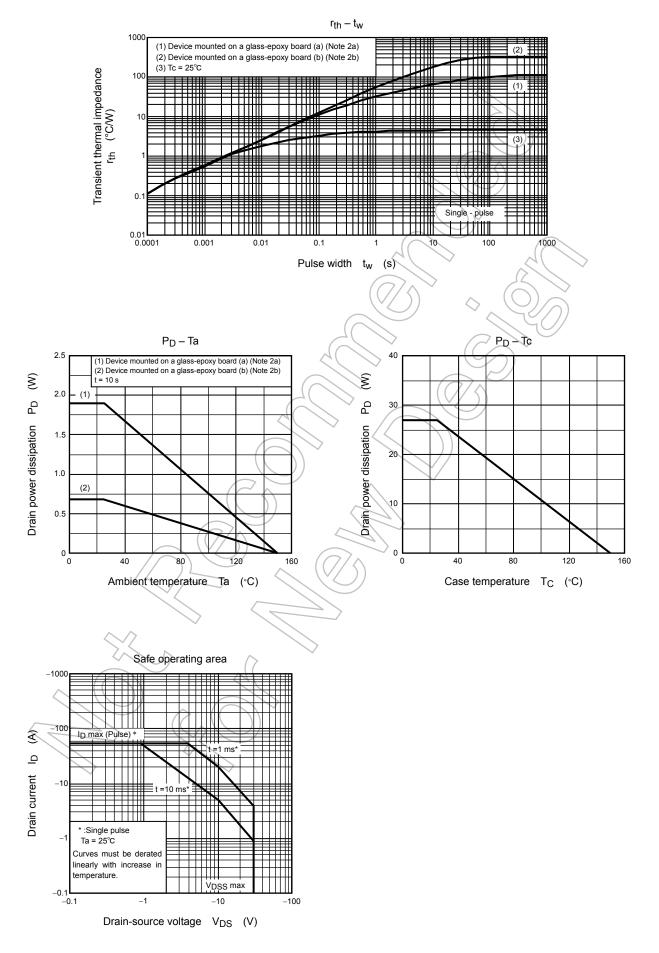
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current Pulse (Note 1)	I _{DRP}			_	-54	А
Forward voltage (diode)	VDSF	I _{DR} = -18 A, V _{GS} = 0 V		_	1.2	V



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