MOSFETs Silicon N-Channel MOS (U-MOSVII-H)

TPC8065-H

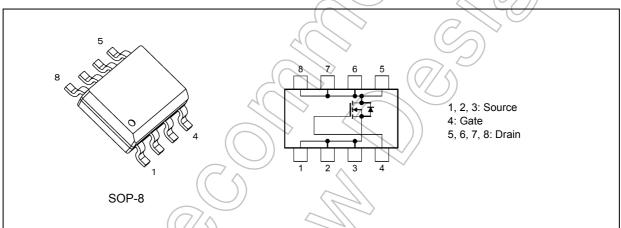
1. Applications

- · High-Efficiency DC-DC Converters
- · Notebook PCs
- · Mobile Handsets

2. Features

- (1) Small, thin package
- (2) High-speed switching
- (3) Small gate charge: $Q_{SW} = 4.3 \text{ nC (typ.)}$
- (4) Low drain-source on-resistance: $R_{DS(ON)} = 11.9 \text{ m}\Omega$ (typ.) ($V_{GS} = 4.5 \text{ V}$)
- (5) Low leakage current: $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 30 \text{ V)}$
- (6) Enhancement mode: $V_{th} = 1.3 \text{ to } 2.3 \text{ V } (V_{DS} = 10 \text{ V}, I_D = 0.2 \text{ mA})$

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) (Ta = 25°C unless otherwise specified)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	30	V
Gate-source voltage		V _{GSS}	±20	
Drain current (DC)	(Note 1)	I _D	13	Α
Drain current (pulsed)	(Note 1)	I _{DP}	52	
Power dissipation (t = 10 s)	(Note 2)	P _D	1.9	W
Power dissipation (t = 10 s)	(Note 3)	P _D	1.0	W
Single-pulse avalanche energy	(Note 4)	E _{AS}	43	mJ
Avalanche current		I _{AR}	13	Α
Channel temperature		T _{ch}	150	°C
Storage temperature		T _{stg}	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production



5. Thermal Characteristics

Characteristics			Symbol	Max	Unit
Channel-to-ambient thermal resistance	(t = 10 s)	(Note 2)	R _{th(ch-a)}	65.7	°C/W
Channel-to-ambient thermal resistance	(t = 10 s)	(Note 3)	R _{th(ch-a)}	125	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: Device mounted on a glass-epoxy board (a), Figure 5.1

Note 3: Device mounted on a glass-epoxy board (b), Figure 5.2

Note 4: V_{DD} = 24 V, T_{ch} = 25°C (initial), L = 0.2 mH, R_G = 1.2 Ω , I_{AR} = 13 A

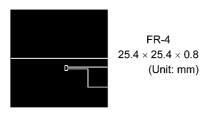
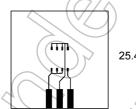


Fig. 5.1 Device Mounted on a Glass-Epoxy Board (a)



 $\begin{aligned} & \text{FR-4} \\ 25.4 \times 25.4 \times 0.8 \\ & \text{(Unit: mm)} \end{aligned}$

Fig. 5.2 Device Mounted on a Glass-Epoxy Board (b)

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

Rev.3.0

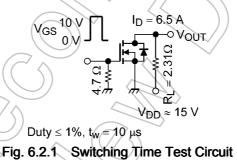
6. Electrical Characteristics

6.1. Static Characteristics (T_a = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±0.1	μА
Drain cut-off current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V		_	10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	30	_	_	V
	V _{(BR)DSX}	I _D = 10 mA, V _{GS} = -20 V	15) >		
Gate threshold voltage	V_{th}	V _{DS} = 10 V, I _D = 0.2 mA	1.3	ソ ー	2.3	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = 4.5 V, I _D = 6.5 A	/ A	11.9	14.7	mΩ
		V _{GS} = 10 V, I _D = 6.5 A)	9.6	11.6	

6.2. Dynamic Characteristics (T_a = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	- /	1350		pF
Reverse transfer capacitance	C _{rss}	((//5)	_((63	_	
Output capacitance	C _{oss}		X	240) —	
Gate resistance	r _g	V _{DS} = 10 V, V _{GS} = 0 V, f = 5 MHz	1.0	14	2.1	Ω
Switching time (rise time)	t _r	See Figure 6.2.1.		2.1	_	ns
Switching time (turn-on time)	t _{on}		/ _/ _//	8.0		
Switching time (fall time)	t _f		<u> </u>	2.4		
Switching time (turn-off time)	t _{off}		ソー	19	_	



6.3. Gate Charge Characteristics ($T_a = 25^{\circ}$ C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus	Qg	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 13 \text{ A}$	_	20	_	nC
gate-drain)		$V_{DD} \approx 24 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 13 \text{ A}$	_	9.9	_	
Gate-source charge 1	Q _{gs1}	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 13 \text{ A}$	_	4.4	_	
Gate-drain charge	Q_{gd}		_	2.1	_	
Gate switch charge	Q _{SW}		_	4.3	_	

6.4. Source-Drain Characteristics (T_a = 25°C unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (pulsed)	(Note 5)	I _{DRP}	_	_	_	52	Α
Diode forward voltage		V_{DSF}	I _{DR} = 13 A, V _{GS} = 0 V	_	_	-1.2	V

Note 5: Ensure that the channel temperature does not exceed 150°C.



7. Marking (Note)

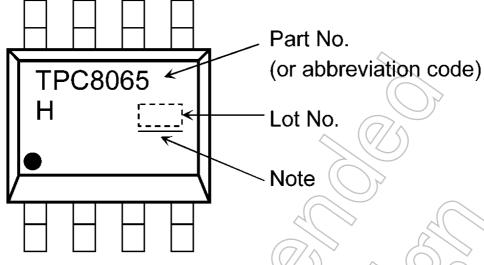


Fig. 7.1 Marking

Note: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

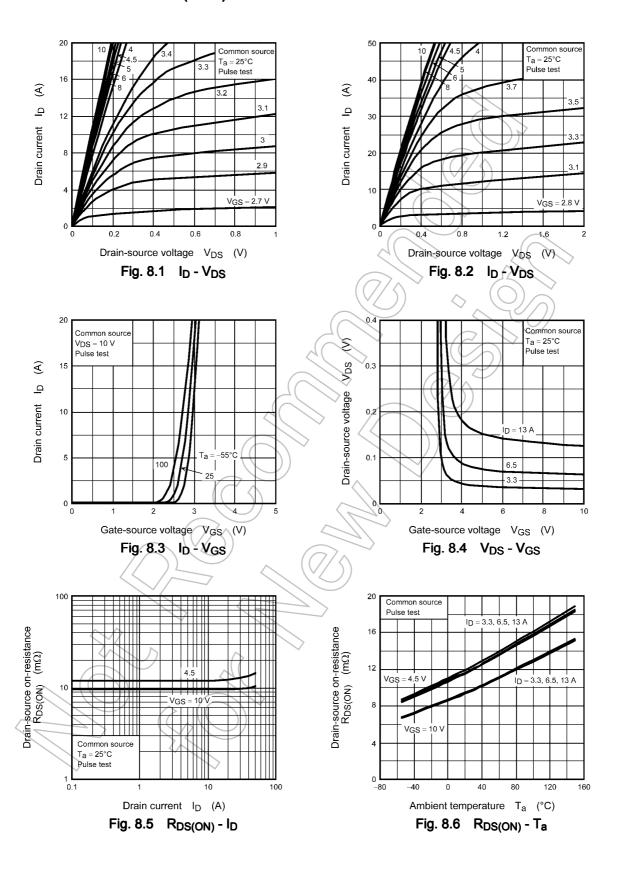
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.



8. Characteristics Curves (Note)



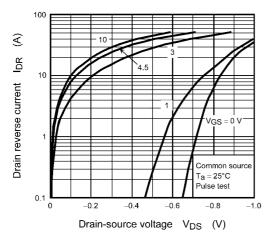


Fig. 8.7 IDR - VDS

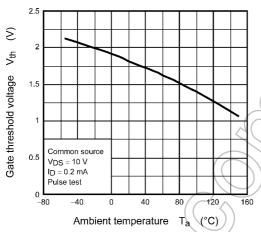


Fig. 8.9 Vth - Ta

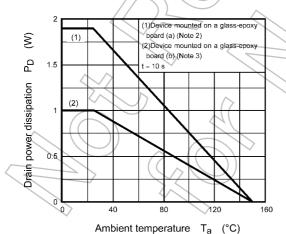


Fig. 8.11 P_D - T_a (Guaranteed Maximum)

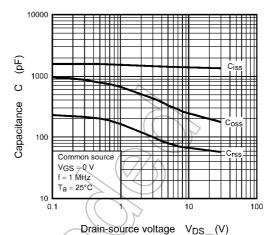


Fig. 8.8 Capacitance - V_{DS}

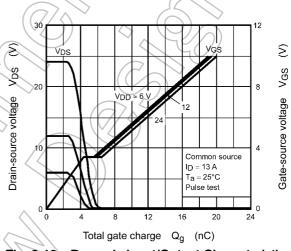
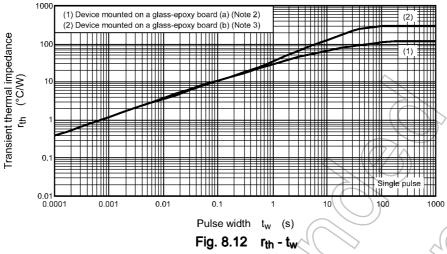


Fig. 8.10 Dynamic Input/Output Characteristics



(Guaranteed Maximum)

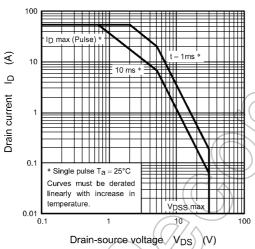


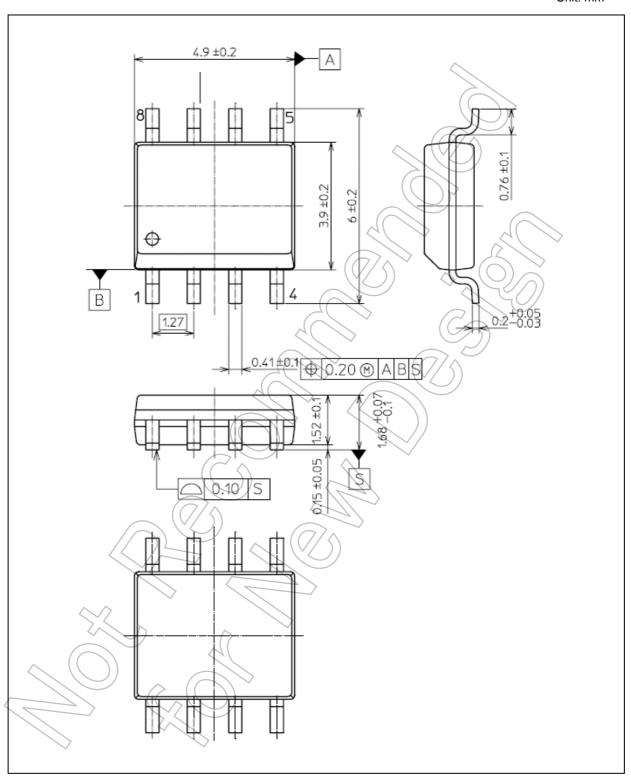
Fig. 8.13 Safe Operating Area (Guaranteed Maximum)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

Unit: mm



Weight: 0.085 g (typ.)

	Package Name(s)
TOSHIBA: 2-5R1S	
Nickname: SOP-8	



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