TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSIV)

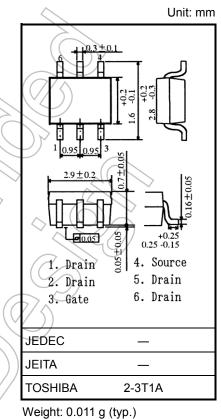
TPC6012

Notebook PC Applications Portable Equipment Applications

- Small footprint due to small and thin package
- Low drain-source ON resistance: $RDS (ON) = 20 m\Omega (typ.)$
- Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 20 \ V)$
- Enhancement mode: V_{th} = 0.5 to 1.2 V (V_{DS} = 10 V, I_{D} = 200 μA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit		
Drain-source voltage		V _{DSS}	20	(Y)	\geq	
Drain-gate voltage (R _{GS} = 20 k Ω)		Vdgr	20	$\langle \rangle$		
Gate-source voltage		V _{GSS}	± 12	V		
Dania average	DC	(Note 1)	۱ _D	6	A	
Drain current	Pulse	(Note 1)	I _{DP}	24	> A	
Drain power dissipatio	on	(t = 5 s) (Note 2a)	PD	2.2	W	(
Drain power dissipatio	on	(t = 5 s) (Note 2b)	PD	0.7	XV	
Single pulse avalanche energy (Note 3)		Eas	2.3	mJ		
Avalanche current		IAR	3	А	\sim	
Channel temperature			150	°C		
Storage temperature range		Tstg	-55 to 150	°C		

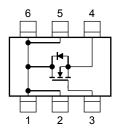


Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient $(t = 5 s)$ (Note 2a)	Rth (ch-a)	56.8	°C/W
Thermal resistance, channel to ambient (t = 5 s) (Note 2b)	Rth (ch-a)	178.5	°C/W

Circuit Configuration



Note: (Note 1), (Note 2), (Note 3): See other pages.

This transistor is an electrostatic-sensitive device. Please handle with caution.

Electrical Characteristics (Ta = 25°C)

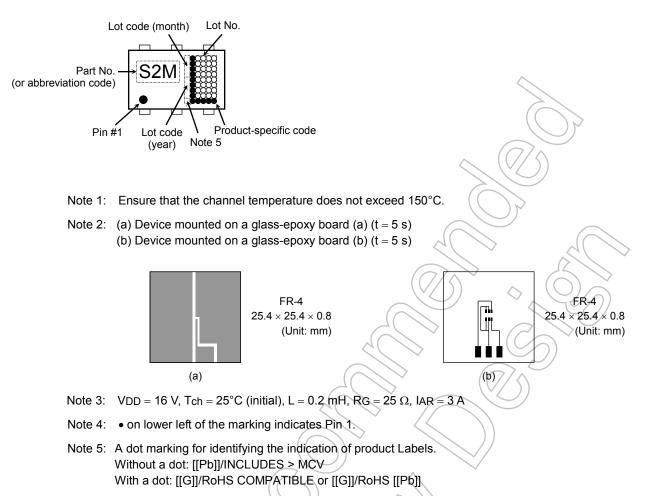
Ch	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cu	rrent	IGSS	$V_{GS} = \pm 12 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$			±100	nA	
Drain cut-off curr	ent	IDSS	V _{DS} = 20 V, V _{GS} = 0 V	_	_	10	μA	
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	20	_		v	
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -12 \text{ V}$	8	_		V	
Gate threshold v	oltage	Vth	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 200 \mu\text{A}$	0.5)/	1.2	V	
Drain-source ON resistance			V _{GS} = 2.5 V, I _D = 3 A	77	25	38	mΩ	
		RDS (ON)	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 3 \text{ A}$	H	15	20		
Input capacitance		C _{iss}			630	_		
Reverse transfer capacitance		Crss	VDS = 10 V, VGS = 0 V, f = 1 MHz	_	150	_	pF	
Output capacitance		Coss			180	1		
Switching time	Rise time	tr			5	>		
	Turn-on time	t _{on}			210) —	ns	
	Fall time	tf	, , , , , , , , , , , , , , , ,		10	_	115	
	Turn-off time	toff	Duty \leq 1%, t _w = 10 µs		24	_		
Total gate charge (gate-source plus		Qg	V _{DD} ≈ 16 V, V _{GS} = 5 V,) _	9			
Gate-source charge 1		Qgs-1	$I_D = 6 A$	_	1.8	—	nC	
Gate-drain ("miller") charge		Q _{gd}			3.4			

Source-Drain Ratings and Characteristics (Ta = 25° C)

Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse (Note 1)	IDRP			_	24	А
Forward voltage	(diode)	VDSF	$I_{DR} = 6 A, V_{GS} = 0 V$	_		-1.2	V

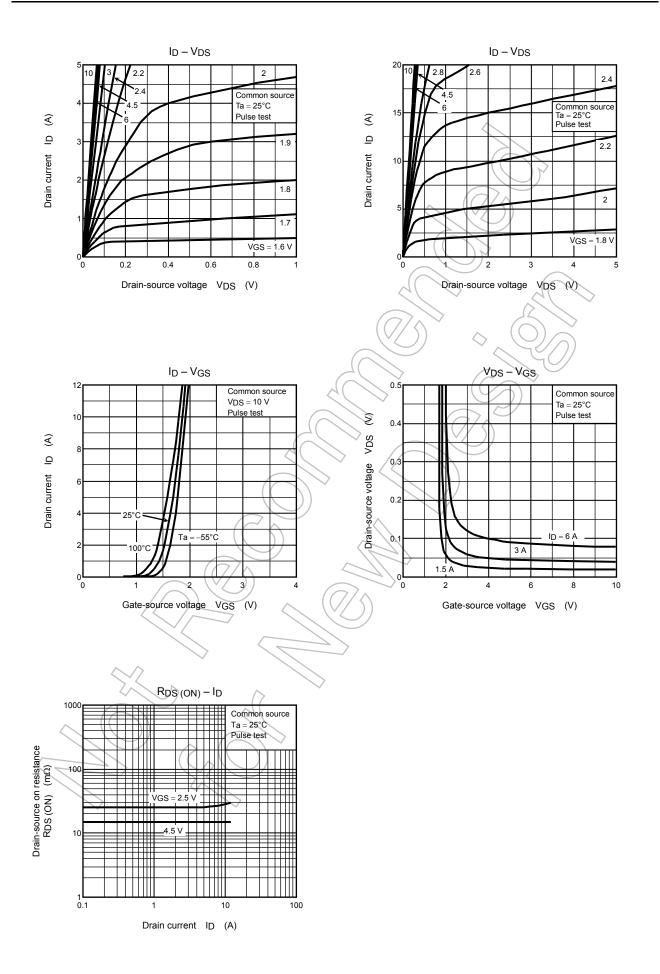


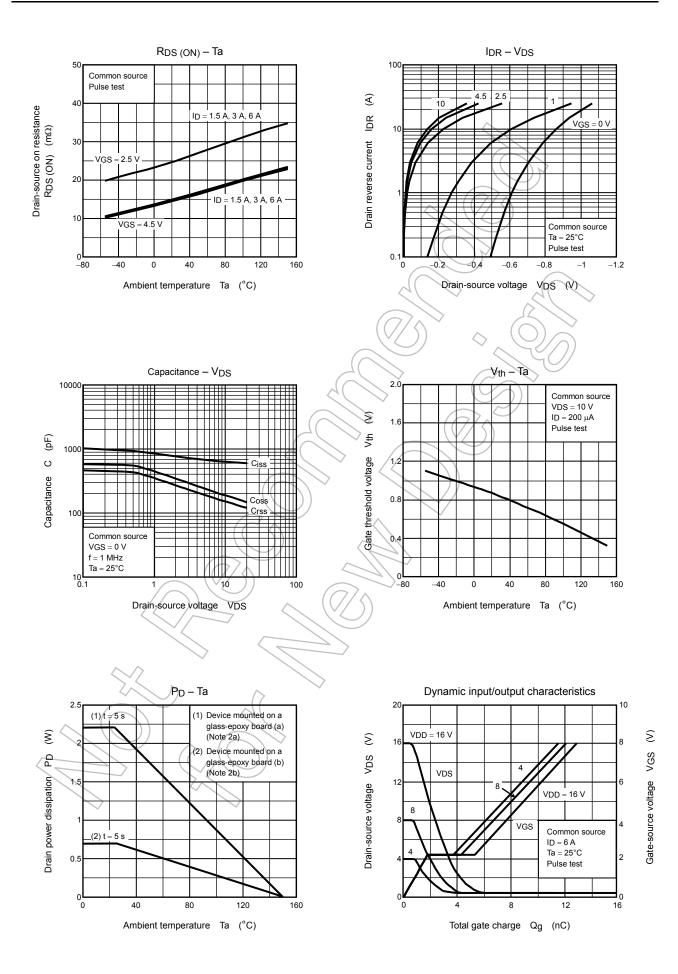
Marking (Note 5)

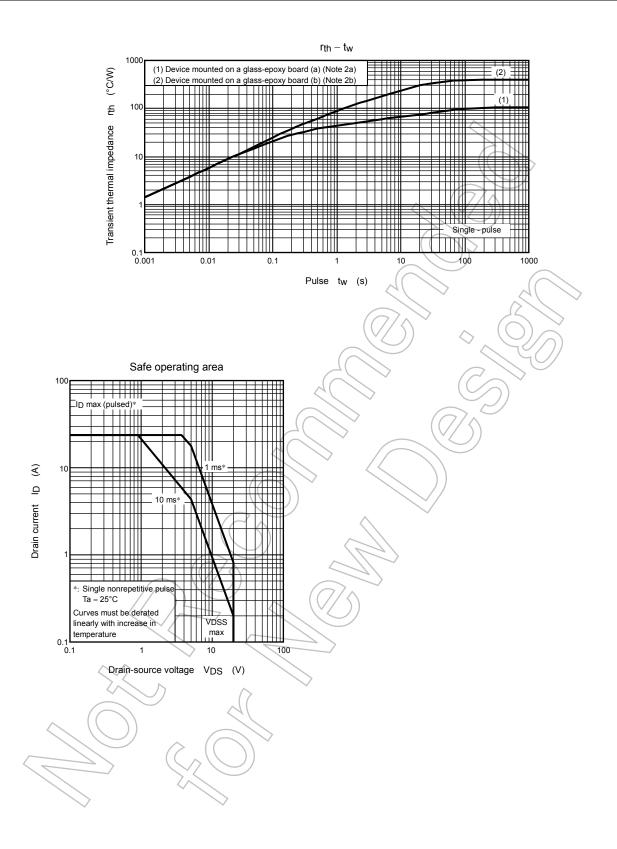


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