TOSHIBA Transistor Silicon NPN Epitaxial Type

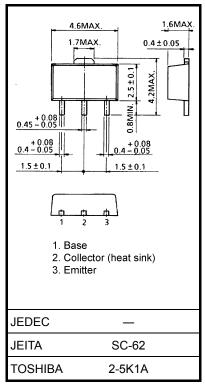
2SC5810

High-Speed Switching Applications DC-DC Converter Applications Strobe Applications

- High DC current gain: h_{FE} = 400 to 1000 (I_C = 0.1 A)
- Low collector-emitter saturation voltage: V_{CE (sat)} = 0.17 V (max)
- High-speed switching: t_f = 85 ns (typ.)

| | | 1 | | | |
|-----------------------------|----------|-------------------------|------------|------|--|
| Characteristics | | Symbol | Rating | Unit | |
| Collector-base voltage | | V _{CBO} | 100 | V | |
| Collector-emitter voltage | | V _{CEX} | 80 | V | |
| | | V _{CEO} | 50 | | |
| Emitter-base voltage | | V _{EBO} | 7 | V | |
| Collector current | DC | Ι _C | 1.0 | A | |
| | Pulse | I _{CP} | 2.0 | | |
| Base current | | Ι _Β | 0.1 | А | |
| Collector power dissipation | DC | D ₋ (Note 1) | 2.0 | W | |
| | t = 10 s | P _C (Note 1) | 1.0 | | |
| Junction temperature | | Тj | 150 | °C | |
| Storage temperature range | | T _{stg} | -55 to 150 | °C | |

Absolute Maximum Ratings (Ta = 25°C)



Weight: 0.05 g (typ.)

Note 1: Mounted on an FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm²)

Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Unit: mm

Electrical Characteristics (Ta = 25°C)

| Characteristics | | Symbol | Test Condition | Min | Тур. | Max | Unit |
|--------------------------------------|--------------|-----------------------|----------------------------------------------------------------------------------------------------------------------|-----|------|------|------|
| Collector cut-off current | | I _{CBO} | V _{CB} = 100 V, I _E = 0 | _ | _ | 100 | nA |
| Emitter cut-off current | | I _{EBO} | V _{EB} = 7 V, I _C = 0 | _ | _ | 100 | nA |
| Collector-emitter breakdown voltage | | V (BR) CEO | I _C = 10 mA, I _B = 0 | 50 | _ | _ | V |
| DC current gain | | h _{FE} (1) | V _{CE} = 2 V, I _C = 0.1 A | 400 | _ | 1000 | |
| | | h _{FE} (2) | V _{CE} = 2 V, I _C = 0.3 A | 200 | _ | _ | |
| Collector-emitter saturation voltage | | V _{CE (sat)} | I _C = 300 mA, I _B = 6 mA | _ | _ | 0.17 | V |
| Base-emitter saturation voltage | | V _{BE (sat)} | I _C = 300 mA, I _B = 6 mA | _ | _ | 1.10 | V |
| Collector output capacitance | | C _{ob} | V _{CB} = 10 V, I _E = 0, f = 1 MHz | _ | 5 | _ | pF |
| Switching time | Rise time | t _r | - See Figure 1. V _{cc} ≈ 30 V, R _L = 100 Ω I _{B1} = 10 mA,I _{B2} = 10 mA | _ | 35 | | ns |
| | Storage time | t _{stg} | | _ | 680 | _ | |
| | Fall time | t _f | | _ | 85 | _ | |



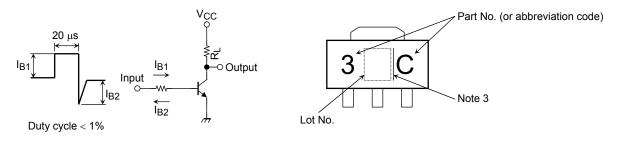
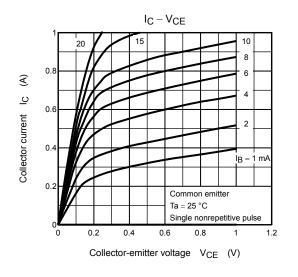


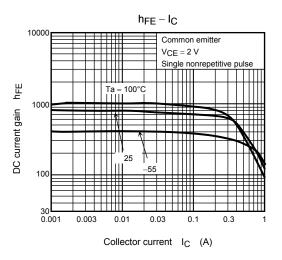
Figure 1 Switching Time Test Circuit & Timing Chart

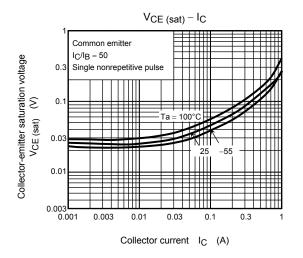
Note 3: A line to the right of a Lot No. identifies the indication of product Labels. Without a line: [[Pb]]/INCLUDES > MCV With a line: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

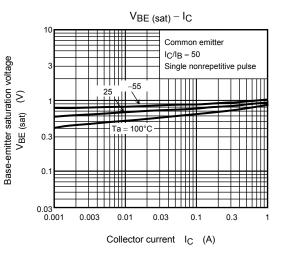
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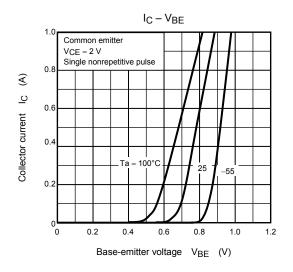
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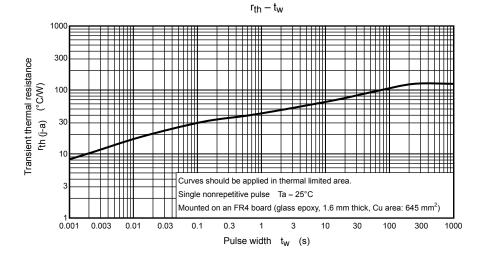


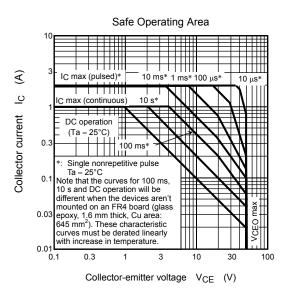












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