

Telecommunication

The TLP627-2 offers two isolated channels in eight lead plastic DIP, while the TLP627-4 provide four isolated channels per package.

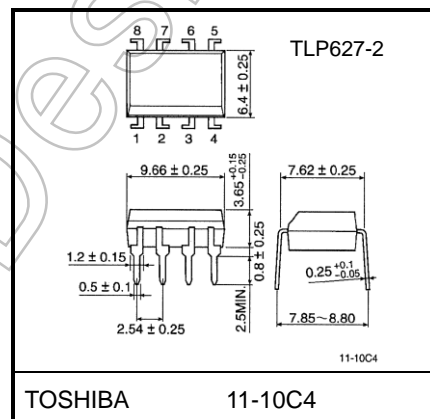
- Collector-Emitter Voltage : 300 V (min)
- Current Transfer Ratio : 1000 % (min)
- UL-recognized : UL 1577, File No.E67349
- cUL-recognized : CSA Component Acceptance Service
No.5A File No.E67349
- VDE-approved : EN 60747-5-5 (Note 1)

Note 1: When a VDE approved type is needed, please designate the **Option(D4)**.

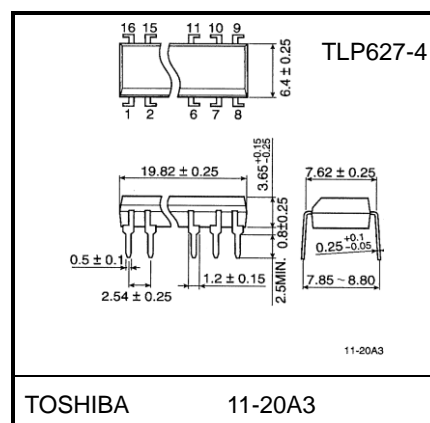
The drawing shows three views of the TLP627 optocoupler:

- Top View:** A square package with pins 1, 2, 3, and 4. Dimensions include a width of 6.4 ± 0.25 and a pin pitch of 4.58 ± 0.25 .
- Side View:** Shows the package height and pin dimensions. Key dimensions include a total height of $3.65^{+0.15}_{-0.05}$, a pin height of 0.8 ± 0.25 , and a mounting tab width of 1.2 ± 0.15 .
- Front View:** Shows the package with mounting tabs. Dimensions include a mounting tab width of 0.5 ± 0.1 , a mounting hole diameter of 2.54 ± 0.25 , a minimum standoff of 2.5 MIN. , a mounting hole offset of 7.62 ± 0.25 , a mounting hole diameter of $0.25^{+0.1}_{-0.05}$, and a mounting hole position range of $7.85 \sim 8.80$.

Weight: 0.26 g (typ.)



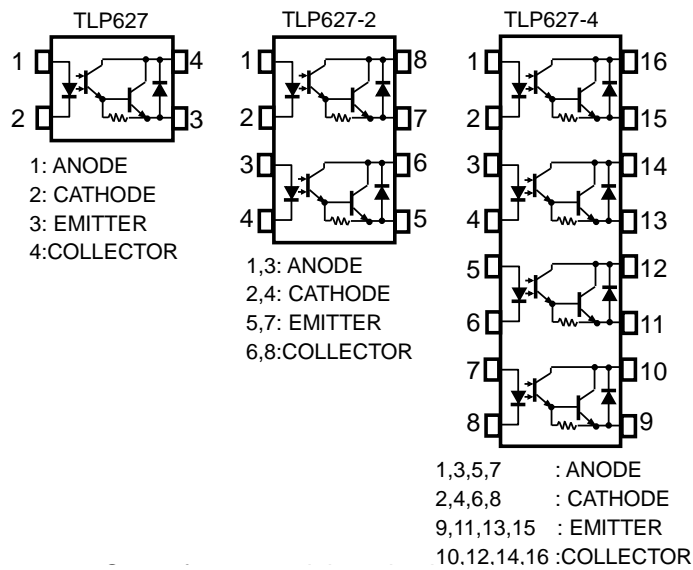
Weight: 0.54 g (typ.)



Weight: 1.1 g (typ.)

Start of commercial production
1984-08

Pin Configuration (top view)



Absolute Maximum Ratings (Ta=25°C)

Characteristics		Symbol	Rating		Unit
			TLP627	TLP627-2 TLP627-4	
LED	Forward Current	I _F	60	50	mA
	Forward Current Derating	ΔI _F /°C	-0.7(Ta≥39°C)	-0.5(Ta≥25°C)	mA /°C
	Pulse Forward Current	I _{FP}	1 (100μs pulse, 100pps)		A
	Reverse Voltage	V _R	5		V
	Diode Power Dissipation (1 Circuit)	P _D	100	70	mW
	Diode Power Dissipation Derating (1 Circuit)	ΔP _D /°C	-1.2 (Ta≥39°C)	-0.7 (Ta≥25°C)	mW /°C
Detector	Collector-Emitter Voltage	V _{CEO}	300		V
	Emitter -Collector Voltage	V _{ECO}	0.3		V
	Collector Current	I _C	150		mA
	Collector Power Dissipation (1 Circuit)	P _C	150(300(Note 1))	100	mW
	Collector Power Dissipation Derating (Ta≥25°C, 1 Circuit)	ΔP _C /°C	-1.5(-3.5(Note 1))	-1.0	mW /°C
Operating Temperature Range		T _{opr}	-55 to 100		°C
Storage Temperature Range		T _{stg}	-55 to 125		°C
Lead Soldering Temperature		T _{sol}	260(10 s)		°C
Total Package Power Dissipation (1 Circuit)		P _T	250(320(Note 1))	150	mW
Total Package Power Dissipation Derating (Ta≥25°C, 1 Circuit)		ΔP _T /°C	-2.5(-3.2(Note 1))	-1.5	mW /°C
Isolation Voltage (AC, 60 s, R.H. ≤ 60 %) (Note 2)		BV _S	5000		V _{rms}

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: I_F=20 mA Max

Note 2: Device considered a two-terminal device : LED side pins Shorted together and DETECTOR side pins shorted together.

Recommended Operating Conditions

Characteristics	Symbol	Min	Typ.	Max	Unit
Supply Voltage	V _{CC}	—	—	200	V
Forward Current	I _F	—	16	25	mA
Collector Current	I _C	—	—	120	mA
Operating Temperature	T _{opr}	-25	—	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Electrical Characteristics (Ta=25°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
LED	Forward Voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
	Reverse Current	I _R	V _R = 5 V	—	—	10	μA
	Capacitance	C _T	V = 0 V, f = 1MHz	—	30	—	pF
Detector	Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	I _C = 0.1mA	300	—	—	V
	Emitter-Collector Breakdown Voltage	V _{(BR)ECO}	I _E = 0.1mA	0.3	—	—	V
	Collector Dark Current	I _{CEO}	V _{CE} = 200V	—	10	200	nA
			V _{CE} = 200V, Ta = 85 °C	—	—	20	μA
	Capacitance Collector to Emitter	C _{CE}	V = 0 V, f = 1MHz	—	10	—	pF

Coupled Electrical Characteristics (Ta=25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Current Transfer Ratio	I_C/I_F	$I_F = 1 \text{ mA}, V_{CE} = 1 \text{ V}$	1000	4000	—	%
Saturated CTR	$I_C/I_F(\text{sat})$	$I_F = 10 \text{ mA}, V_{CE} = 1 \text{ V}$	500	—	—	%
Collector-Emitter Saturation Voltage	$V_{CE}(\text{sat})$	$I_C = 10 \text{ mA}, I_F = 1 \text{ mA}$	—	—	1.0	V
		$I_C = 100 \text{ mA}, I_F = 10 \text{ mA}$	0.3	—	1.2	

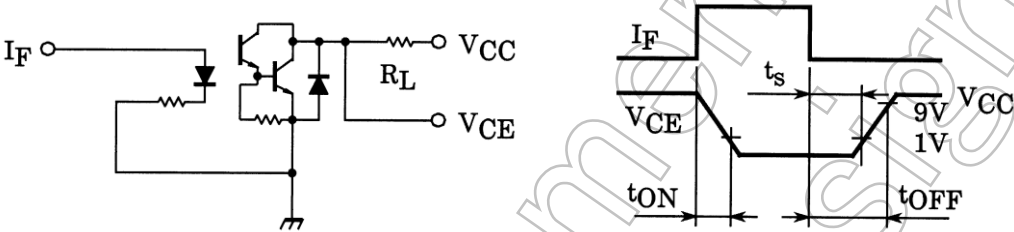
Isolation Electrical Characteristics (Ta=25°C)

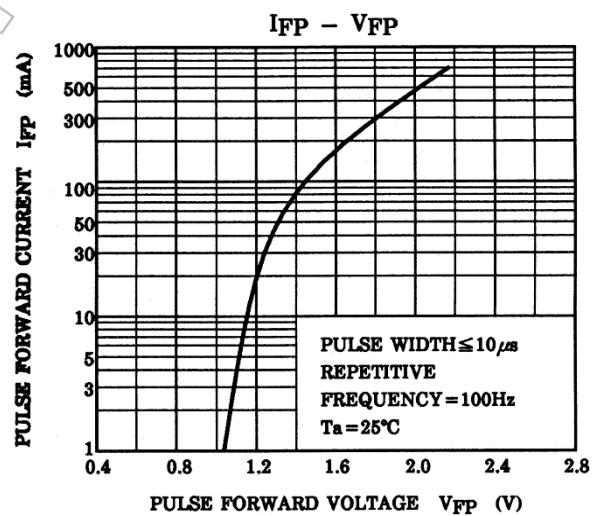
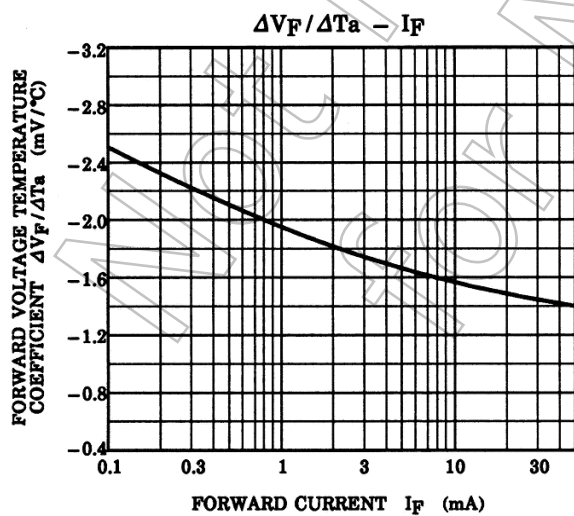
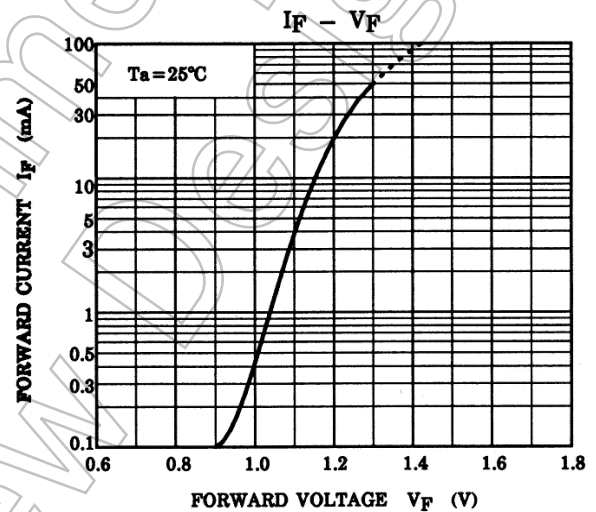
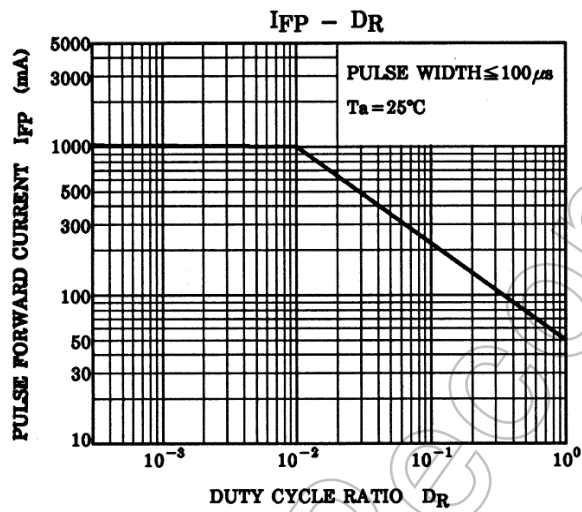
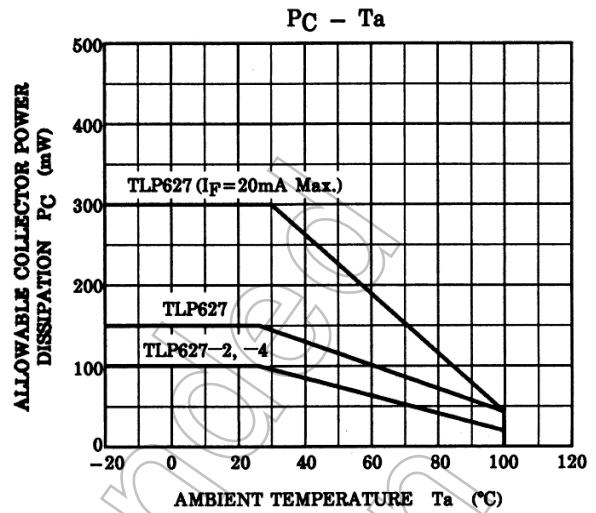
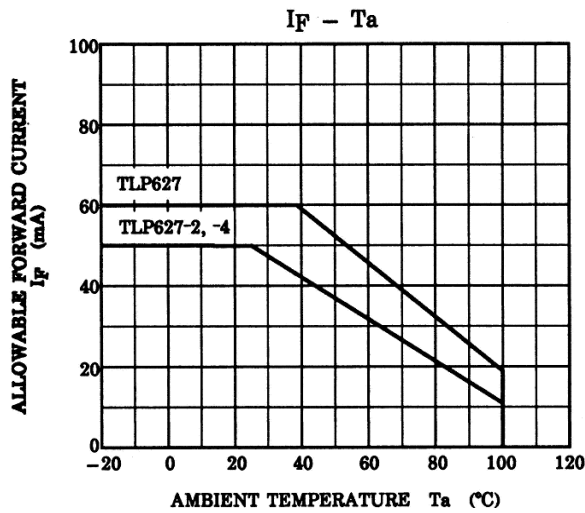
Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Capacitance Input to Output	C_S	$V_S = 0 \text{ V}, f = 1 \text{ MHz}$	—	0.8	—	pF
Isolation Resistance	R_S	$V_S = 500 \text{ V}, \text{R.H.} \leq 60\%$	5×10^{10}	10^{14}	—	Ω
Isolation Voltage	BV_S	AC, 60 s	5000	—	—	V rms

Switching Characteristics (Ta=25°C)

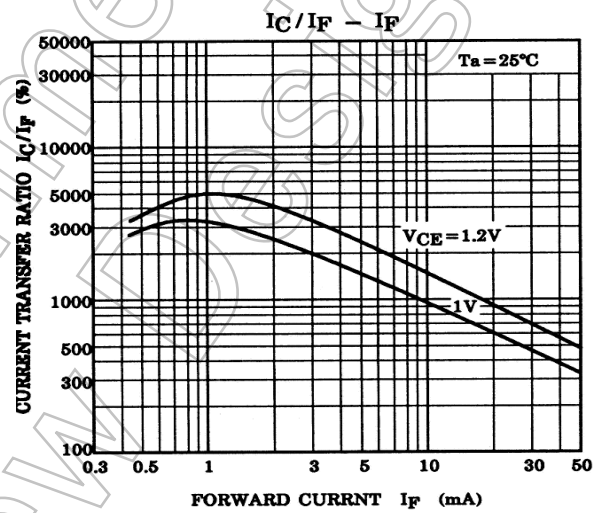
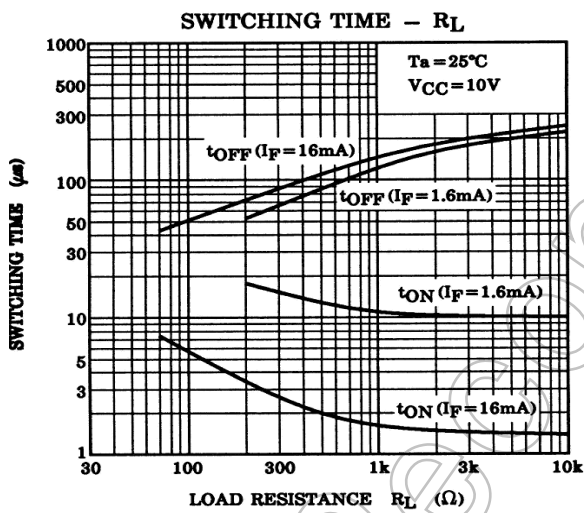
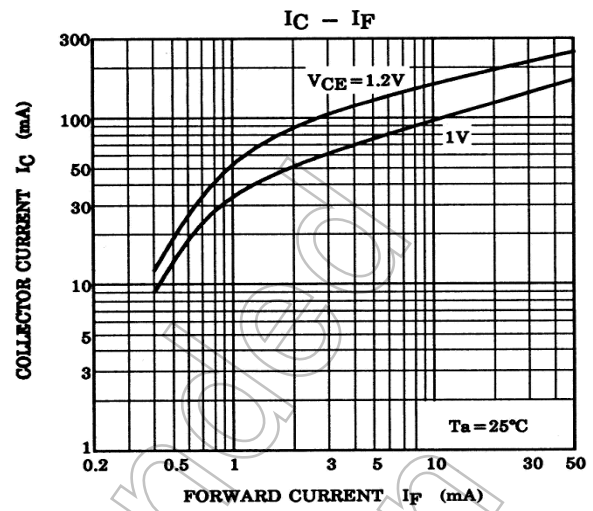
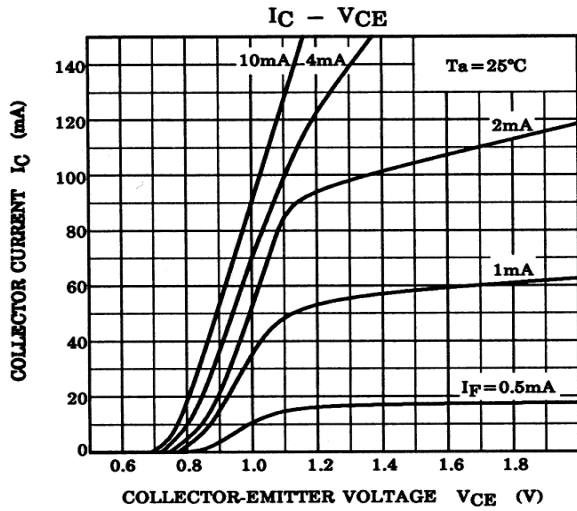
Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Rise Time	t_r	$V_{CC} = 10\text{ V}$ $I_C = 10\text{ mA}$ $R_L = 100\ \Omega$	—	40	—	μs
Fall Time	t_f		—	15	—	
Turn-on Time	t_{on}		—	50	—	
Turn-off Time	t_{off}		—	15	—	
Turn-on Time	t_{ON}	$R_L = 180\ \Omega$ (Fig.1) $V_{CC} = 10\text{ V}, I_F = 16\text{ mA}$	—	5	—	
Storage Time	t_s		—	40	—	
Turn-off Time	t_{OFF}		—	80	—	

Fig.1 Switching Time Test Circuit

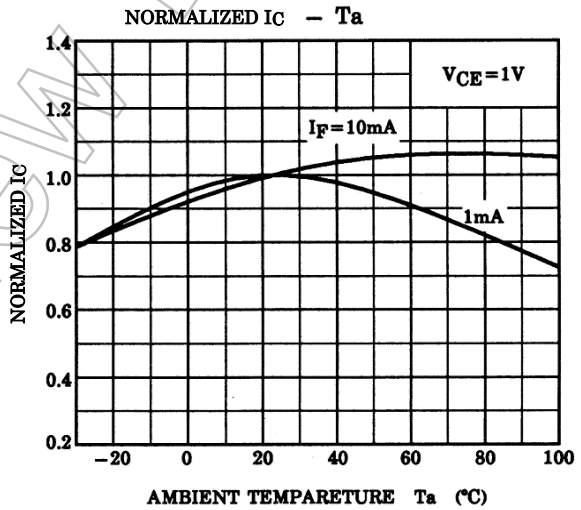
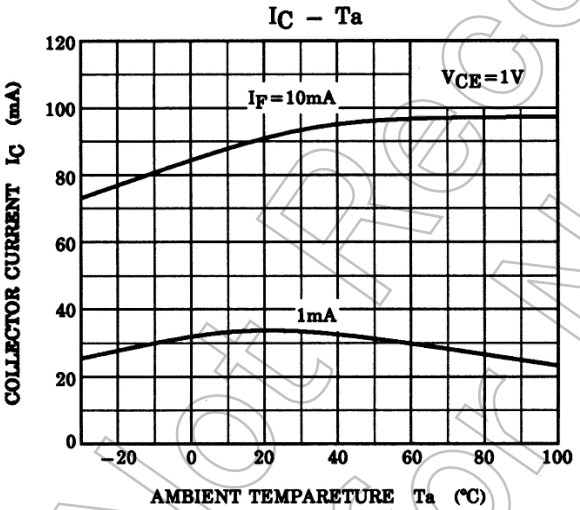
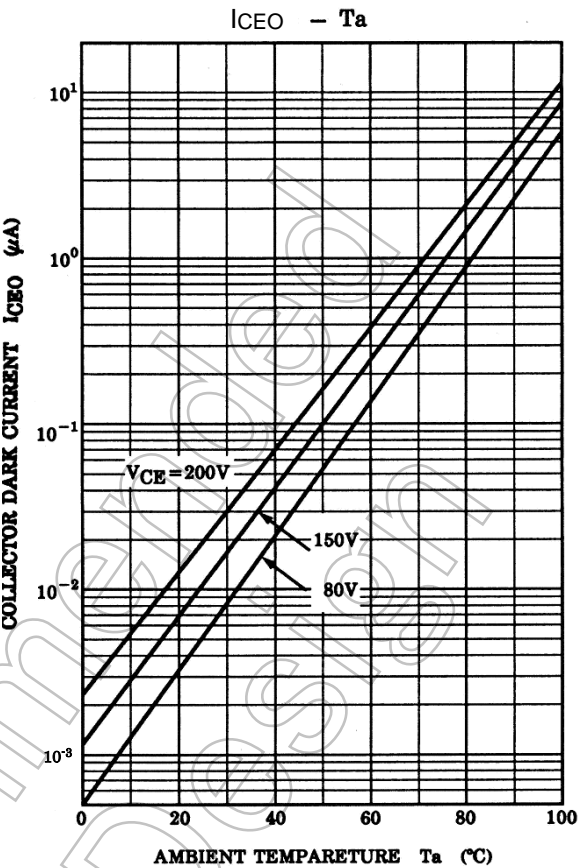
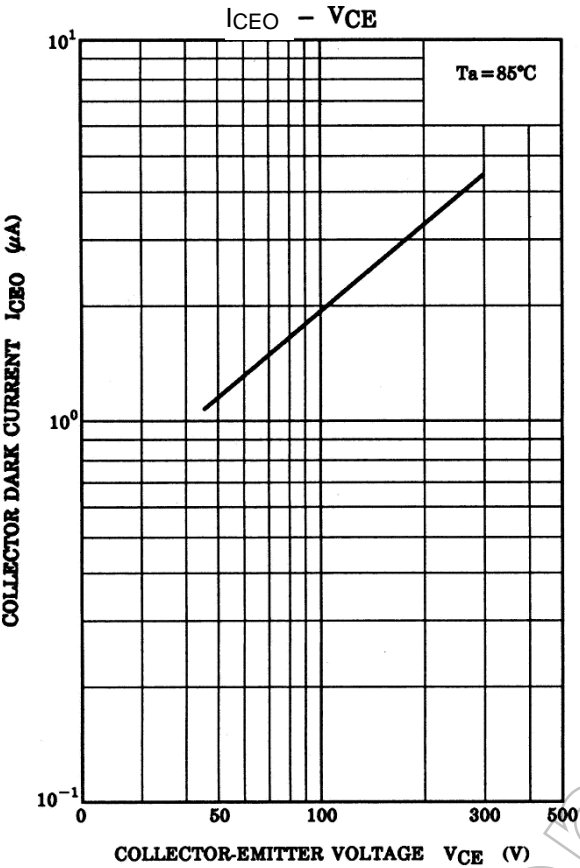




NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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