Unit: mm

TOSHIBA Photocoupler PHOTORELAY

TLP3220

Memory Tester Logic Tester **Measurement Instrument**

TOSHIBA

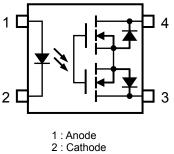
The TOSHIBA TLP3220 is a super small-outline photorelay, suitable for surface-mount assembly. The TLP3220 consists of an infrared emitting diode optically coupled to a photo-MOS FET and housed in a 4-pin package.

Features

- 4 pin SSOP (SSOP4) •
- 1-Form-A •
- Peak off-state voltage •
- Trigger LED current •
- On-state current •
- On-state resistance •
- Output capacitance •
- Isolation voltage
- UL-recognized ٠

- : 1.8 mm high, 1.27 mm pitch
- : 100 V (min)
- : 5 mA (max)
- : 80 mA (max)
- : 14 Ω (max), 8 Ω (typ.)
- : 8 pF (max), 6 pF (typ.)
- : 1500 Vrms (min)
- : UL 1577, File No.E67349

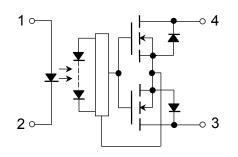
Pin configuration (top view)



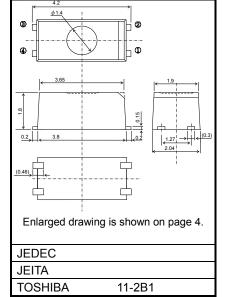
- 3 : Drain
- 4 : Drain

Schematic

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Weight: 0.03 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol	Rating	Unit
	Forward current	lF	50	mA
	Forward current derating (Ta≥25°C)	∆IF/°C	-0.5	mA/°C
Ω	Reverse voltage	VR	5	V
LED	Diode power dissipation	PD	50	mW
	Diode power dissipation derating (Ta >25°C)	∆P _D /°C	-0.5	mW/°C
	Junction temperature	Tj	125	°C
ctor	Off-State output terminal voltage	Voff	100	V
	On-State current	ION	80	mA
	On-State current derating (Ta≥25°C)	∆lon/°C	-0.8	mA/°C
Detector	Output power dissipation	Po	96	mW
	Output power dissipation derating (Ta \ge 25°C)	ΔP _o /°C	-0.96	mW / °C
	Junction temperature	Tj	125	°C
Stora	age temperature range	Tstg	-40 to 125	°C
Oper	ating temperature range	Topr	-20 to 85	°C
Lead	soldering temperature (10 s)	T _{sol}	260	°C
Isola	tion voltage (AC, 60 s, R.H.≤ 60 %) (Note 1)	BVs	1500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1): Device considered a two-terminal device: Pins 1 and 2 shorted together, and pins 3 and 4 shorted together.

Precautions

This device is sensitive to electrostatic discharge. When using this device, please ensure that all tools and equipment are earthed.

Recommended Operating Conditions

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	V _{DD}	—	—	80	V
Forward current	lF	10	_	30	mA
Operating temperature	T _{opr}	25	_	60	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	IF = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I _R	$V_R = 5 V$	_		10	μA
	Capacitance	Ст	V = 0 V, f = 1 MHz	_	15		pF
Detector	Off-state current IOFF	lorr	Voff = 80 V	_	_	200	pА
		Voff = 100 V			1	μA	
	Capacitance	COFF	V = 0 V, f = 100 MHz, t < 1 s		6	8	pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I _{FT}	I _{ON} = 80 mA	_	1	5	mA
Return LED current	IFC	$I_{OFF} = 1 \ \mu A$	0.2			mA
On-state resistance	R _{ON}	I_{ON} = 80 mA, I_F = 10 mA, t = 10 ms	_	8	14	Ω

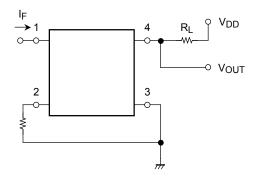
Isolation Characteristics (Ta = 25°C)

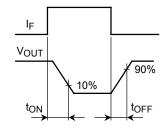
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	Cs	Vs = 0 V, f = 1 MHz	—	0.6	_	pF
Isolation resistance	Rs	V _S = 500 V, R.H.≤ 60 %	$5 imes 10^{10}$	10 ¹⁴	_	Ω
Isolation voltage	BVs	AC, 60 s	1500	—	_	Vrms

Switching Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Turn-on time	ton	R _L = 200 Ω (Note 2)	—	100	300	0
Turn-off time	tOFF	$V_{DD} = 20 V, I_F = 5 mA$	—	100	300	μS

(Note 2): switching time test circuit

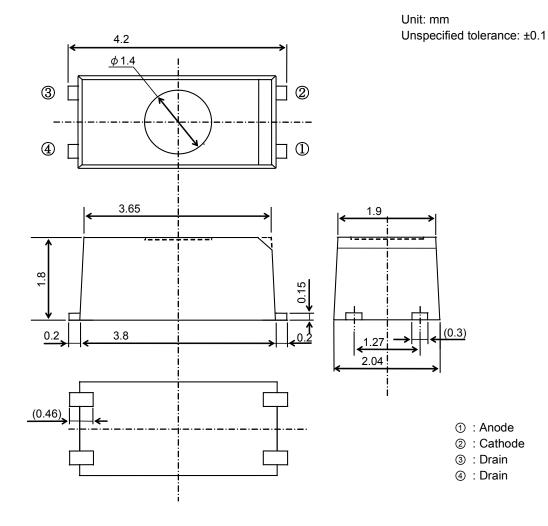


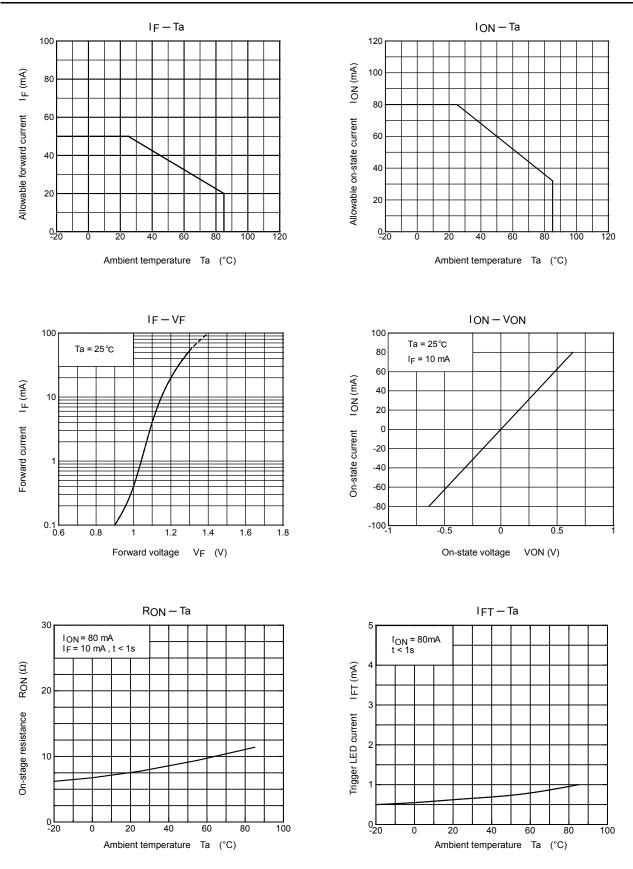


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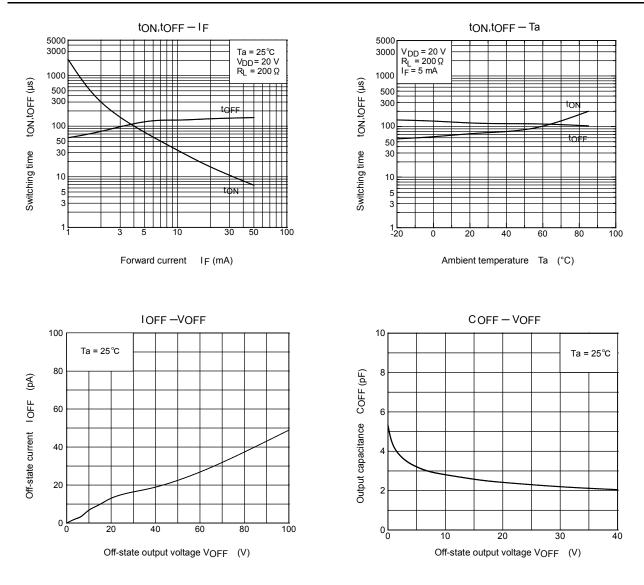
TLP3220

Package Dimensions





NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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