TLP3021(S),TLP3022(S),TLP3023(S)

OFFICE MACHINE HOUSEHOLD USE EQUIPMENT TRIAC DRIVER **SOLID STATE RELAY**

The TOSHIBA TLP3021 (S), TLP3022 (S) and TLP3023 (S) consist of photo-triac optically coupled to an infrared emitting diode in a six lead plastic DIP.

Peak Off-State Voltage : 400 V (min)

Trigger LED Current : 15 mA (max) (TLP3021(S))

10 mA (max) (TLP3022(S)) 5 mA (max) (TLP3023(S))

On-State Current : 100 mA (max) Isolation Voltage : 5000Vrms(Min)

: UL 1577, File No.E67349 UL-recognized

: CSA Component Acceptance Service cUL-recognized

No.5A File No.E67349

: EN 60747-5-5 ,EN 62368-1 (Note 1) VDE-approved

Unit: mm 7.62±0.25 JEDEC JEÍTA TOSHIBA 11-7A9S

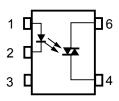
Weight: 0.39g (typ.)

Note 1: When a VDE approved type is needed, please designate the Option (D4).

Construction Mechanical Rating

	7.62 mm pitch	10.16 mm pitch
	Standard Type	TLPxxxxF Type
Creepage Distance	7.0 mm (Min)	8.0 mm (Min)
Clearance	7.0 mm (Min)	8.0 mm (Min)
Insulation Thickness	0.5 mm (Min)	0.5 mm (Min)

Pin Configuration (top view)



- 1: Anode
- 2: Csthode
- 3: N.C.
- 4:Terminal 1
- 6:Terminal 2

Start of commercial production 1986-11

Absolute Maximum Ratings (Ta=25°C)

	CHARACTERISTIC	SYMBOL	RATING	UNIT	
	Forward Current	lF	50	mA	
	Forward Current Derating (Ta≥53°C)	ΔIF /°C	-0.7	mA /°C	
	Peak Forward Current (100μs pulse, 100pps)	IFP	(M)	Α	
LED	Reverse Voltage	V_{R}	5	V	
	Power Dissipation		P _D	100	mW
	Power Dissipation Derating (Ta≥25°C)	ΔP _D /°C	-1.0	mW/°C	
	Junction Temperature	Ť.	(125)	°C	
	Off-State Output Terminal Voltage	V _{DRM}	400	V	
		Ta=25°C		100	
	On-State RMS Current	Ta=70°C	T(RMS)	50	mA
ECTOR	On-State Current Derating (Ta≥25°C)	ΔI _T /°C	-1.1	mA /°C	
LEC.	Peak On-State Current (100μs pulse, 120pps)	\\TP	2 (A	
DETI	Peak Nonrepetitive Surge Current (Pw=10ms)	ITSM	1,2		
	Power Dissipation	> P _D	300	mW	
	Power Dissipation Derating (Ta≥25°C)	ΔP _D /°C	-4.0	mW/°C	
	Junction Temperature	Tj (115	°C	
Stor	rage Temperature Range		Tstg	-55 to 150	°C
Ope	erating Temperature Range	Topr	-40 to 100	°C	
Lea	d Soldering Temperature (10 s)	T _{sol}	260	°C	
Tota	al Package Power Dissipation	PT	330	mW	
Tota	al Package Power Dissipation Derating (Ta≥25°C)	ΔP _T /°C	-4.4	mW /°C	
Isola	ation Voltage (AC,60 s. , R.H.≤60 %)	BVs	5000	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 2) Device considered a two terminal device: Pins1,2 and 3 shorted together and pin4 and pin6 shorted together.

Recommended Operating Conditions

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply Voltage	VAC	_	_	120	V_{ac}
Forward Current	lF*	15	20	25	mA
Peak On-State Current	ITP	_	_	1	Α
Operating Temperature	Topr	-25	-	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

^{*}In The case of TLP3022

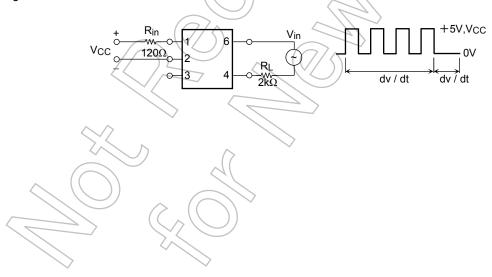
Individual Electrical Characteristics (Ta=25°C)

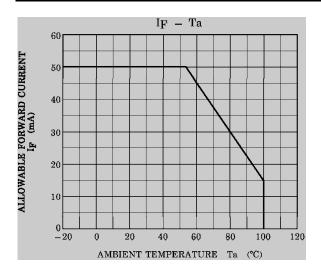
	CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
	Forward Voltage	VF	I _F = 10 mA	1.0	1.15	1.3	V
ED	Reverse Current	I _R	V _R = 5 V	_	_	10	μΑ
	Capacitance	Ст	V = 0 V, f = 1 MHz	-<	10	-	pF
2	Peak Off-State Current	IDRM	V _{DRM} = 400 V		10	1000	nA
0 +	Peak On-State Voltage	V_{TM}	I _{TM} = 100 mA	- \	1.7	3.0	V
O	Holding Current	lΗ	_	(7)	0.6	_	mA
_ F	Critical Rate of Rise of Off-State Voltage	dv/dt	Vin = 120 Vrms , Ta = 85 °C (Fig.1)	200	500		V/μs
D E	Critical Rate of Rise of Commutating Voltage	dv/dt(c)	Vin = 30 Vrms , IT = 15 mA (Fig.1)	FR	0.2		V/μs

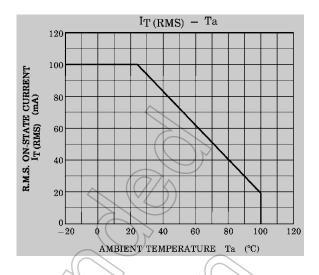
Coupled Electrical Characteristics (Ta=25°C)

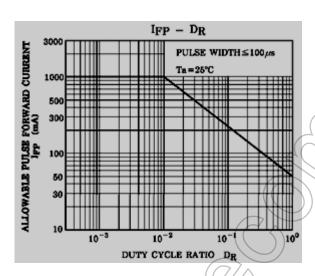
CHARACTER	ISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
	TLP3021(S)	I _{FT}		_ `	7	15//	
Trigger LED Current	TLP3022(S)		V _T = 3 V	5 10		10	mA
	TLP3023(S)				\mathcal{D}	5	
Capacitance (Input to C	Output)	Cs	VS = 0 V , f = 1 MHz		0.8		pF
Isolation Resistance		Rs	VS = 500 V, R.H.≤ 60 %	5×10 ¹⁰	10 ¹⁴		Ω
Isolation Voltage		BVs	AC, 60 s	5000	_	_	Vrms

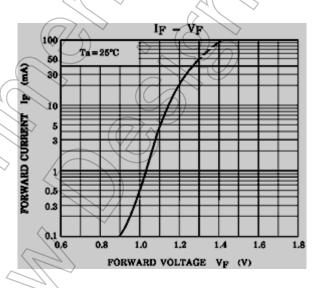
Fig. 1 dv / dt test circuit

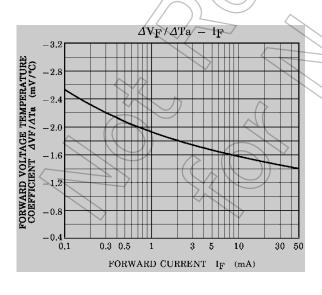


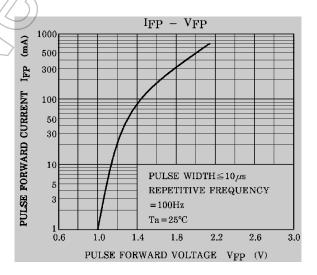






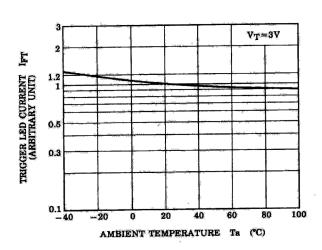




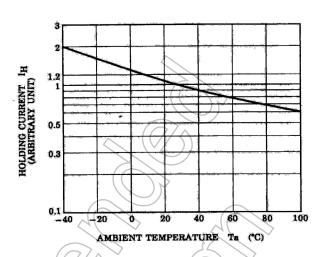


NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

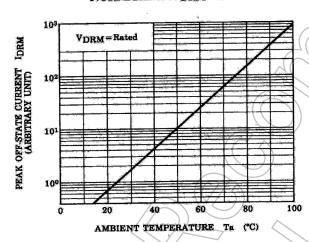
NORMALIZED IFT - Ta



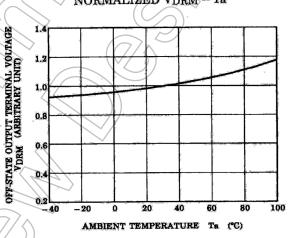
NORMALIZED IH - Ta



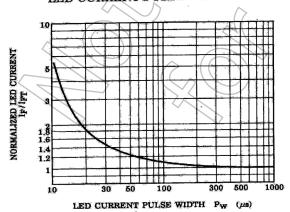
NORMALIZED IDRM - Ta



NORMALIZED VDRM - Ta



NORMALIZED LED CURRENT - LED CURRENT PULSE WIDTH



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