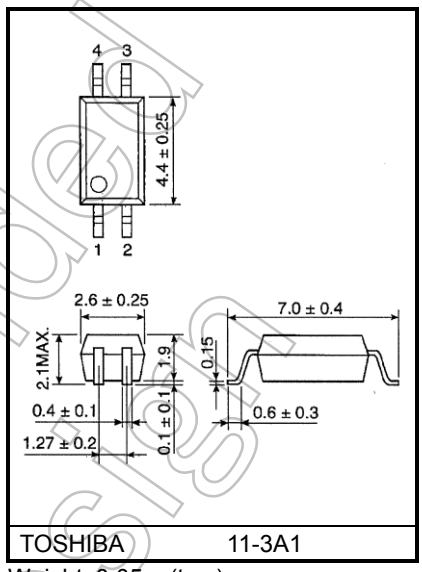
TLP285

Power Supplies
Programmable Controllers
Hybrid ICs

Unit: mm



TOSHIBA 11-3A1

Weight: 0.05 g (typ.)

The Toshiba TLP285 consists of photo transistor, optically coupled to an infrared emitting diode. TLP285 is housed in the SOP4 package, very small and thin coupler.
Since TLP285 is guaranteed wide operating temperature (Ta = -55 to 110 ˚C) and high isolation voltage (3750 Vrms), it's suitable for high-density surface mounting applications such as small switching power supplies and programmable controllers.

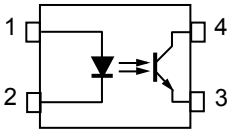
- Collector-Emitter Voltage : 80 V (min)
- Current Transfer Ratio : 50% (min)
  Rank GB : 100% (min)
- Isolation Voltage : 3750 Vrms (min)
- Guaranteed performance over -55 to 110 ˚C
- UL-recognized : UL 1577, File No.E67349
- cUL-recognized : CSA Component Acceptance Service No.5A
  File No.E67349
- VDE-approved : EN 60747-5-5 (Note 1)

Note 1: When a VDE approved type is needed, please designate the **Option(V4)**.

Construction Mechanical Rating

Creepage Distance	5.0 mm (min)
Clearance	5.0 mm (min)
Insulation Thickness	0.4 mm (min)

Pin Configuration (top view)



1: ANODE
2: CATHODE
3: EMITTER
4: COLLECTOR

Start of commercial production
2008-01

Current Transfer Ratio

TYPE	Classification (Note1)	Current Transfer Ratio (%) (I_C/I_F) I_F = 5 mA, V_{CE} = 5 V, Ta = 25°C		Marking of Classification
		Min	Max	
TLP285	Blank	50	600	Blank,Y■, YE, G, G■, GR, B, BL, GB
	Rank Y	50	150	YE, Y■
	Rank GR	100	300	GR, G, G■
	Rank BL	200	600	BL, B
	Rank GB	100	600	GB, GR, G, G■, BL, B
	Rank YH	75	150	Y■
	Rank GRL	100	200	G
	Rank GRH	150	300	G■
	Rank BLL	200	400	B

Note1: Ex. rank GB: TLP285 (GB)

Note: Application type name for certification test, please use standard product type name, i.e.
TLP285 (GB): TLP285

Absolute Maximum Ratings (Ta = 25°C)

	CHARACTERISTIC	SYMBOL	RATING	UNIT
LED	Forward Current	$I_{F(RMS)}$	50	mA
	Forward Current Derating (Ta ≥ 75°C)	ΔI_F/°C	-1.0	mA/°C
	Pulse Forward Current (Note 1)	I_{FP}	1	A
	Reverse Voltage	V_R	5	V
	Diode power dissipation	P_D	100	mW
	Diode power dissipation derating (Ta ≥ 75°C)	ΔP_D/°C	-2.0	mW/°C
	Junction Temperature	T_j	125	°C
DETECTOR	Collector-Emitter Voltage	V_{CEO}	80	V
	Emitter-Collector Voltage	V_{ECO}	7	V
	Collector Current	I_C	50	mA
	Collector Power Dissipation	P_C	150	mW
	Collector Power Dissipation Derating (Ta ≥ 25°C)	ΔP_C/°C	-1.5	mW/°C
	Junction Temperature	T_j	125	°C
Operating Temperature Range		T_{opr}	-55 to 110	°C
Storage Temperature Range		T_{stg}	-55 to 125	°C
Lead Soldering Temperature (10 s)		T_{sol}	260	°C
Total Package Power Dissipation		P_T	200	mW
Total Package Power Dissipation Derating (Ta ≥ 25°C)		ΔP_T/°C	-2.0	mW/°C
Isolation Voltage (Note 2)		BV_S	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Pulse width ≤ 100 μs, frequency 100 Hz

Note 2: AC, 60 s, R.H.≤60 %
Device considered a two terminal device : LED side pins shorted together and DETECTOR side pins shorted together.

Electrical Characteristics (Ta = 25°C)

	CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
LED	Forward Voltage	V_F	I_F = 10 mA	1.0	1.15	1.3	V
	Reverse Current	I_R	V_R = 5 V	—	—	10	μA
	Capacitance	C_T	V = 0 V, f = 1 MHz	—	30	—	pF
DETECTOR	Collector-Emitter Breakdown Voltage	$V_{(BR)CEO}$	I_C = 0.5 mA	80	—	—	V
	Emitter-Collector Breakdown Voltage	$V_{(BR)ECO}$	I_E = 0.1 mA	7	—	—	V
	Collector Dark Current (Note 2)	I_{CEO}	V_{CE} = 48 V, Ambient Light Below (100 ℓx) (Note 1)	—	0.01 (2)	0.1 (10)	μA
			V_{CE} = 48 V, Ta = 85 °C Ambient Light Below (100 ℓx) (Note 1)	—	2 (4)	50 (50)	μA
	Capacitance (Collector to Emitter)	C_{CE}	V = 0 V, f = 1 MHz	—	10	—	pF

Note 1: Irradiation to marking side using standard light bulb.

Note 2: Because of the construction, leak current might be increased by ambient light.
Please use photocoupler with less ambient light.

Coupled Electrical Characteristics (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Current Transfer Ratio	I_C/I_F	I_F = 5 mA, V_{CE} = 5 V	50	—	600	%
		Rank GB	100	—	600	
Saturated CTR	$I_C/I_{F(sat)}$	I_F = 1 mA, V_{CE} = 0.4 V	—	60	—	%
		Rank GB	30	—	—	
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	I_C = 2.4 mA, I_F = 8 mA	—	—	0.4	V
		I_C = 0.2 mA, I_F = 1 mA	—	0.2	—	
		Rank GB	—	—	0.4	
OFF-State Collector Current	$I_{C(off)}$	V_F = 0.7 V, V_{CE} = 48 V	—	—	10	μA

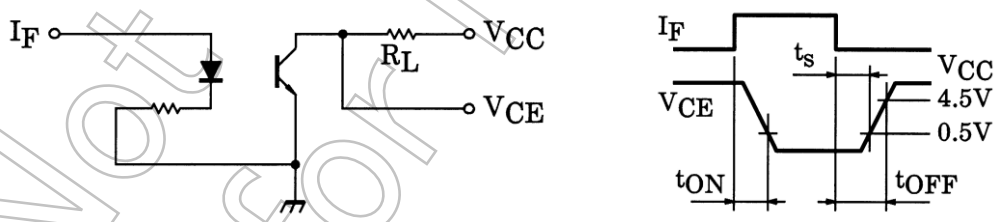
Isolation Characteristics (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Capacitance (Input to Output)	C_S	V_S = 0 V, f = 1 MHz	—	0.8	—	pF
Isolation Resistance	R_S	V_S = 500 V, R.H.≤60 %	1×10^{12}	10^{14}	—	Ω
Isolation Voltage	BV_S	AC, 60 s	3750	—	—	Vrms

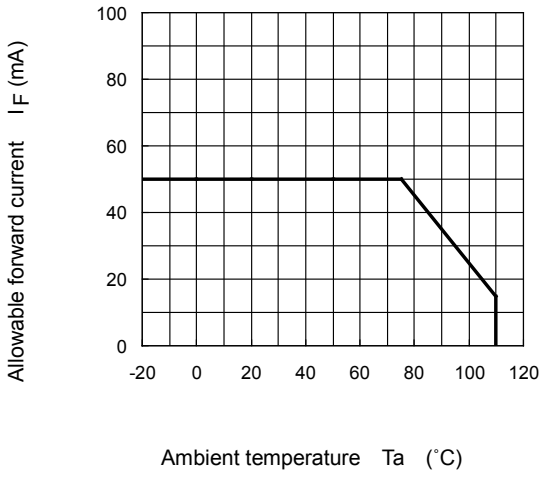
Switching Characteristics (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Rise Time	t_r	V_{CC} = 10 V, I_C = 2 mA R_L = 100 Ω	—	2	—	μs
Fall Time	t_f		—	3	—	
Turn-On Time	t_{on}		—	3	—	
Turn-Off Time	t_{off}		—	3	—	
Turn-On Time	t_{ON}	R_L = 1.9 kΩ (Fig.1) V_{CC} = 5 V, I_F = 16 mA	—	2	—	μs
Storage Time	t_s		—	25	—	
Turn-Off Time	t_{OFF}		—	40	—	

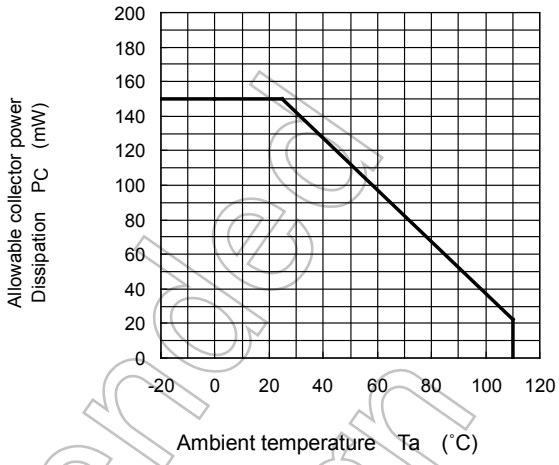
Fig.1: Switching Time Test Circuit



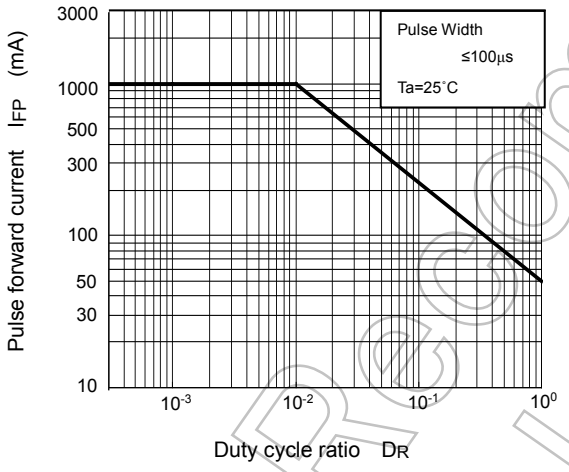
I_F - Ta



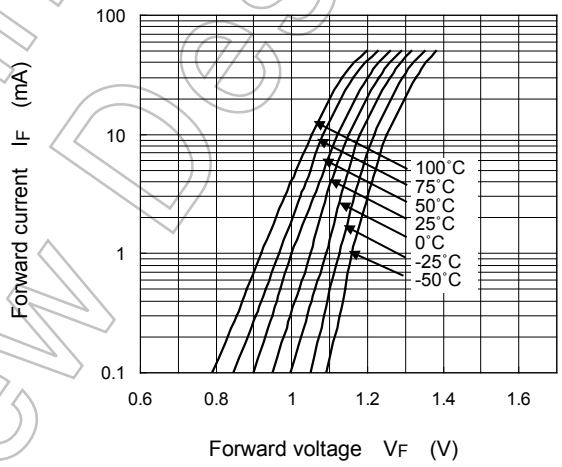
P_C - Ta



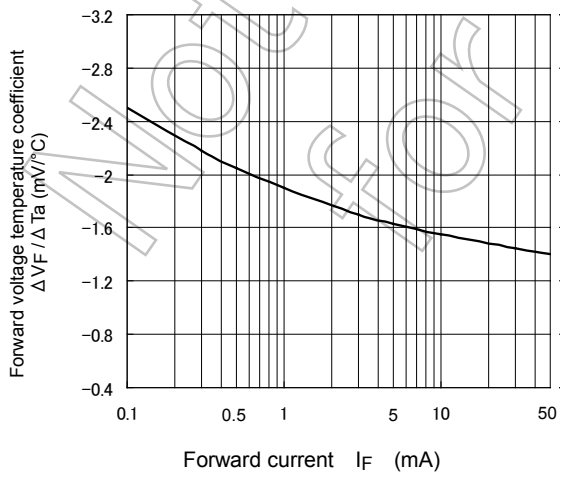
I_{FP} - D_R



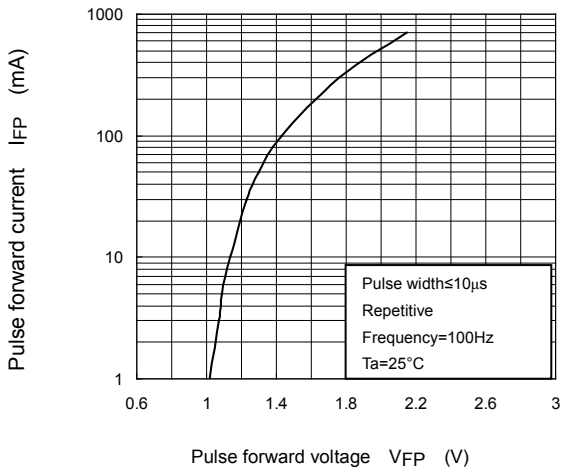
I_F - V_F



$\Delta V_F / \Delta Ta$ - I_F

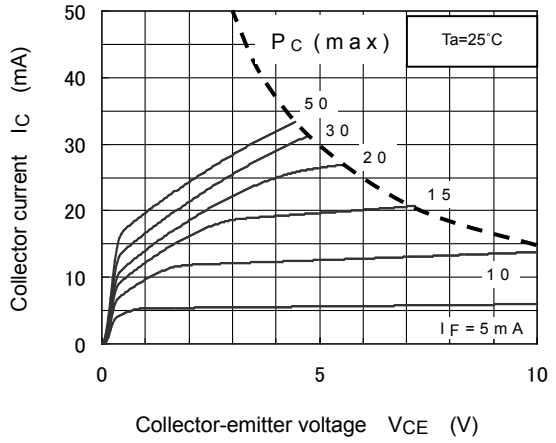


I_{FP} – V_{FP}

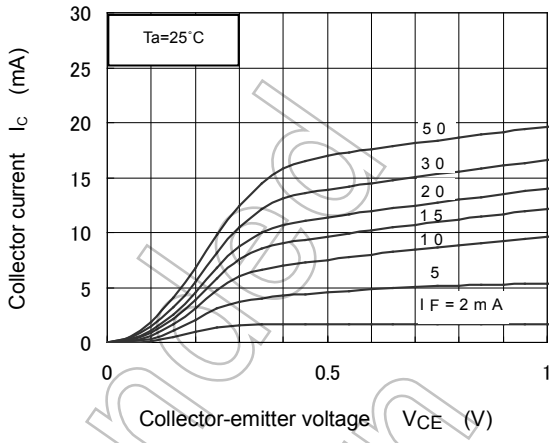


NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

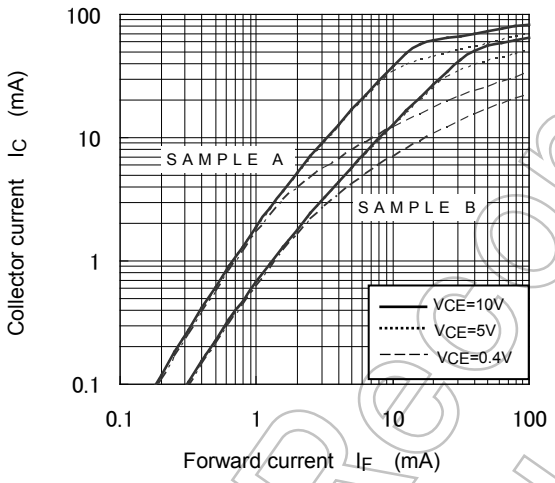
I_C-V_{CE}



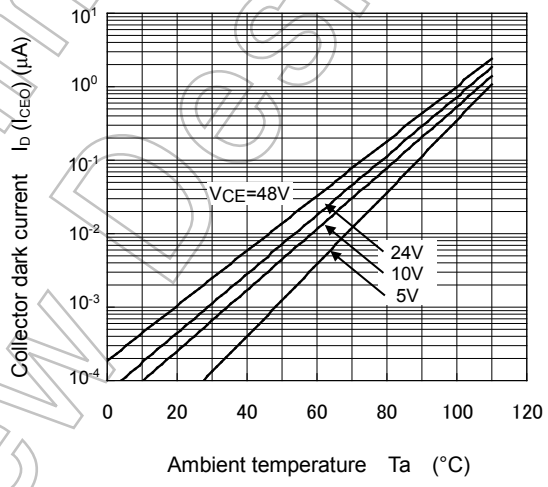
I_C-V_{CE}



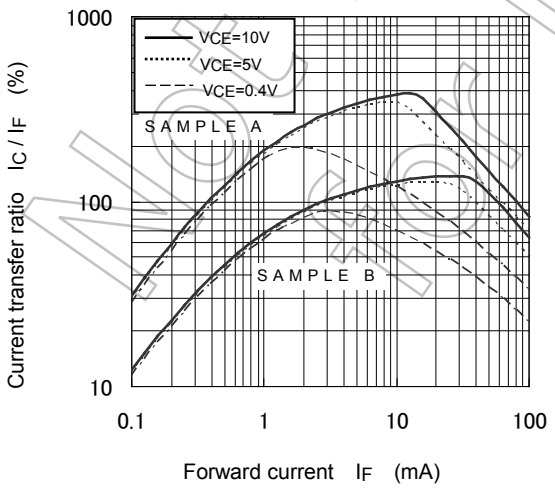
I_C-I_F



I_{CEO}-Ta

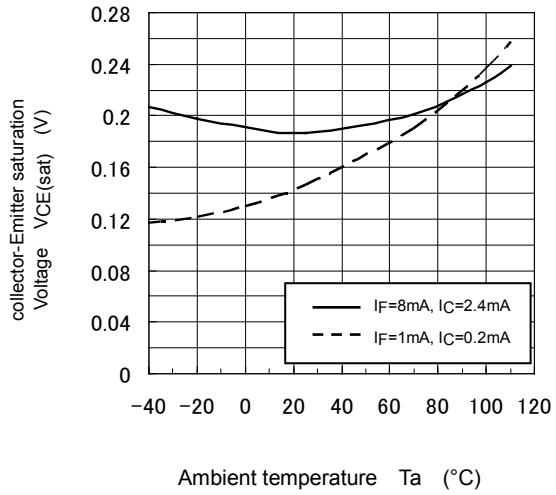


I_C / I_F - I_F

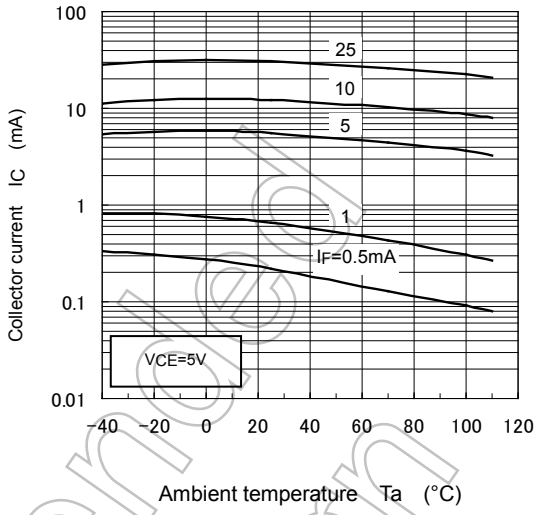


NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

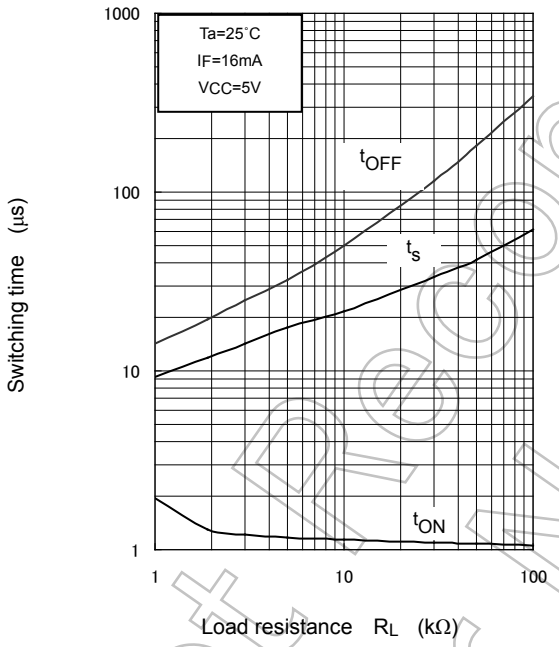
$V_{CE(sat)}$ - Ta



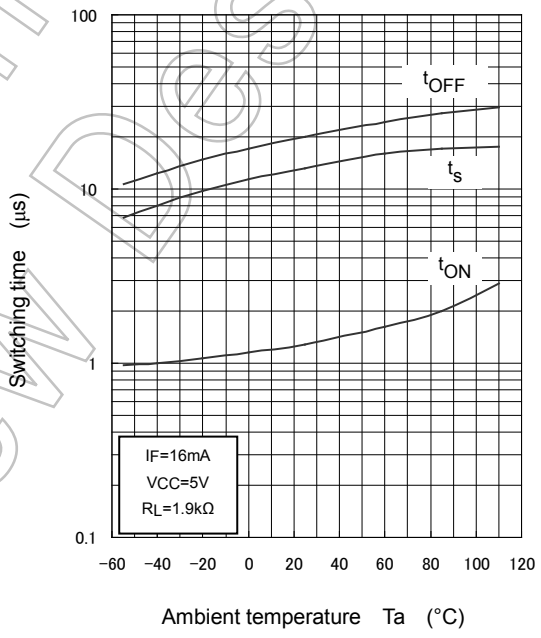
I_C - Ta



Switching time - R_L



Switching time - Ta



NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Soldering and Storage

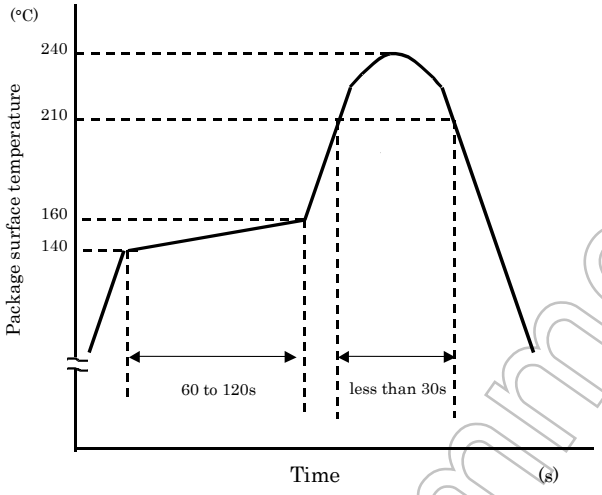
1. Soldering

Soldering

When using a soldering iron or medium infrared ray/hot air reflow, avoid a rise in device temperature as much as possible by observing the following conditions.

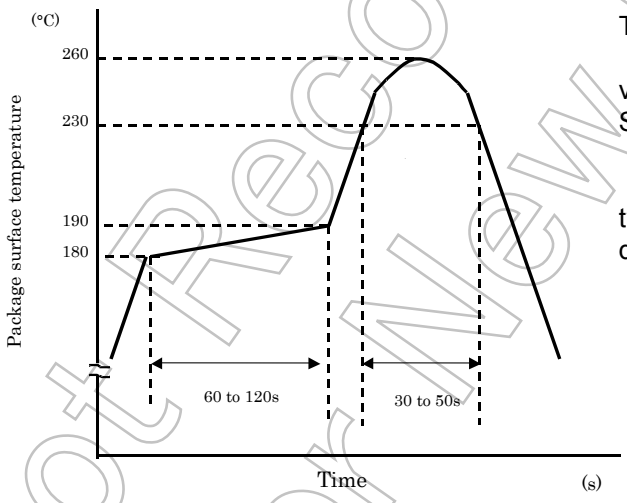
1) Using solder reflow

·Temperature profile example of lead (Pb) solder



This profile is based on the device's maximum heat resistance guaranteed value.
Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

·Temperature profile example of using lead (Pb)-free solder



This profile is based on the device's maximum heat resistance guaranteed value.
Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

Reflow soldering must be performed once or twice.
The mounting should be completed with the interval from the first to the last mountings being 2 weeks.

2) Using solder flow (for lead (Pb) solder, or lead (Pb)-free solder)

・Please preheat it at 150°C between 60 and 120 seconds.

・Complete soldering within 10 seconds below 260°C. Each pin may be heated at most once.

3) Using a soldering iron

Complete soldering within 10 seconds below 260°C, or within 3 seconds at 350°C. Each pin may be heated at most once.

2. Storage

1) Avoid storage locations where devices may be exposed to moisture or direct sunlight.
2) Follow the precautions printed on the packing label of the device for transportation and storage.
3) Keep the storage location temperature and humidity within a range of 5°C to 35°C and 45% to 75%, respectively.
4) Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
5) Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
6) When restoring devices after removal from their packing, use anti-static containers.
7) Do not allow loads to be applied directly to devices while they are in storage.
8) If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

Embossed-Tape Packing (TP) Specification for Mini-Flat Couplers

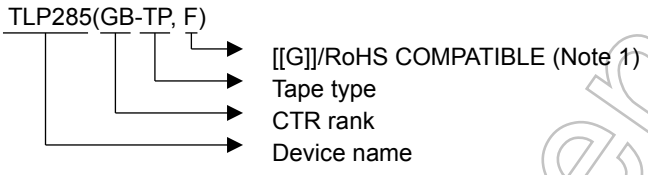
1. Applicable Package

Package	Product Type
SOP4	Mini-Flat Coupler

2. Product Naming System

Type of package used for shipment is denoted by a symbol suffix after a product number. The method of classification is as below.

(Example)



Note 1: Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.
The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

3. Tape Dimensions

3.1 Orientation of Device in Relation to Direction of Tape Movement

Device orientation in the recesses is as shown in Figure 2.

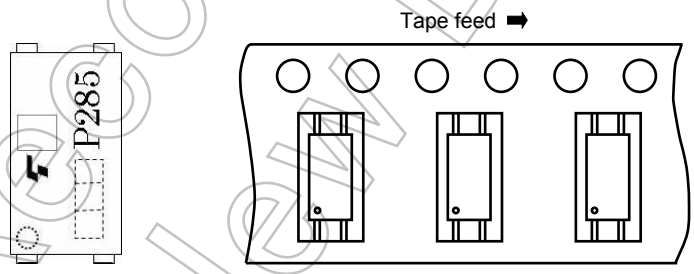


Figure 2 Device Orientation

3.2 Tape Packing Quantity: 2500 devices per reel

3.3 Empty Device Recesses Are as Shown in Table 1.

Table 1 Empty Device Recesses

	Standard	Remarks
Occurrences of 2 or more successive empty device recesses	0 device	Within any given 40-mm section of tape, not including leader and trailer
Single empty device recesses	6 device (max.) per reel	Not including leader and trailer

3.4 Start and End of Tape

The start of the tape has 50 or more empty holes. The end of tape has 50 or more empty holes and two empty turns only for a cover tape.

3.5 Tape Specification

(1) Tape material: Plastic (protection against electrostatics)

(2) Dimensions: The tape dimensions are as shown in Figure 3 and table 2.

Unit: mm

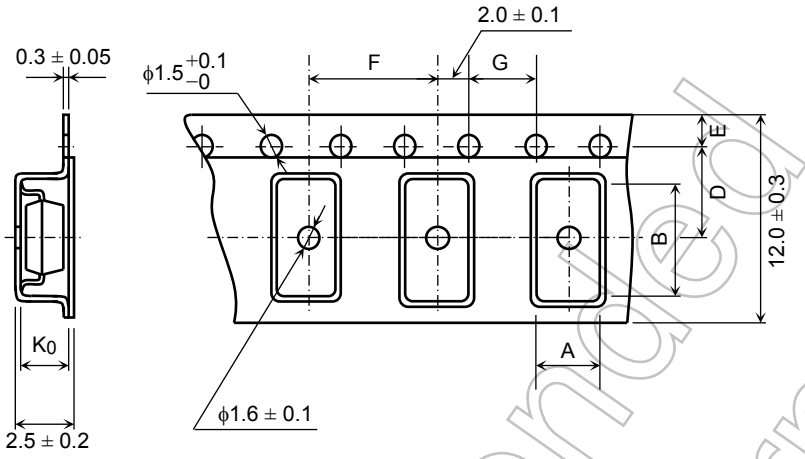


Figure 3 Tape Forms

Table 2 Tape Dimensions

Unit: mm
Unless otherwise specified: ±0.1

Symbol	Dimension	Remark
A	3.1	—
B	7.5	—
D	5.5	Center line of indented square hole and sprocket hole
E	1.75	Distance between tape edge and hole center
F	8.0	Cumulative error $^{+0.1}_{-0.3}$ (max) per 10 feed holes
G	4.0	Cumulative error $^{+0.1}_{-0.3}$ (max) per 10 feed holes
K_0	2.3	Internal space

3.6 Reel

(1) Material: Plastic

(2) Dimensions: The reel dimensions are as shown in Figure 4 and Table 3.

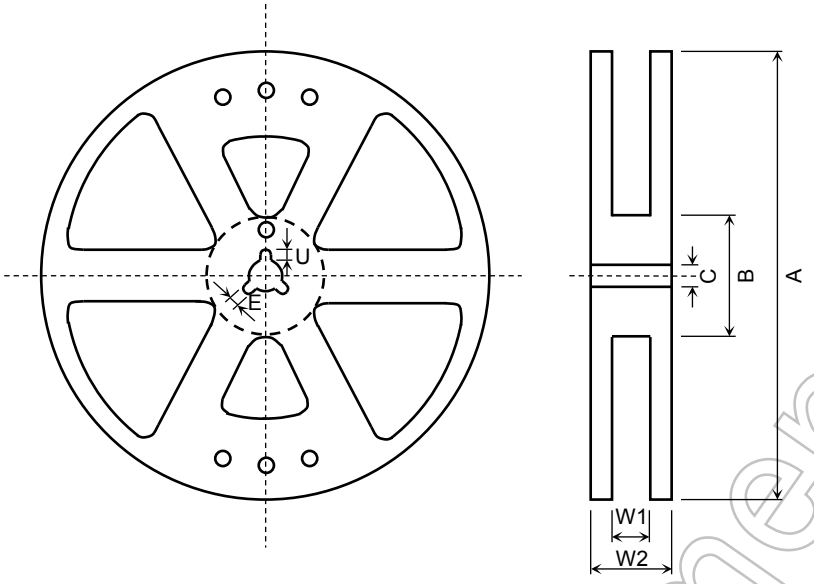


Figure 4 Reel Form

Table 3 Reel Dimensions

Unit: mm

Symbol	Dimension
A	Φ330 ±2
B	Φ80 ±1
C	Φ13 ±0.5
E	2.0 ±0.5
U	4.0 ±0.5
W1	13.5 ±0.5
W2	17.5 ±1.0

4. Packing

Packed in a shipping carton.

5. Label Indication

The carton bears a label indicating the product number, the symbol representing classification of standard, the quantity, the lot number and the Toshiba company name.

6. Ordering Method

When placing an order, please specify the product number, the CTR rank, the tape type and the quantity as shown in the following example.

(Example)

TLP285(GB-TP,F) 2500 pcs

- Quantity (must be a multiple of 2500)
- [[G]]/RoHS COMPATIBLE (Note 1)
- Tape type
- CTR rank
- Device name

Note 1 : Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

EN 60747-5-5 Option (V4) Specification

Types : TLP285 (Note 1)

Type designations for “option: (V4)”, which are tested under EN 60747 requirements.

Ex.: TLP285 (V4-GB-TP,F)
V4 : EN 60747 option
GB : CTR rank type
TP : Standard tape & reel type
F : [[G]]/RoHS COMPATIBLE (Note 2)

Note 1: Use TOSHIBA standard type number for safety standard application.
Ex.: TLP285 (V4-GB-TP,F) → TLP285

Note 2: Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.
The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

EN 60747 Isolation Characteristics

Description	Symbol	Rating	Unit
Application classification for rated mains voltage ≤ 150Vrms for rated mains voltage ≤ 300Vrms		 I-IV I-III	—
Climatic classification		55 / 110 / 21	—
Pollution degree		2	—
Maximum operating insulation voltage	V_{IORM}	707	Vpk
Input to output test voltage, Method A Vpr=1.6 × V_{IORM}, type and sample test tp=10s, partial discharge<5pC	V_{pr}	1131	Vpk
Input to output test voltage, Method B Vpr=1.875 × V_{IORM}, 100% production test tp=1s, partial discharge<5pC	V_{pr}	1325	Vpk
Highest permissible overvoltage (transient overvoltage, tpr=60s)	V_{TR}	6000	Vpk
Safety limiting values (max. permissible ratings in case of fault, also refer to thermal derating curve) current (input current I_F, Psi=0) power (output or total power dissipation) temperature	 I_{si} P_{si} T_{si}	 250 400 150	 mA mW °C
Insulation resistance V_{IO}=500V, Ta=T_{si}	R_{si}	≥ 10^9	Ω

Insulation Related Specifications

Minimum creepage distance	Cr	5.0 mm
Minimum clearance	Cl	5.0 mm
Minimum insulation thickness	ti	0.4 mm
Comparative tracking index	CTI	175

Note: If a printed circuit is incorporated, the creepage distance and clearance may be reduced below this value.
If this is not permissible, the user shall take suitable measures.

Note: This photocoupler is suitable for 'safe electrical isolation' only within the safety limit data.
Maintenance of the safety data shall be ensured by means of protective circuit.

VDE test sign:

V

Marking on packing

VDE

Marking Example

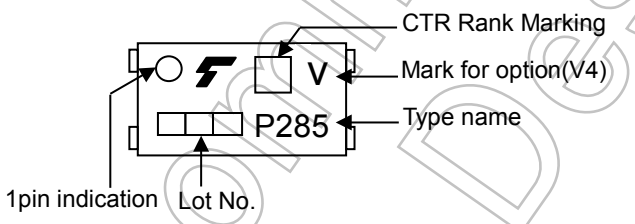


Figure 1 Partial discharge measurement procedure according to EN 60747
Destructive test for qualification and sampling tests.

Method A

(for type and sampling tests, destructive tests)

t_1, t_2	= 1 to 10 s
t_3, t_4	= 1 s
t_p(Measuring time for partial discharge)	= 10 s
t_b	= 12 s
t_{ini}	= 60 s

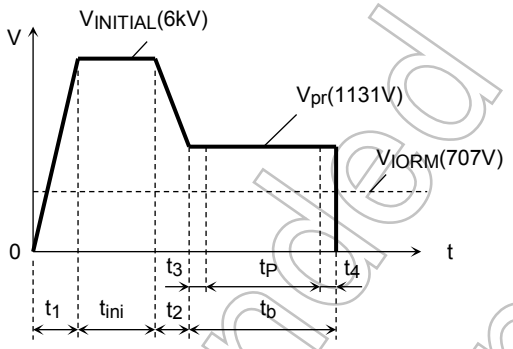


Figure 2 Partial discharge measurement procedure according to EN 60747
Non-destructive test for100% inspection.

Method B

(for sample test, non-destructive test)

t_3, t_4	= 0.1 s
t_p(Measuring time for partial discharge)	= 1 s
t_b	= 1.2 s

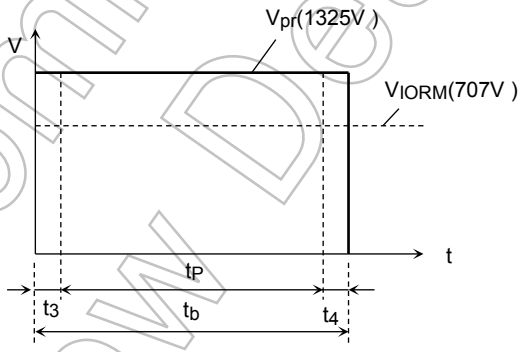
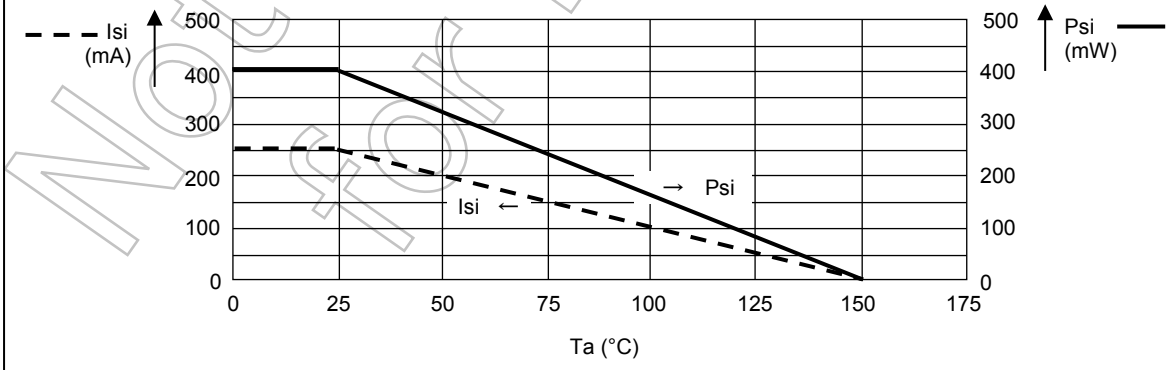


Figure 3 Dependency of maximum safety ratings on ambient temperature



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