TOSHIBA Photocoupler IRED & Photo-Transistor

# **TLP126**

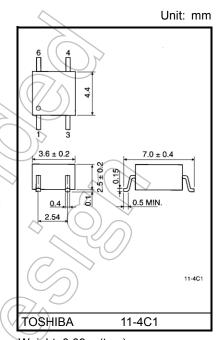
Programmable Controllers AC / DC-Input Module Telecommunication

The TOSHIBA mini flat coupler TLP126 is a small outline coupler, suitable for a surface mount assembly.

TLP126 consists of a photo transistor, optically coupled to two infrared emitting diodes connected inverse parallel, and provides high CTR at low AC input current.

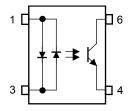
- Collector-emitter voltage: 80 V (min)
- Current transfer ratio: 100% (min)
- Isolation voltage: 3750 Vrms (min)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A

File No.E67349



Weight: 0.09 g (typ.)

# Pin Configurations (top view)



- 1 : Anode, Cathode
- 3 : Cathode, Anode
- 4 : Emitter
- 6: Collector

Start of commercial production 1988-04

#### Absolute Maximum Ratings (Ta = 25°C)

	Characteristics	Symbol	Rating	Unit
	Forward current	I <sub>F(RMS)</sub>	50	mA
	Forward current derating (Ta ≥ 53°C)	ΔI <sub>F</sub> /°C	-0.7	mA/°C
ED	Peak forward current (100 µs pulse,100 pps)	IFP	1	Α 🤇
	Diode power dissipation	PD	100	mW /
	Diode power dissipation derating $(Ta \ge 53^{\circ}C)$	ΔP <sub>D</sub> /°C	-1.39	mW/°C
	Junction temperature	Tj	125	°e
	Collector-emitter voltage	VCEO	80	(v// )
	Emitter-collector voltage	VECO	7	V
or	Collector current	IC	50	mA
Detector	Peak collector current (10 ms pulse,100 pps)	ICP	100	mA
De	Power dissipation	Pc	150	mW
	Power dissipation derating (Ta ≥ 25°C)	ΔP <sub>C</sub> /°C	-1.5	> mW/°C
	Junction temperature	Tj	(125)	°C>
Stor	age temperature range	T <sub>stg</sub>	-55 to 125	°C <
Ope	rating temperature range	Topr	-55 to 100	°¢
Lea	d soldering temperature (10 s)	T <sub>sol</sub>	260	.6
Tota	al package power dissipation	Pī	200	mW
Tota	al package power dissipation derating (Ta ≥ 25°C)	ΔΡτ/°C	-2.0	mW/°C
Isola	ation voltage (AC, 60 s, R.H. ≤ 60 %) (Note 1)	BVs	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 1: Device considered a two terminal device: Pins1, and 3 shorted together and 4 and 6 shorted together.

#### **Recommended Operating Conditions**

Characteristics	Symbol	Min	Тур.	Max	Unit
Supply voltage	Vcc	_	5	48	V
Forward current	IF(RMS)	_	1.6	20	mA
Collector current	Ic	_	1	10	mA
Operating temperature	T <sub>opr</sub>	-25	_	75	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

#### **Electrical Characteristics (Ta = 25°C)**

	Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
LED	Forward voltage	VF	I <sub>F</sub> = ±10 mA	1.0	1.15	1.3	V
۳	Capacitance	CT	V = 0 V, f = 1 MHz	_	60	-	pF
	Collector-emitter breakdown voltage	V(BR)CEO	IC = 0.5 mA	80	_	1	V
Detector	Emitter-collector breakdown voltage	V(BR)ECO	IE = 0.1 mA	7		1	V
	Collector dark current ICFO	lono	V <sub>CE</sub> = 48 V	$\mathcal{L}$	) 10	100	nA
	Collector dark current	ollector dark current  ICEO  VCE = 48 V, Ta = 85 °C	)    -	2	50	μΑ	
	Capacitance collector to emitter	CCE	V = 0 V, f = 1 MHz	)}	12	_	pF

## **Coupled Electrical Characteristics (Ta = 25°C)**

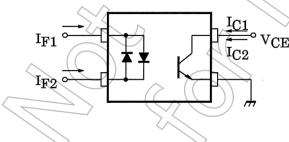
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Current transfer ratio	I <sub>C</sub> /I <sub>F</sub>	$I_F = \pm 1 \text{ mA}, V_{CE} = 0.5 \text{ V}$	100	/-//	1200	%
Low input CTR	I <sub>C</sub> /I <sub>F(low)</sub>	IF = ±0.5 mA, V <sub>CE</sub> = 1.5 V	50		_	%
Collector-emitter saturation voltage	saturation voltage V <sub>CE(sat)</sub>	IC = 0.5 mA, IF = ±1 mA		((	0.4	V
Conector-emitter Saturation Voltage		I <sub>C</sub> = 1 mA, I <sub>F</sub> = ±1 mA		0.2	-	]
Off-state collector current	I <sub>C(off)</sub>	$V_F = \pm 0.7 \text{ V}, V_{CE} = 48 \text{ V}$		1	10	μΑ
CTR symmetry	I <sub>C(ratio)</sub>	I <sub>C</sub> (I <sub>F</sub> = -1 mA) / I <sub>C</sub> (I <sub>F</sub> = 1 mA) (Note 2)	0.3		3	_

### Coupled Electrical Characteristics (Ta = -25 to 75°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Current transfer ratio	Jc/l <sub>F</sub>	$I_F = \pm 1 \text{ mA, V}_{CE} = 0.5 \text{ V}$	50	_	_	%
Low input CTR	IC/IF(low)	IF = ±0.5 mA, VCE = 1.5 V	_	50	_	%

Note 2:

$$I_{C(ratio)} = \frac{I_{C2}(I_F = I_{F2}, V_{CE} = 5V)}{I_{C1}(I_F = I_{F1}, V_{CE} = 5V)}$$



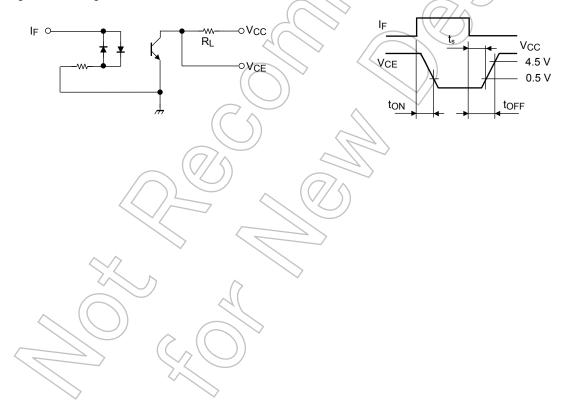
## Isolation characteristics (Ta = 25°C)

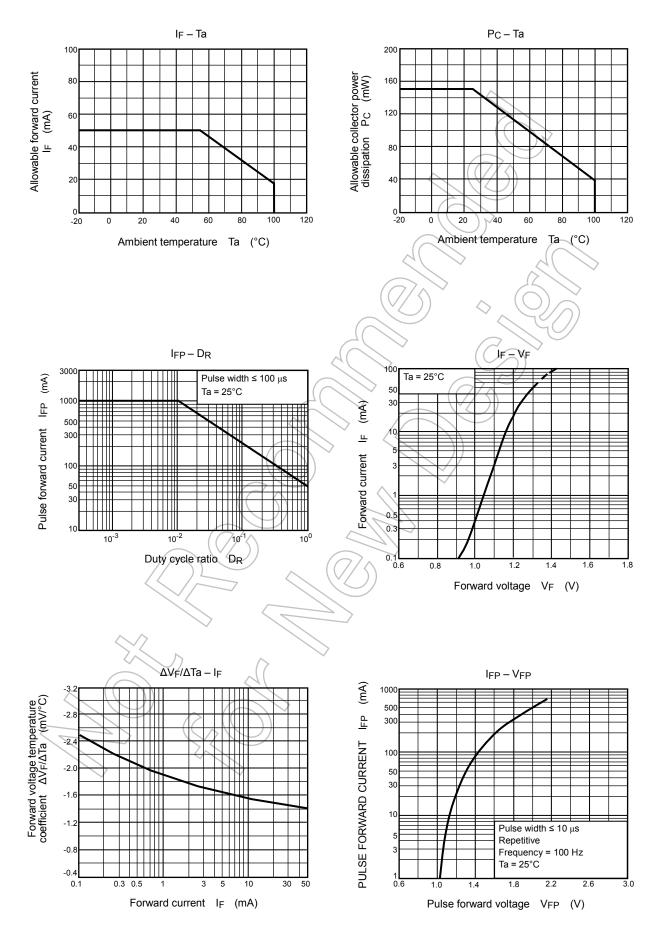
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	Cs	V <sub>S</sub> = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation resistance	Rs	V <sub>S</sub> = 500 V, R.H. ≤ 60 %	5×10 <sup>10</sup>	10 <sup>14</sup>	_	Ω
Isolation voltage	BVS	AC, 60 s	3750	_	_	Vrms

## **Switching Characteristics (Ta = 25°C)**

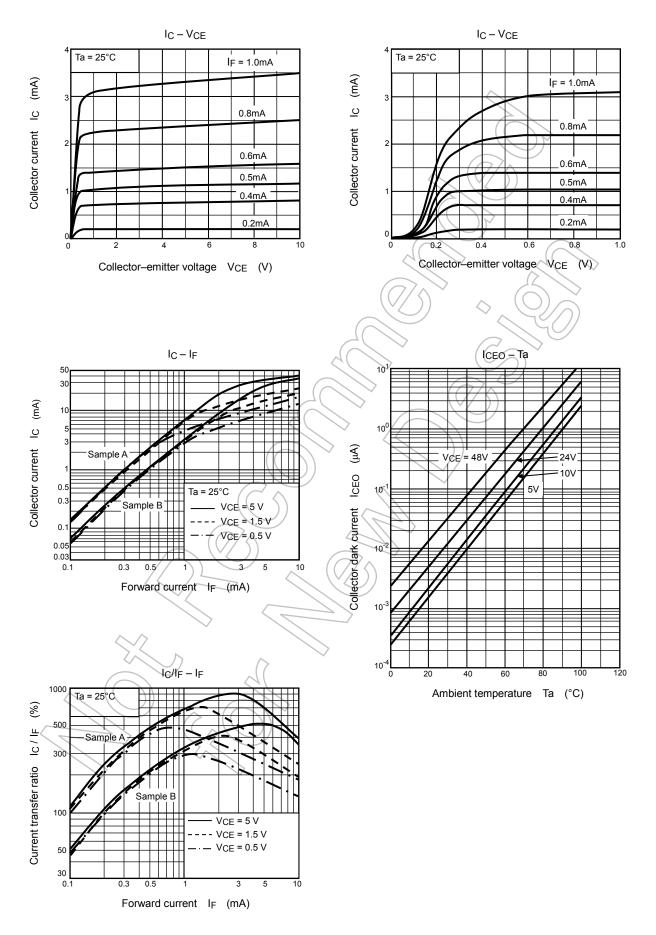
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Rise time	t <sub>r</sub>		_	8	_	
Fall time	tf	$V_{CC} = 10 \text{ V, } I_{C} = 2 \text{ mA}$ $R_{L} = 100\Omega$	_	8	-	
Turn-on time	ton		_	(10	<u></u>	μS
Turn-off time	t <sub>off</sub>		- 52	8	_	
Turn-on time	ton	5,1((//3)),	+(	))10_	_	
Storage time	ts	$V_{CC} = 5 \text{ V, IF} = \pm 1.6 \text{ mA}$	7	50)/	_	μS
Turn-off time	toff	$R_L = 4.7 \text{ k}\Omega$ (Fig.1)	7 (	> 300	_	

Fig. 1 Switching time test circuit

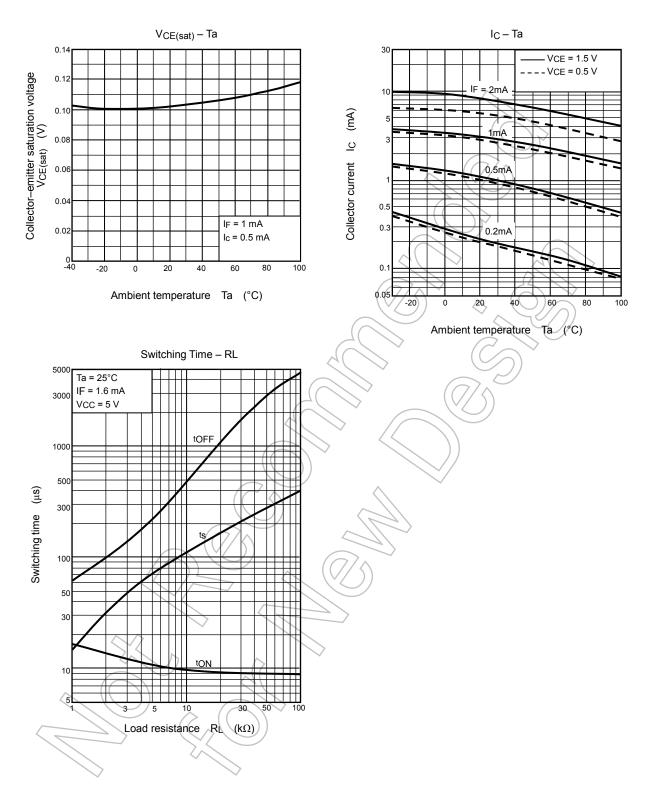




NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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