TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSVII)

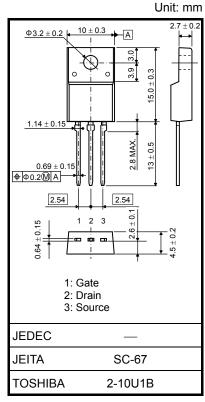
TK9A55DA

Switching Regulator Applications

- Low drain-source ON resistance: $RDS(ON) = 0.68 \Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 4.7 \text{ S}$ (typ.)
- Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 550 \ V)$
- Enhancement-mode: $V_{th} = 2 \text{ to } 4 \text{ V} (V_{DS} = 10 \text{ V}, \text{ ID} = 1 \text{ mA})$

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	550	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	I _D	8.5	А	
	Pulse (Note 1)	I _{DP}	34	A	
Drain power dissipation	on (Tc = 25°C)	PD	40	W	
Single pulse avalanche energy (Note 2)		E _{AS}	252	mJ	
Avalanche current		I _{AR}	8.5	А	
Repetitive avalanche energy (Note 3)		E _{AR}	4	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	–55 to 150	°C	

Absolute Maximum Ratings (Ta = 25°C)



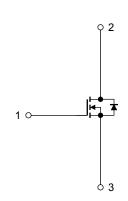
Weight : 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to case	R _{th (ch-c)}	3.125	°C/W	
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W	

Internal Connection



Start of commercial production 2009-01

Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 6.04 mH, R_G = 25 Ω , I_{AR} = 8.5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.

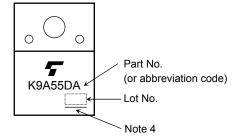
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS}=\pm 30~V,~V_{DS}=0~V$	_		±1	μA
Drain cut-off curr	rent	I _{DSS}	$V_{DS} = 550 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			10	μA
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	550			V
Gate threshold v	oltage	V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.0		4.0	V
Drain-source ON	resistance	R _{DS (ON)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 4.3 \text{ A}$	—	0.68	0.86	Ω
Forward transfer	admittance	Y _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 4.3 \text{ A}$	1.2	4.7	_	S
Input capacitance		C _{iss}		—	1050	_	pF
Reverse transfer capacitance		C _{rss}	$V_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$	_	5	_	
Output capacitance		C _{oss}			100		
Switching time	Rise time	tr	V_{GS} $I_D = 4.3 \text{ A } V_{OUT}$	_	25		
	Turn-on time	t _{on}	$\begin{bmatrix} 0 \lor - J & \Box & \downarrow \bullet \\ 0 \lor - I & \bullet & \downarrow \bullet \\ 50 \Omega & \bullet & \bullet & \bullet \end{bmatrix} R_{L} = 47 \Omega$		60		ns
	Fall time	t _f	/// VDD≈ 200 V		10	_	110
	Turn-off time	t _{off}	Duty \leq 1%, t _w = 10 μ s	—	75	_	
Total gate charge		Qg		_	20		
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 8.5 \text{ A}$		13		nC
Gate-drain charge		Q _{gd}]	—	7	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	8.5	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	34	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 8.5 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 8.5 A, V _{GS} = 0 V,	_	1200	_	ns
Reverse recovery charge	Qrr	dI _{DR} /dt = 100 A/μs	_	11		μC

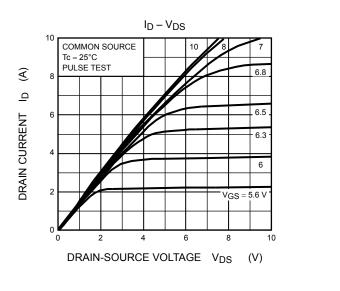
Marking

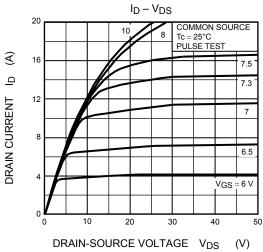


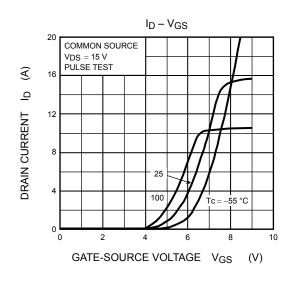
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

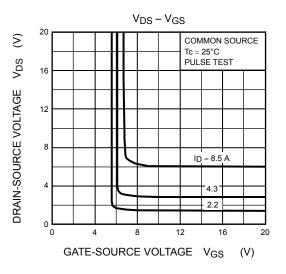
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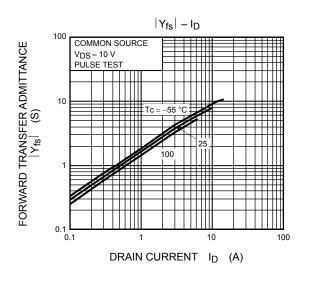
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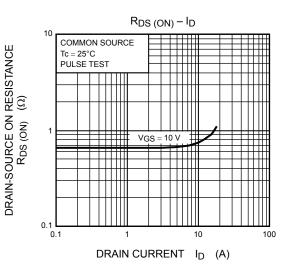




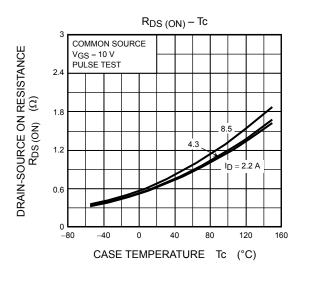


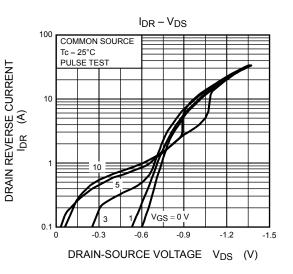


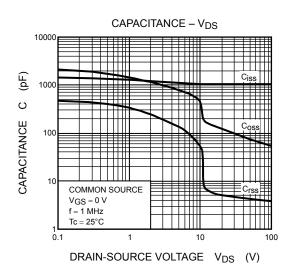


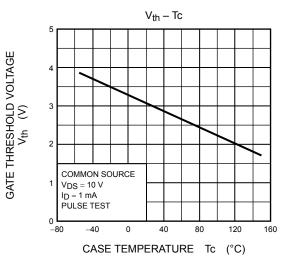


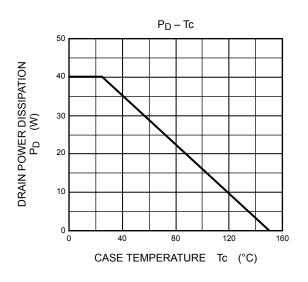
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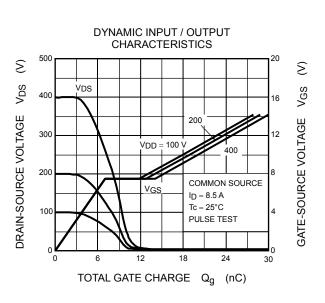


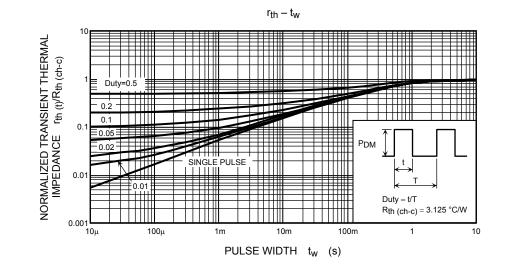


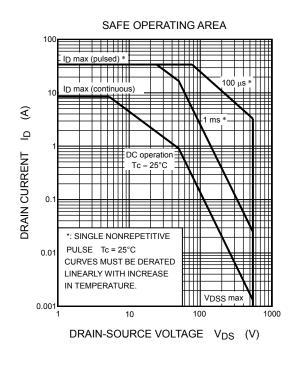


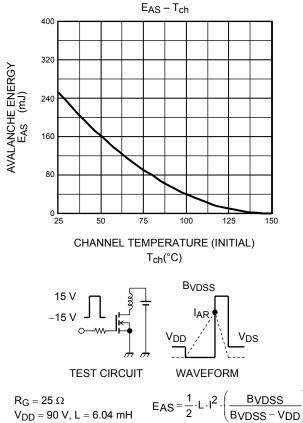












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