TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSVII)

TK6A50D

Switching Regulator Applications

• Low drain-source ON-resistance: $R_{DS (ON)} = 1.2 \Omega (typ.)$

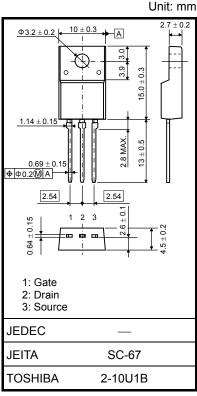
• High forward transfer admittance: |Y_{fs}| = 2.5 S (typ.)

• Low leakage current: I_{DSS} = 10 μA (max) (V_{DS} = 500 V)

• Enhancement mode: V_{th} = 2.4 to 4.4 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	500	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	I _D	6		
	Pulse (t = 1 ms) (Note 1)	I _{DP}	24	Α	
Drain power dissipati	on (Tc = 25°C)	P _D	35	W	
Single pulse avalanche energy (Note 2)		E _{AS}	144	mJ	
Avalanche current		I _{AR}	6	Α	
Repetitive avalanche	energy (Note 3)	E _{AR}	3.5	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature	range	T _{stg}	-55 to 150	°C	



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

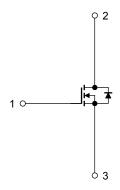
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	3.57	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 6.8 mH, $R_G = 25 \Omega$, $I_{AR} = 6 \text{ A}$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



Start of commercial production 2009-04

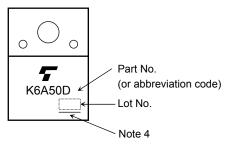
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μΑ
Drain cut-off curr	ent	I _{DSS}	V _{DS} = 500 V, V _{GS} = 0 V	_	_	10	μΑ
Drain-source breakdown voltage		V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	500	_	_	٧
Gate threshold v	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.4	_	4.4	٧
Drain-source ON	resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 3 A	_	1.2	1.4	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 3 A	0.6	2.5	_	S
Input capacitance		C _{iss}		_	540	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	3	_	pF
Output capacitance		Coss		_	60	_	
Switching time	Rise time	t _r	10 V V_{GS} 0 V $V_{DD} \approx 200 \text{ V}$ $V_{DD} \approx 200 \text{ V}$	_	18	_	
	Turn-on time	t _{on}		_	40	_	20
	Fall time	t _f		_	8	_	ns
	Turn-off time	t _{off}		_	55	_	
Total gate charge		Qg		_	11	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 6 \text{ A}$	_	6	_	nC
Gate-drain charge		Q _{gd}		_	5	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

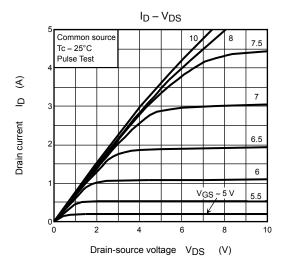
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I_{DR}	_	_	_	6	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	24	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 6 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	$I_{DR} = 6 \text{ A}, V_{GS} = 0 \text{ V},$	_	1000	_	ns
Reverse recovery charge	Qrr	dI _{DR} /dt = 100 A/μs	_	6	_	μС

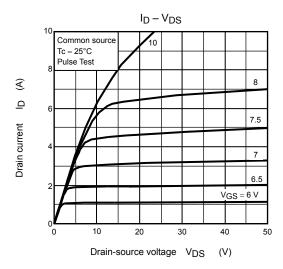
Marking

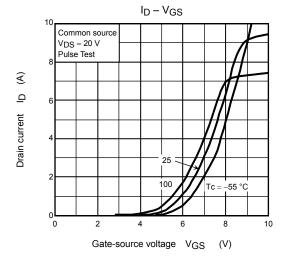


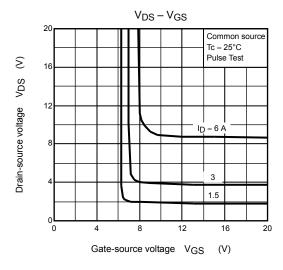
Note 4: A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

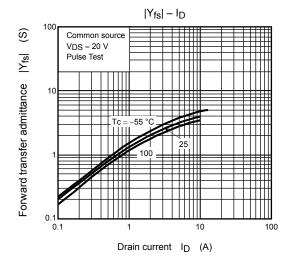
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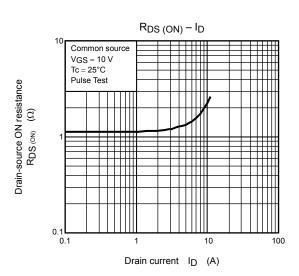


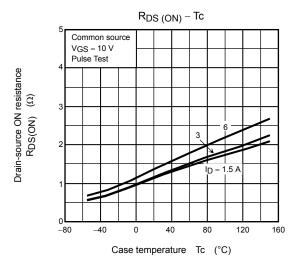


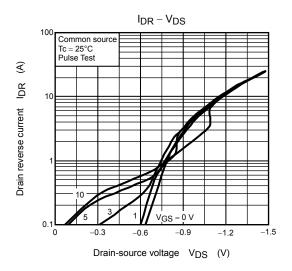


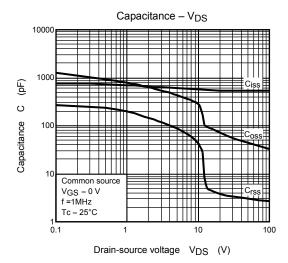


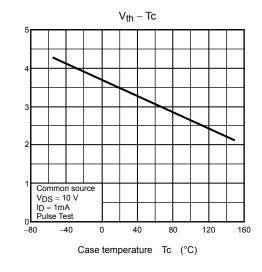


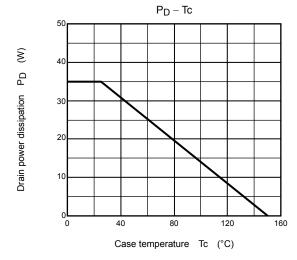


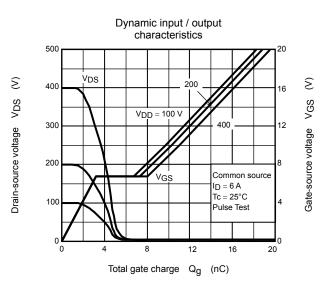








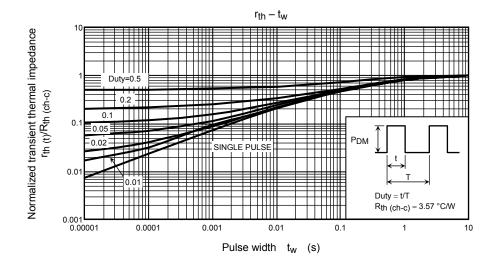


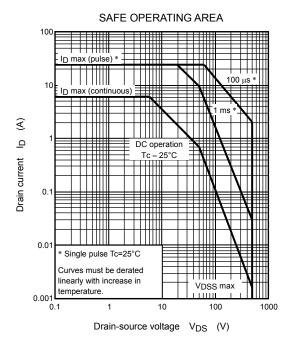


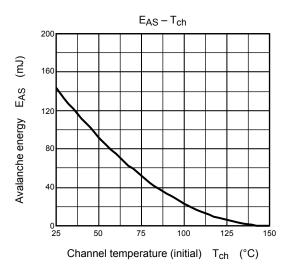
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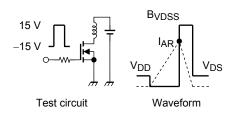
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Gate threshold voltage









$$R_G = 25 \ \Omega$$

$$V_{DD} = 90 \ V, \ L = 6.8 \ mH$$

$$\mathsf{EAS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{\mathsf{BVDSS}}{\mathsf{BVDSS} - \mathsf{VDD}} \right)$$

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