MOSFETs Silicon N-Channel MOS (π-MOSVII)

# TK6A45DA

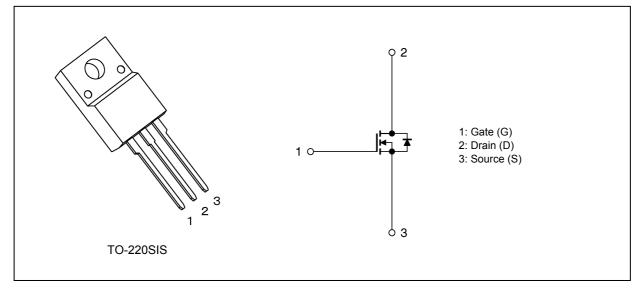
#### 1. Applications

• Switching Voltage Regulators

#### 2. Features

- (1) Low drain-source on-resistance:  $R_{DS(ON)} = 1.1 \Omega$  (typ.)
- (2) High forward transfer admittance:  $|Y_{fs}| = 3.0 \text{ S}$  (typ.)
- (3) Low leakage current:  $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 450 \ V)$
- (4) Enhancement mode:  $V_{th}$  = 2.4 to 4.4 V (V\_{DS} = 10 V,  $I_{D}$  = 1 mA)

### 3. Packaging and Internal Circuit



#### 4. Absolute Maximum Ratings (Note) ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	450	V
Gate-source voltage		V <sub>GSS</sub>	±30	
Drain current (DC)	(Note 1)	Ι <sub>D</sub>	5.5	A
Drain current (pulsed)	(Note 1)	I <sub>DP</sub>	22	1
Power dissipation $(T_c = 25^{\circ}C)$		PD	35	W
Single-pulse avalanche energy	(Note 2)	E <sub>AS</sub>	178	mJ
Avalanche current		I <sub>AR</sub>	5.5	A
Repetitive avalanche energy	(Note 3)	E <sub>AR</sub>	3.5	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature		T <sub>stg</sub>	-55 to 150	]

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production 2009-09 2014-01-05 Rev.2.0

#### 5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R <sub>th(ch-c)</sub>	3.57	°C/W
Channel-to-ambient thermal resistance	R <sub>th(ch-a)</sub>	62.5	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 9.82 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 5.5 A

Note 3: Repetitive rating; pulse width limited by maximum channel temperature

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

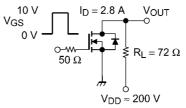
### 6. Electrical Characteristics

### 6.1. Static Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±30 V, $V_{DS}$ = 0 V	_	_	±1	μA
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 450 V, V <sub>GS</sub> = 0 V	_	_	10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	450	_	—	V
Gate threshold voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.4	—	4.4	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2.8 A	_	1.1	1.35	Ω
Forward transfer admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 2.8 A	0.75	3.0		S

### 6.2. Dynamic Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	490	_	pF
Reverse transfer capacitance	C <sub>rss</sub>	1	_	3	_	
Output capacitance	C <sub>oss</sub>	]		55	_	
Switching time (rise time)	tr	See Figure 6.2.1.	_	18	_	ns
Switching time (turn-on time)	t <sub>on</sub>	]		40	_	
Switching time (fall time)	t <sub>f</sub>	]		8		
Switching time (turn-off time)	t <sub>off</sub>		_	55	_	



Duty  $\leq$  1%,  $t_W = 10 \ \mu s$ 

Fig. 6.2.1 Switching Time Test Circuit

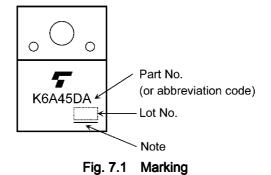
### 6.3. Gate Charge Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 360 \text{ V},  \text{V}_{GS} \text{ = } 10 \text{ V},  \text{I}_{D} \text{ = } 5.5 \text{ A}$	_	11	_	nC
Gate-source charge	Q <sub>gs</sub>	]	_	6	_	
Gate-drain charge	Q <sub>gd</sub>			5	_	

### 6.4. Source-Drain Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 1)	I <sub>DR</sub>	—	_	_	5.5	A
Reverse drain current (pulsed)	(Note 1)	I <sub>DRP</sub>	—	_	_	22	
Diode forward voltage		V <sub>DSF</sub>	I <sub>DR1</sub> = 5.5 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time		t <sub>rr</sub>	I <sub>DR</sub> = 5.5 A, V <sub>GS</sub> = 0 V		1200	_	ns
Reverse recovery charge		Q <sub>rr</sub>	-dI <sub>DR</sub> /dt = 100 A/μs		7.2	_	μC

### 7. Marking (Note)

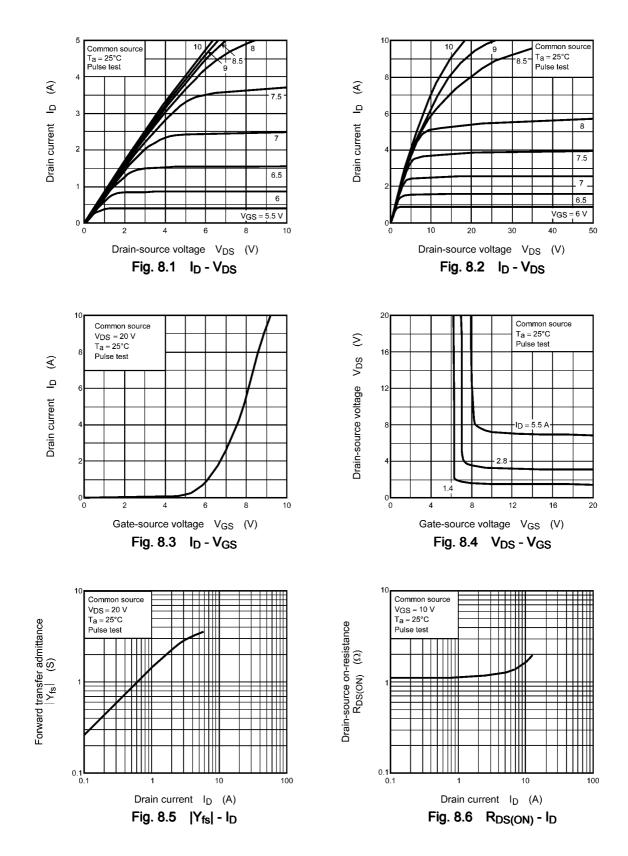


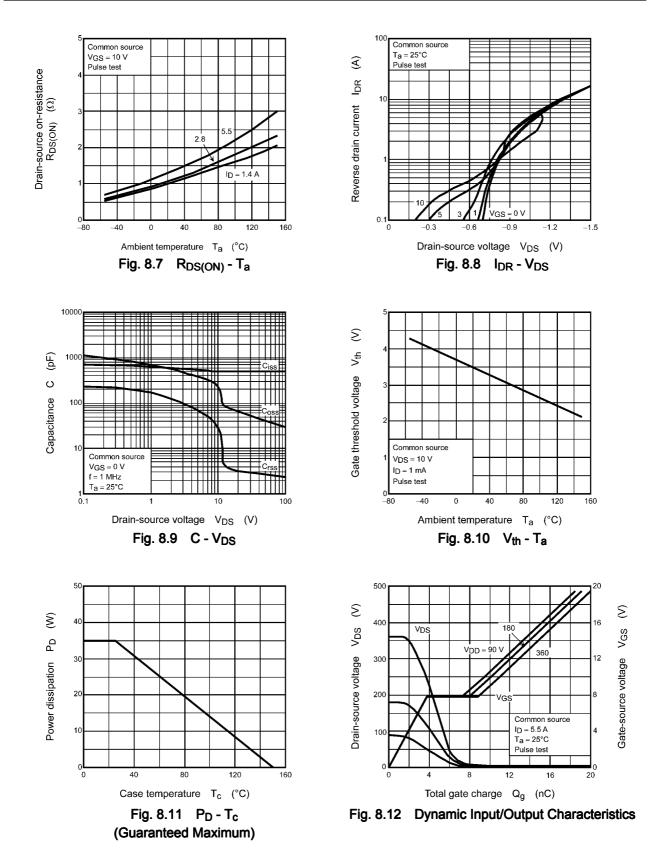
 Note:
 A line under a Lot No. identifies the indication of product Labels.

 Not underlined: [[Pb]]/INCLUDES > MCV
 Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

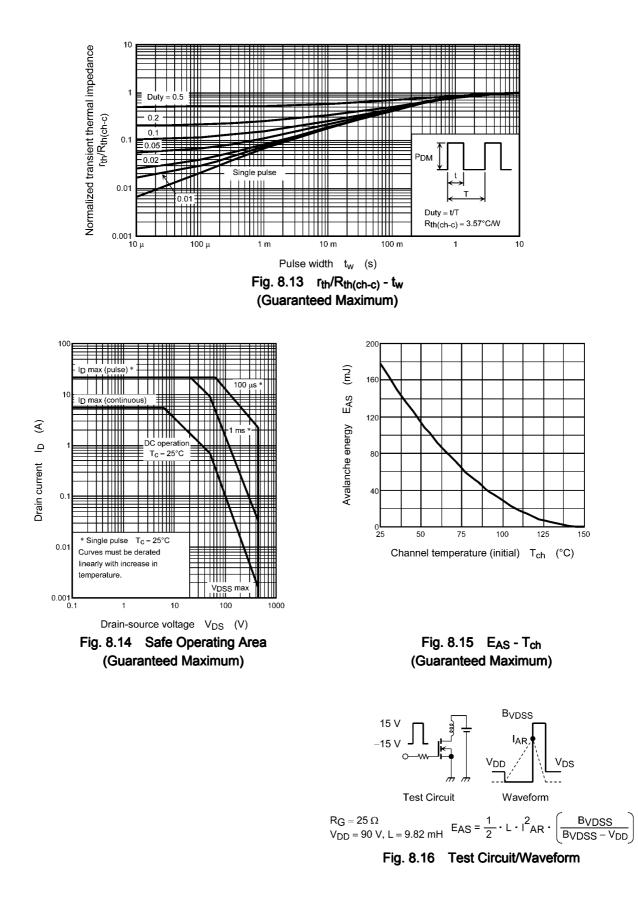
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 The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

### 8. Characteristics Curves (Note)







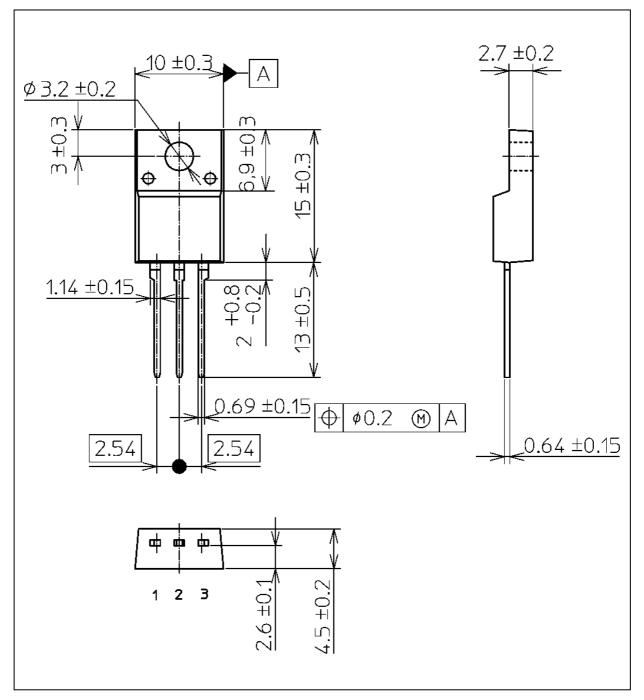


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

### Package Dimensions

TK6A45DA

Unit: mm



Weight: 1.7 g (typ.)

	Package Name(s)
JEITA: SC-67	
TOSHIBA: 2-10U1S	
Nickname: TO-220SIS	

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