MOSFETs Silicon N-channel MOS (U-MOSIV)

# TK65S04K3L

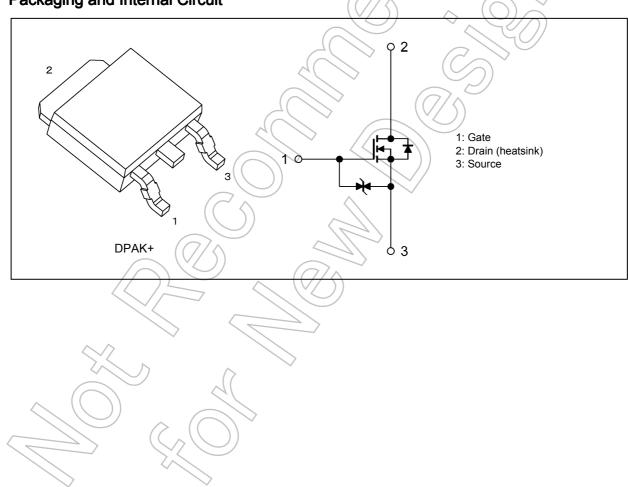
#### 1. Applications

- Automotive
- Motor Drivers
- DC-DC Converters
- Switching Voltage Regulators

#### 2. Features

- (1) AEC-Q101 qualified
- (2) Low drain-source on-resistance:  $R_{DS(ON)} = 3.6 \text{ m}\Omega \text{ (typ.)} (V_{GS} = 10 \text{ V})$
- (3) Low leakage current:  $I_{\rm DSS}$  = 10  $\mu A$  (max) (V\_{\rm DS} = 40 V)
- (4) Enhancement mode:  $V_{th} = 2.0$  to 3.0 V ( $V_{DS} = 10$  V,  $I_D = 1$  mÅ)

#### 3. Packaging and Internal Circuit



#### 4. Absolute Maximum Ratings (Note) ( $T_a = 25^{\circ}C$ unless otherwise specified)

| Characteristi                 | cs                      |          | Symbol           | Rating     | Unit |
|-------------------------------|-------------------------|----------|------------------|------------|------|
| Drain-source voltage          |                         |          | V <sub>DSS</sub> | 40         | V    |
| Gate-source voltage           |                         |          | V <sub>GSS</sub> | ±20        |      |
| Drain current (DC)            |                         | (Note 1) | I <sub>D</sub>   | 65         | A    |
| Drain current (pulsed)        |                         | (Note 1) | I <sub>DP</sub>  | 130        |      |
| Power dissipation             | (T <sub>c</sub> = 25°C) |          | PD               | 88         | W    |
| Single-pulse avalanche energy |                         | (Note 2) | E <sub>AS</sub>  | 130        | mJ   |
| Avalanche current             |                         |          | I <sub>AR</sub>  | 65         | A    |
| Channel temperature           |                         | (Note 3) | T <sub>ch</sub>  | ) 175      | °C   |
| Storage temperature           |                         | (Note 3) | T <sub>stg</sub> | -55 to 175 |      |

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### 5. Thermal Characteristics

|                                    | Characteristics |          | Symbol                | Max | Unit |
|------------------------------------|-----------------|----------|-----------------------|-----|------|
| Channel-to-case thermal resistance | ( )             | $(\vee)$ | R <sub>th(ch-c)</sub> | 1.7 | °C/W |
|                                    |                 |          | /                     |     |      |

Note 1: Ensure that the channel temperature does not exceed 175°C.

Note 2:  $V_{DD}$  = 25 V,  $T_{ch}$  = 25°C (initial), L = 32  $\mu$ H,  $R_G$  = 1  $\Omega$ ,  $I_{AR}$  = 65 A

Note 3: The definitions of the absolute maximum channel and storage temperatures are qualified per AEC-Q101.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

#### 6. Electrical Characteristics

### 6.1. Static Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

| Characteristics                | Symbol               | Test Condition                                  | Min       | Тур.             | Max | Unit |
|--------------------------------|----------------------|---|-----------|------------------|-----|------|
| Gate leakage current           | I <sub>GSS</sub>     | $V_{GS}$ = ±16 V, $V_{DS}$ = 0 V                | _         | _                | ±10 | μA   |
| Drain cut-off current          | I <sub>DSS</sub>     | V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V   | Y         | _                | 10  |      |
| Drain-source breakdown voltage | V <sub>(BR)DSS</sub> | I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V   | 40        |                  | _   | V    |
|                                | V <sub>(BR)DSX</sub> | I <sub>D</sub> = 10 mA, V <sub>GS</sub> = -20 V | 20        | $ \rightarrow ($ | _   |      |
| Gate threshold voltage         | V <sub>th</sub>      | V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA   | 2.0       | 2_               | 3.0 |      |
| Drain-source on-resistance     | R <sub>DS(ON)</sub>  | V <sub>GS</sub> = 6 V, I <sub>D</sub> = 32.5 A  | $\gamma$  | 4.9              | 7.9 | mΩ   |
|                                |                      | V <sub>GS</sub> = 10 V, I <sub>D</sub> = 32.5 A | $\supset$ | 3.6              | 4.5 |      |

### 6.2. Dynamic Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

| Characteristics                | Symbol           | Test Condition   | Min    | Тур. | Max | Unit |
|--------------------------------|------------------|--|--------|------|-----|------|
| Input capacitance              | C <sub>iss</sub> | V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz | - /    | 2800 | -   | pF   |
| Reverse transfer capacitance   | C <sub>rss</sub> |  | ((     | 440  | _   |      |
| Output capacitance             | C <sub>oss</sub> |  | R      | 680  | ) — |      |
| Switching time (rise time)     | tr               | See Figure 6.2.1.  | $\sim$ | TH/  | _   | ns   |
| Switching time (turn-on time)  | t <sub>on</sub>  |  |        | 24   | —   |      |
| Switching time (fall time)     | t <sub>f</sub>   |  | ~_)    | 16   | —   |      |
| Switching time (turn-off time) | t <sub>off</sub> |  |        | 59   | _   |      |

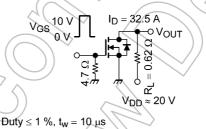


Fig. 6.2.1 Switching Time Test Circuit

### 6.3. Gate Charge Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

| Characteristics                                 | Symbol          | Test Condition                                       | Min | Тур. | Max | Unit |
|---|-----------------|--|-----|------|-----|------|
| Total gate charge (gate-source plus gate-drain) | Qg              | $V_{DD} \approx 32$ V, $V_{GS}$ = 10 V, $I_D$ = 65 A | —   | 63   | —   | nC   |
| Gate-source charge                              | Q <sub>gs</sub> |  | _   | 39   | _   |      |
| Gate-drain charge                               | Q <sub>gd</sub> |  | _   | 24   | _   |      |

## 6.4. Source-Drain Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

| Characteristics                |          | Symbol           | Test Condition                                | Min | Тур. | Max  | Unit |
|--------------------------------|----------|------------------|---|-----|------|------|------|
| Reverse drain current (DC)     | (Note 4) | I <sub>DR</sub>  | —   | _   | —    | 65   | A    |
| Reverse drain current (pulsed) | (Note 4) | I <sub>DRP</sub> |   | _   | _    | 130  |      |
| Diode forward voltage          |          | V <sub>DSF</sub> | I <sub>DR</sub> = 65 A, V <sub>GS</sub> = 0 V | _   | _    | -1.2 | V    |
| Reverse recovery time          |          | t <sub>rr</sub>  | I <sub>DR</sub> = 65 A, V <sub>GS</sub> = 0 V |     | 51   | _    | ns   |
| Reverse recovery charge        |          | Q <sub>rr</sub>  | -dI <sub>DR</sub> /dt = 50 A/μs               | _   | 33   | —    | nC   |

Note 4: Ensure that the channel temperature does not exceed 175°C.

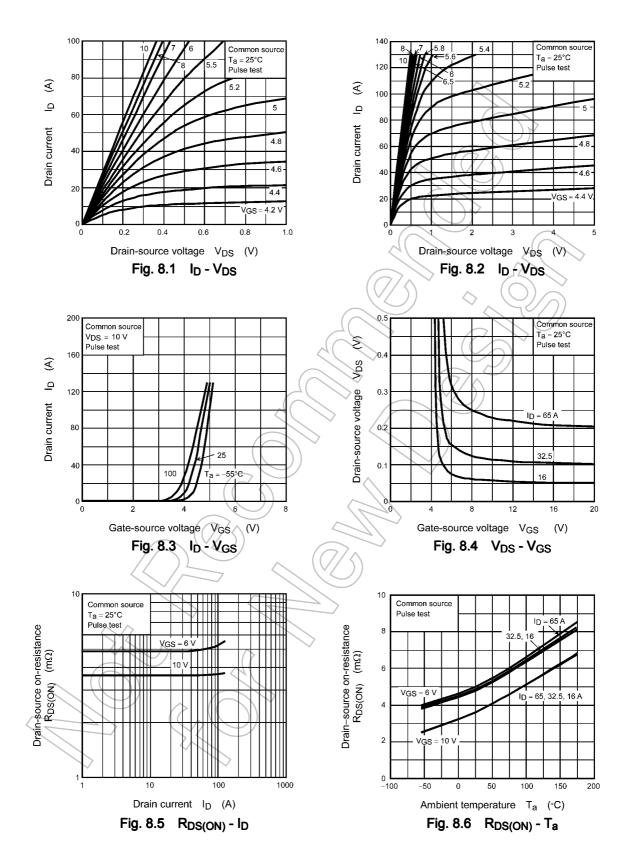
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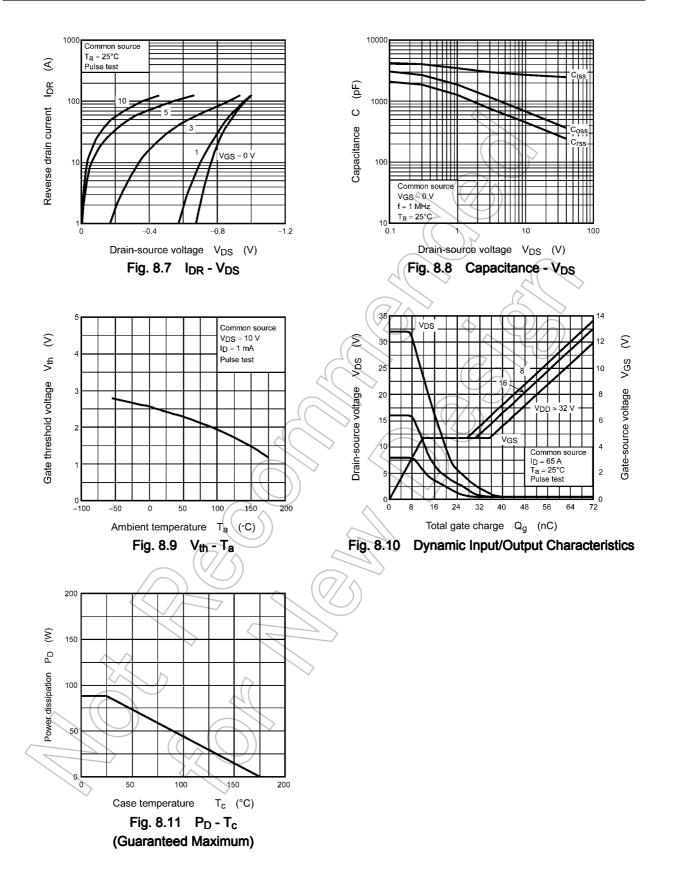
## 7. Marking (Note)

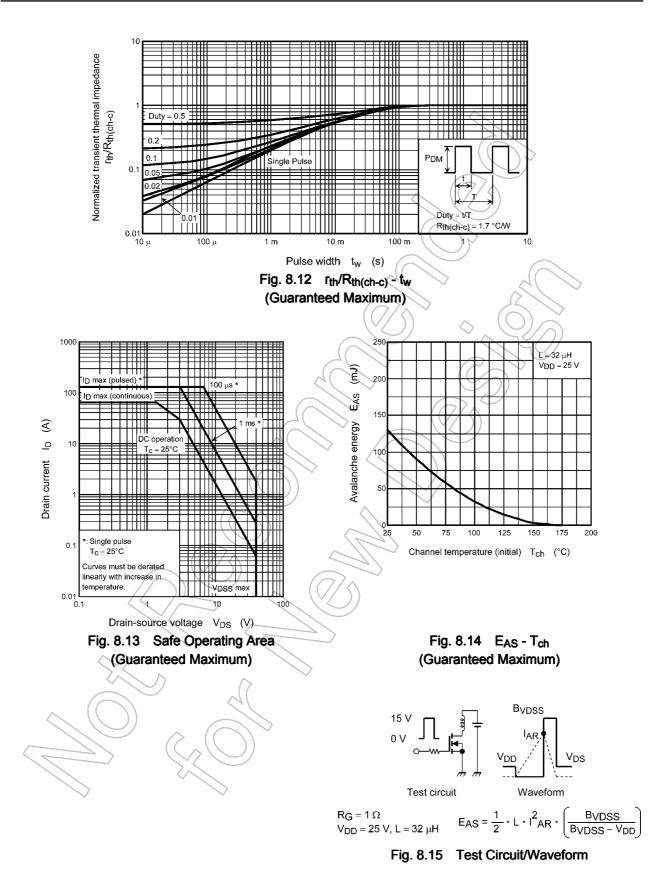
TOSHIBA

|            | K65S04K3 🔶 Part No.   |
|------------|---|
|            | K65S04K3 ← Part No.<br>(or abbreviation code)   |
|            | L [k Lot No.  |
|            | Note  |
|            | Fig. 7.1 Marking  |
| Note:      | A line under a Lot No. identifies the indication of product Labels.   |
|            | Not underlined: [[Pb]]/INCLUDES > MCV<br>Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]                                       |
|            | Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS                             |
|            | compatibility of Product.<br>The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the |
|            | restriction of the use of certain hazardous substances in electrical and electronic equipment.  |
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### 8. Characteristics Curves (Note)



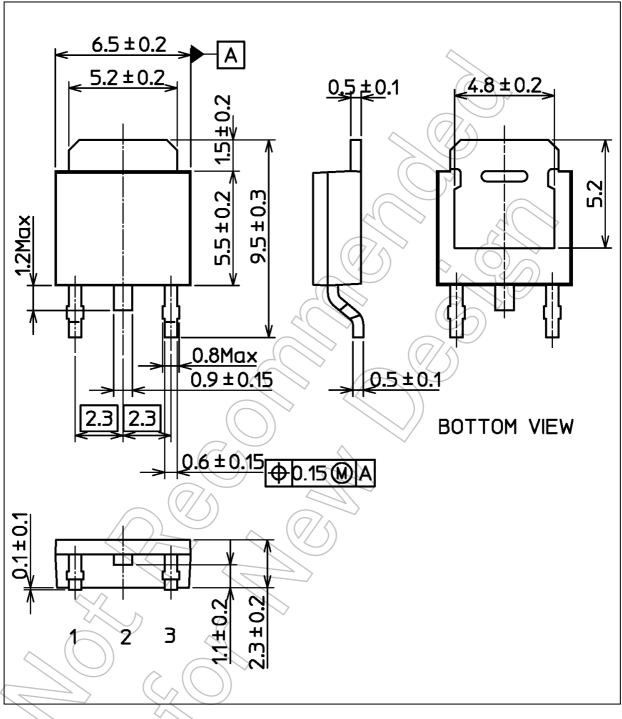




Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Unit: mm



Weight: 0.36 g (typ.)

|                 | Package Name(s) |  |
|-----------------|-----------------|--|
| TOSHIBA: 2-7M1A |                 |  |
| Nickname: DPAK+ |                 |  |

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