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TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSVII)

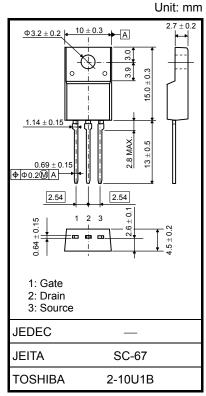
TK5A53D

Switching Regulator Applications

- Low drain-source ON-resistance: $RDS(ON) = 1.2 \Omega(typ.)$
- High forward transfer admittance: $|Y_{fs}| = 2.8 \text{ S}$ (typ.)
- Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 525 \ V)$
- Enhancement mode: $V_{th} = 2.4$ to 4.4 V ($V_{DS} = 10$ V, $I_D = 1$ mA)

<u> </u>								
Characteristics			Symbol	Rating	Unit			
Drain-source voltage			V _{DSS}	525	V			
Gate-source voltage			V _{GSS}	±30	V			
Drain current	DC (Not	e 1)	I _D	5	А			
	Pulse (Not	e 1)	I _{DP}	20	A			
Drain power dissipation (Tc = 25° C)			PD	35	W			
Single pulse avalanche energy (Note 2)			E _{AS}	158	mJ			
Avalanche current			I _{AR}	5	А			
Repetitive avalanche energy (Note 3)			E _{AR}	3.5	mJ			
Channel temperature			T _{ch}	150	°C			
Storage temperature range			T _{stg}	-55 to 150	°C			

Absolute Maximum Ratings (Ta = 25°C)



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

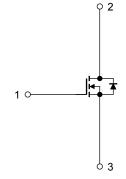
Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to case	R _{th (ch-c)}	3.57	°C/W	
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 90 \text{ V}, \text{ T}_{ch} = 25^{\circ}C(\text{initial}), \text{ L} = 10.8 \text{ mH}, \text{ R}_{G} = 25 \Omega, \text{ I}_{AR} = 5 \text{ A}$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



Start of commercial production 2009-04

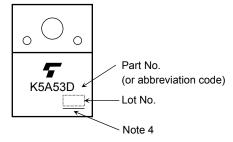
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS}=\pm 30~V,~V_{DS}=0~V$	_		±1	μA
Drain cut-off current		I _{DSS}	$V_{DS} = 525 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			10	μA
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	525		_	V
Gate threshold ve	oltage	V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.4	4 — 4.4		V
Drain-source ON	resistance	R _{DS (ON)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 2.5 \text{ A}$	_	1.2	1.5	Ω
Forward transfer	er admittance $ Y_{fs} $ $V_{DS} = 10 \text{ V}, I_D = 2.5 \text{ A}$		$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 2.5 \text{ A}$	0.7	2.8	_	S
Input capacitance		C _{iss}		_	540	_	
Reverse transfer capacitance		C _{rss}	V_{DS} = 25 V, V_{GS} = 0 V, f = 1 MHz	_	3	_	pF
Output capacitance		C _{oss}	1	_	60	_	
Switching time	Rise time	tr	$V_{GS} = 2.5 \text{ A } V_{OUT}$	_	18		- ns
	Turn-on time	t _{on}			40		
	Fall time	t _f		_	8	_	
	Turn-off time	t _{off}	$V_{DD} \approx 200 \text{ V}$ Duty \leq 1%, t _W = 10 µs		55	_	
Total gate charge		Qg		_	11		
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}$	_	6	_	nC
Gate-drain charge		Q _{gd}]		5		

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	5	А
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	20	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 5 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 5 A, V _{GS} = 0 V,	_	1000	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} /dt = 100 A/μs		6		μC

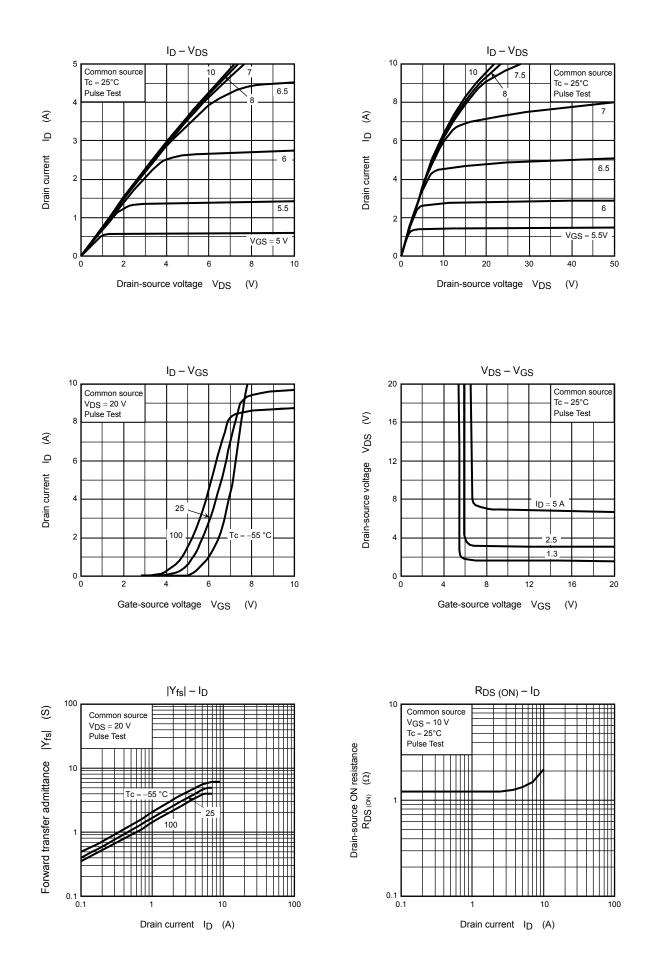
Marking



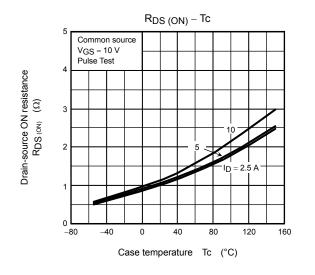
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

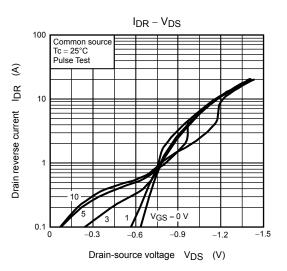
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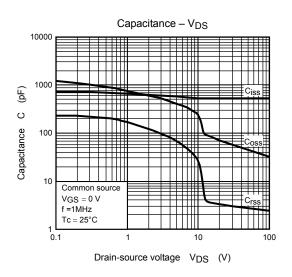
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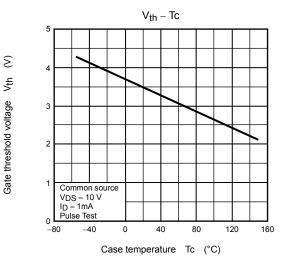


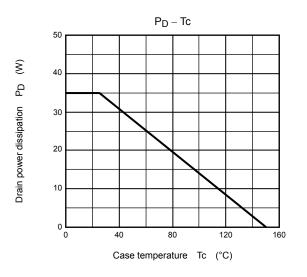
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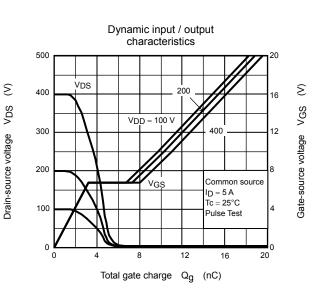


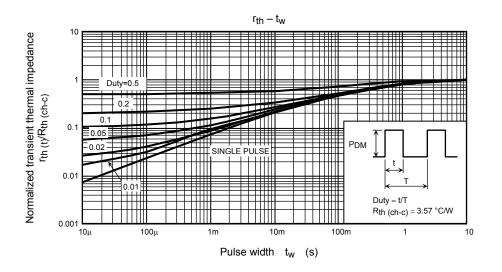


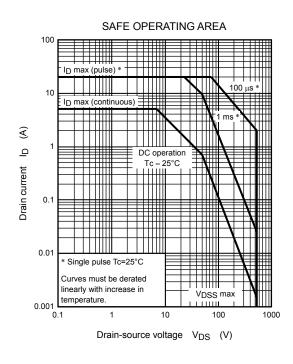


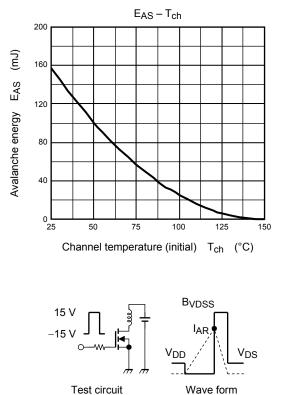














 $R_G = 25 \Omega$

 $E_{AS} = \frac{1}{2} \cdot L \cdot l^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$ V_{DD} = 90 V, L = 10.8 mH

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