

MOSFETs Silicon N-Channel MOS ( $\pi$ -MOSIX)

# TK2K2A60F

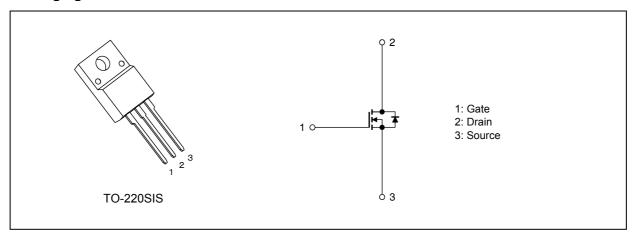
#### 1. Applications

· Switching Power Supplies

#### 2. Features

- (1) Easy to control Gate switching
- (2) Low drain-source on-resistance:  $R_{DS(ON)} = 1.82 \Omega$  (typ.)
- (3) Enhancement mode:  $V_{th} = 2$  to 4 V ( $V_{DS} = 10$  V,  $I_D = 0.35$  mA)

#### 3. Packaging and Internal Circuit



## 4. Absolute Maximum Ratings (Note) (Ta = 25 °C unless otherwise specified)

Characteristics	Symbol	Rating	Unit		
Drain-source voltage			$V_{DSS}$	600	V
Gate-source voltage			V <sub>GSS</sub>	±30	
Drain current (DC)		(Note 1)	I <sub>D</sub>	3.5	Α
Drain current (pulsed)		(Note 1)	I <sub>DP</sub>	14	
Power dissipation	(T <sub>c</sub> = 25 °C)		P <sub>D</sub>	30	W
Single-pulse avalanche energy		(Note 2)	E <sub>AS</sub>	78	mJ
Single-pulse avalanche current			I <sub>AS</sub>	3.5	Α
Reverse drain current (DC)		(Note 1)	I <sub>DR</sub>	3.5	
Reverse drain current (pulsed)		(Note 1)	I <sub>DRP</sub>	14	]
Channel temperature			T <sub>ch</sub>	150	°C
Storage temperature			T <sub>stg</sub>	-55 to 150	
Isolation voltage (RMS)	(t = 1.0 s)		V <sub>ISO(RMS)</sub>	2000	V
Mounting torque			TOR	0.6	N·m

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

1

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production

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#### 5. Thermal Characteristics

Characteristics		Max	Unit
Channel-to-case thermal resistance	R <sub>th(ch-c)</sub>	4.16	°C/W
Channel-to-ambient thermal resistance	R <sub>th(ch-a)</sub>	62.5	

Note 1: Ensure that the channel temperature does not exceed 150  $^{\circ}\text{C}.$ 

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25 °C (initial), L = 11.2 mH,  $I_{AS}$  = 3.5 A

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.



#### 6. Electrical Characteristics

## 6.1. Static Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μΑ
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	_	_	10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	600	_	_	V
Gate threshold voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.35 mA	2	_	4	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1.8 A	_	1.82	2.2	Ω

## 6.2. Dynamic Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	$V_{DS} = 300 \text{ V}, V_{GS} = 0 \text{ V}, f = 100 \text{ kHz}$	_	450	_	pF
Reverse transfer capacitance	$C_{rss}$		_	3.6	_	
Output capacitance	C <sub>oss</sub>		_	16	_	
Gate resistance	r <sub>g</sub>	V <sub>DS</sub> = OPEN , f = 1 MHz	_	7	_	Ω
Switching time (rise time)	t <sub>r</sub>	See Figure 6.2.1	_	14	_	ns
Switching time (turn-on time)	t <sub>on</sub>		_	32	_	
Switching time (fall time)	t <sub>f</sub>		_	15	_	
Switching time (turn-off time)	t <sub>off</sub>		_	50	_	
MOSFET dv/dt ruggedness	dv/dt	$V_{DS} \le V_{(BR)DSS}$ , $I_D \le 1.8 A$	12		_	V/ns

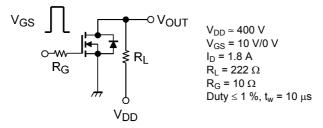


Fig. 6.2.1 Switching Time Test Circuit

### 6.3. Gate Charge Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3.5 \text{ A}$	_	13	_	nC
Gate-source charge 1	Q <sub>gs1</sub>		_	3.3	_	
Gate-drain charge	Q <sub>gd</sub>		_	5.3	_	

## 6.4. Source-Drain Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Diode forward voltage	$V_{DSF}$	$I_{DR} = 3.5 \text{ A}, V_{GS} = 0 \text{ V}$	-	1	-1.7	V
Reverse recovery time		V <sub>DD</sub> ≈ 400 V,		600		ns
Reverse recovery charge	$Q_{rr}$	I <sub>DR</sub> = 3.5 A, V <sub>GS</sub> = 0 V -dI <sub>DR</sub> /dt = 100 A/μs	_	2.9	_	μС
Peak reverse recovery current	I <sub>rr</sub>	-αιρκ/αι – 100 Α/μο	_	9.5	_	Α
Diode dv/dt ruggedness	dv/dt	$V_{DD} \le 400 \text{ V}, I_{DR} \le 3.5 \text{ A}, V_{GS} = 0 \text{ V}$	4	_	_	V/ns



### 7. Marking (Note)

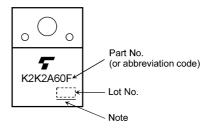


Fig. 7.1 Marking

Note: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.



### 8. Characteristics Curves (Note)

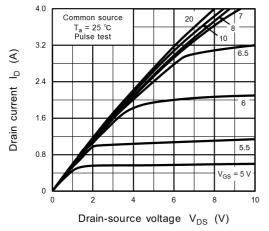


Fig. 8.1 I<sub>D</sub> - V<sub>DS</sub>

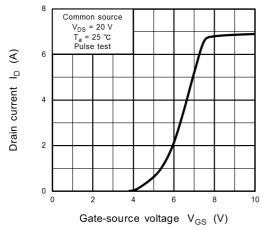


Fig. 8.3 I<sub>D</sub> - V<sub>GS</sub>

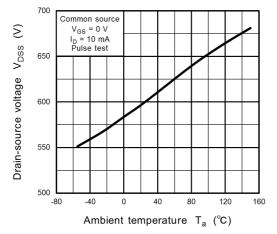


Fig. 8.5 V<sub>DSS</sub> - T<sub>a</sub>

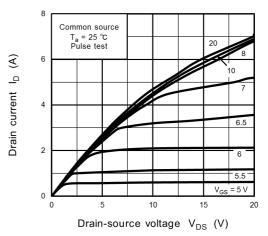
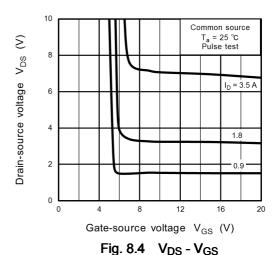


Fig. 8.2 I<sub>D</sub> - V<sub>DS</sub>



Drain current  $I_D$  (A)

Fig. 8.6 R<sub>DS(ON)</sub> - I<sub>D</sub>



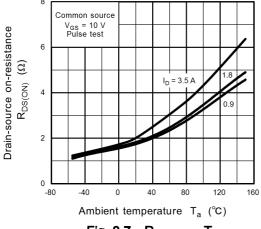
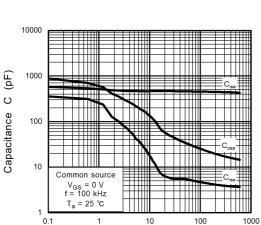


Fig. 8.7 R<sub>DS(ON)</sub> - T<sub>a</sub>



Drain-source voltage  $V_{DS}$  (V) Fig. 8.9 C -  $V_{DS}$ 

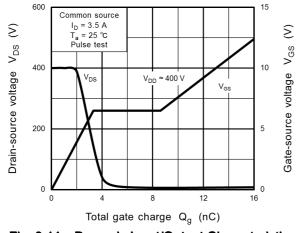


Fig. 8.11 Dynamic Input/Output Characteristics

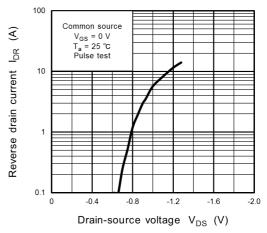


Fig. 8.8 IDR - VDS

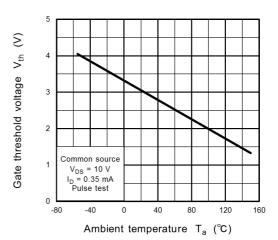


Fig. 8.10 V<sub>th</sub> - T<sub>a</sub>

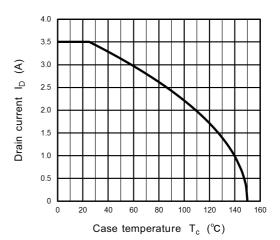


Fig. 8.12 I<sub>D</sub> - T<sub>c</sub> (Guaranteed Maximum)

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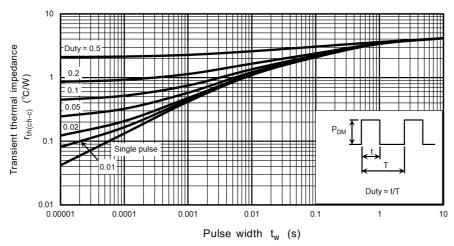


Fig. 8.13 r<sub>th</sub> - t<sub>w</sub> (Guaranteed Maximum)

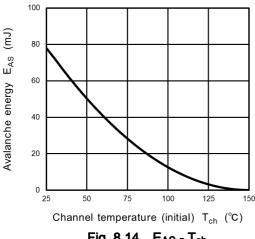


Fig. 8.14 E<sub>AS</sub> - T<sub>ch</sub> (Guaranteed Maximum)

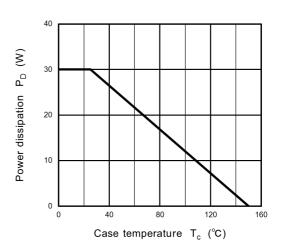


Fig. 8.15 P<sub>D</sub> - T<sub>c</sub> (Guaranteed Maximum)

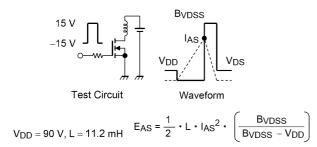


Fig. 8.16 Test Circuit/Waveform

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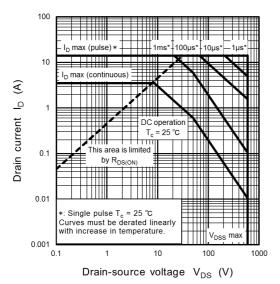


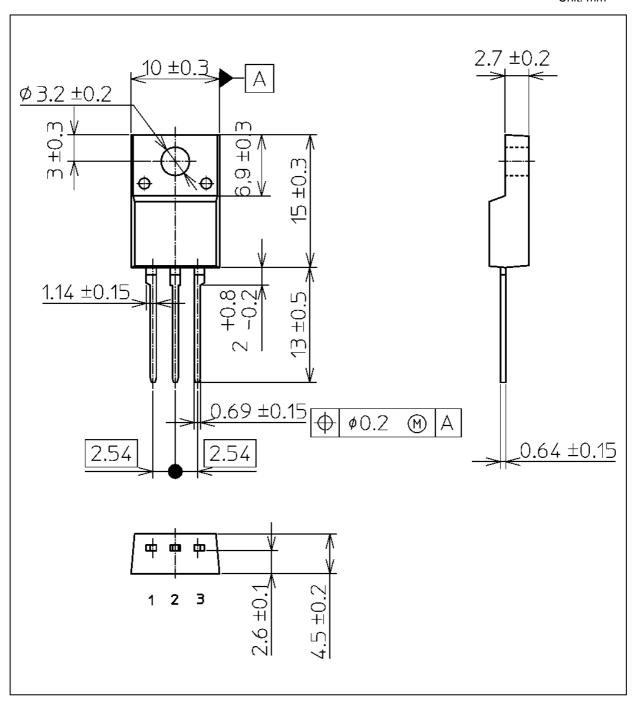
Fig. 8.17 Safe Operating Area (Guaranteed Maximum)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



## **Package Dimensions**

Unit: mm



Weight: 1.7 g (typ.)

	Package Name(s)
	Fackage Name(s)
JEITA: SC-67	
TOSHIBA: 2-10U1S	
Nickname: TO-220SIS	



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