TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (DTMOS II)

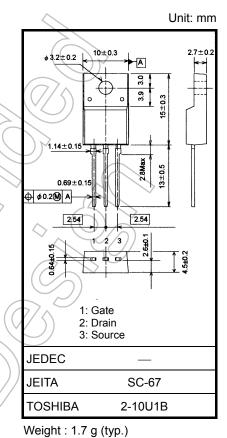
# **TK20A60U**

#### Switching Regulator Applications

- Low drain-source ON-resistance:  $RDS(ON) = 0.165 \Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 12 \text{ S}$  (typ.)
- Low leakage current:  $I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 600 \ V)$
- Enhancement-mode:  $V_{th} = 3.0$  to 5.0 V ( $V_{DS} = 10$  V,  $I_D = 1$  mA)

Characteristics			Symbol	Rating	Unit
Drain-source voltage			V <sub>DSS</sub>	600	$(\underline{\langle x \rangle})$
Gate-source voltage			V <sub>GSS</sub>	±30	V
Drain current	DC	(Note 1)	Ι <sub>D</sub>	20	A
	Pulse	(Note 1)	I <sub>DP</sub>	40	$>$ $\land$
Drain power dissipation (Tc = 25°C)			PD	45	W
Single pulse avalanche energy (Note 2)			Eas	144	mJ
Avalanche current			IAR	20	A
Repetitive avalanche energy (Note 3)			EAR	4.5	mJ
Channel temperature			Тсп	150	°C
Storage temperature range			(T <sub>stg</sub> )	-55 to 150	°C

#### Absolute Maximum Ratings (Ta = 25°C)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### **Thermal Characteristics**

Thermal resistance, channel to case Rth (ch-c) 2.78	°C/W
Thermal resistance, channel to ambient R <sub>th (ch-a)</sub> 62.5	°C/W

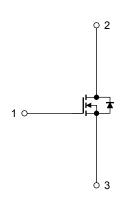
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 0.63 mH, R<sub>G</sub> = 25  $\Omega$ , I<sub>AR</sub> = 20 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

Internal Connection



Start of commercial production 2008-05

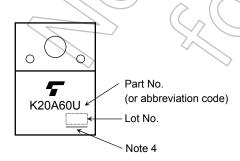
**Electrical Characteristics (Ta = 25°C)** 

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS}=\pm 30~V,~V_{DS}=0~V$		_	±1	μA
Drain cut-off current		IDSS	$V_{DS} = 600 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	_	_	100	μA
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	600	_	_	V
Gate threshold voltage		V <sub>th</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	3.0	_	5.0	V
Drain-source ON-resistance		R <sub>DS (ON)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ A}$	$(\mathcal{F})$	0.165	0.19	Ω
Forward transfer admittance		Y <sub>fs</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ A}$	3	12	_	S
Input capacitance		C <sub>iss</sub>		$(\mathcal{A})$	1470	_	
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$		150	_	pF
Output capacitance		C <sub>oss</sub>		7	3500	_	
Switching time	Rise time	tr	$\begin{array}{c} 10 \text{ V} \\ \text{V}_{\text{GS}} \end{array} \qquad I_{\text{D}} = 10 \text{ A}  \text{V}_{\text{OUT}} \\ \bullet  \circ  \circ  \circ  \circ  \circ  \circ  \circ  \circ  \circ $	—	40	$\langle \langle$	ns
	Turn-on time	t <sub>on</sub>	$0 \vee $		80	$\geq -$	
	Fall time	t <sub>f</sub>	/// NL =5012		12	) _	
	Turn-off time	t <sub>off</sub>	Duty $\leq$ 1%, t <sub>w</sub> = 10 µs	$\mathcal{A}$	100	_	
Total gate charge		Qg		$\sim$	27		
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 20 \text{ A}$	) —	16	_	nC
Gate-drain charge		Q <sub>gd</sub>		_	11	_	

### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)			_	_	20	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	- (9) -	_	_	40	Α
Forward voltage (diode)	VDSF	I <sub>DR</sub> = 20 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	trr	I <sub>DR</sub> = 20 A, V <sub>GS</sub> = 0 V,	_	450	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 100 A/μs	_	8.1	_	μC

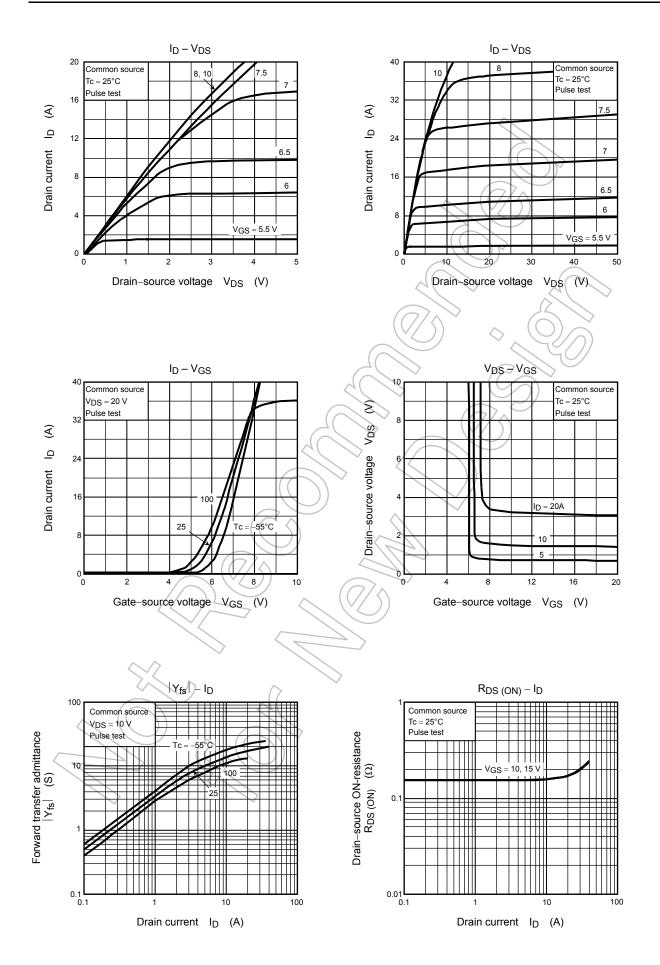
### Marking

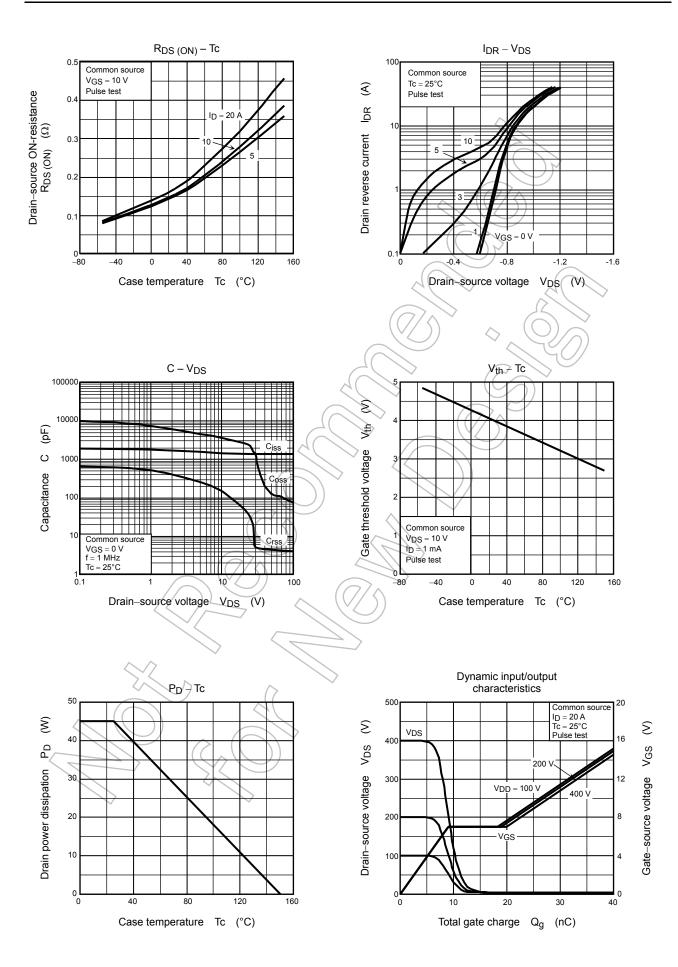


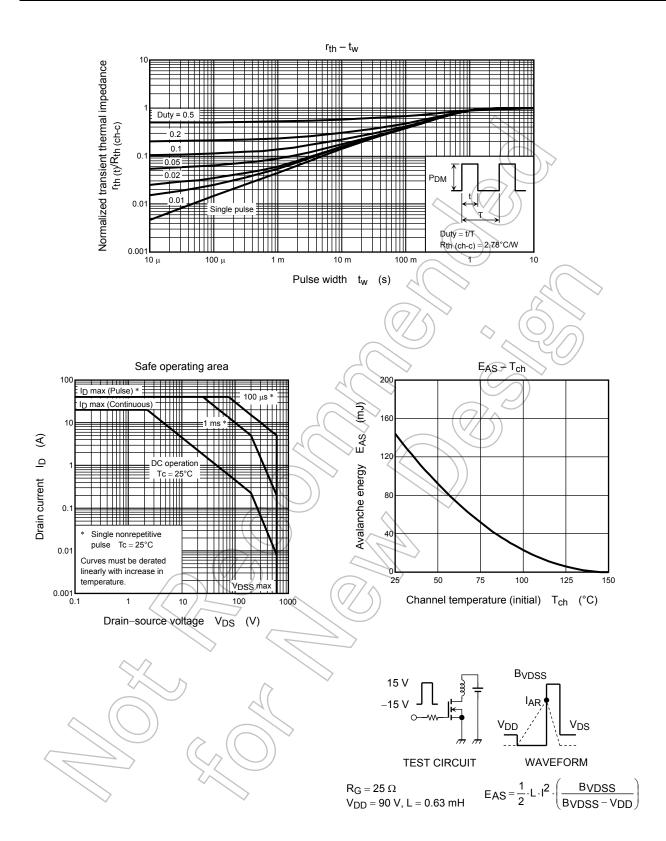
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

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