MOSFETs Silicon N-channel MOS (U-MOSIV)

TK18E10K3

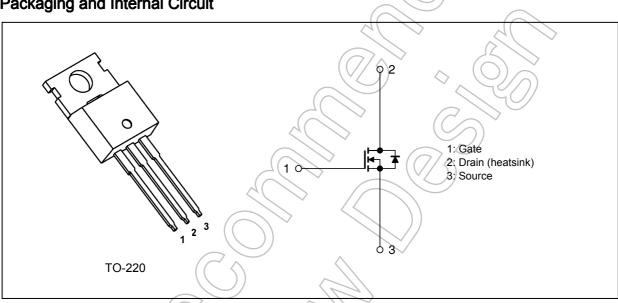
1. Applications

Switching Voltage Regulators

2. Features

- (1) Low drain-source on-resistance: $R_{DS(ON)} = 33 \text{ m}\Omega$ (typ.)
- (2)High forward transfer admittance: $|Y_{fs}| = 28 \text{ S}$ (typ.)
- (3) Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 100 \ V)$
- Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA) (4)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) (Ta = 25°C unless otherwise specified)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	100	V
Drain-gate voltage (R _{GS} =	20 kΩ)	V _{DGR}	100	
Gate-source voltage		V _{GSS}	±20	
Drain current (DC)	(Note 1)	Ι _D	18	A
Drain current (pulsed)	(Note 1)	I _{DP}	54	
Power dissipation (T _c = 1	25°C)	PD	71	W
Single-pulse avalanche energy	(Note 2)	E _{AS}	28	mJ
Avalanche current		I _{AR}	18	A
Repetitive avalanche energy	(Note 3)	E _{AR}	7.1	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature		T _{stg}	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production

5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R _{th(ch-c)}	1.76	°C/W
Channel-to-ambient thermal resistance	R _{th(ch-a)}	83.3	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 25 V, T_{ch} = 25°C (initial), L = 0.14 mH, R_G = 25 Ω , I_{AR} = 18 A

Note 3: Repetitive rating; pulse width limited by maximum channel temperature

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

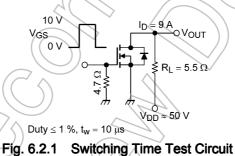
6. Electrical Characteristics

6.1. Static Characteristics (T_a = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	V_{GS} = ±20 V, V_{DS} = 0 V	_	_	±1	μA
Drain cut-off current	I _{DSS}	$V_{DS} = 100 V, V_{GS} = 0 V$	K	—	10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	100		_	V
Drain-source breakdown voltage	V _{(BR)DSX}	I _D = 10 mA, V _{GS} = -20 V	65)2	—	
Gate threshold voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0		4.0	
Drain-source on-resistance	R _{DS(ON)}	$V_{GS} = 10 \text{ V}, I_D = 9 \text{ A}$	(\rightarrow)	33	42	mΩ
Forward transfer admittance	Y _{fs}	V _{DS} = 10 V, I _D = 9 A	_14	28	—	S

6.2. Dynamic Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	-2	1580	> - <	pF
Reverse transfer capacitance	C _{rss}		-(135		
Output capacitance	C _{oss}		Ĺ	200		
Switching time (rise time)	t _r	See Figure 6.2.1.	$\overline{\mathcal{A}}$	9		ns
Switching time (turn-on time)	t _{on}	$\mathcal{A}(\mathbb{N})$ (0	$\langle \rangle$	21		
Switching time (fall time)	t _f			7	_	
Switching time (turn-off time)	t _{off}		<u>) – (</u>	30	_	



6.3. Gate Charge Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

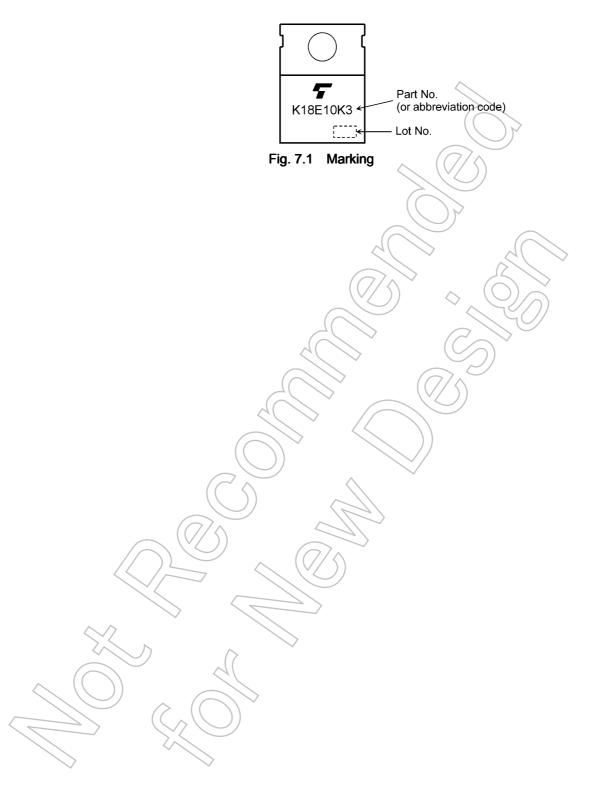
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 80 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 18 \text{ A}$	_	33	—	nC
Gate-source charge	Q _{gs}		_	20	_	
Gate-drain charge	Qgd			13	_	

6.4. Source-Drain Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

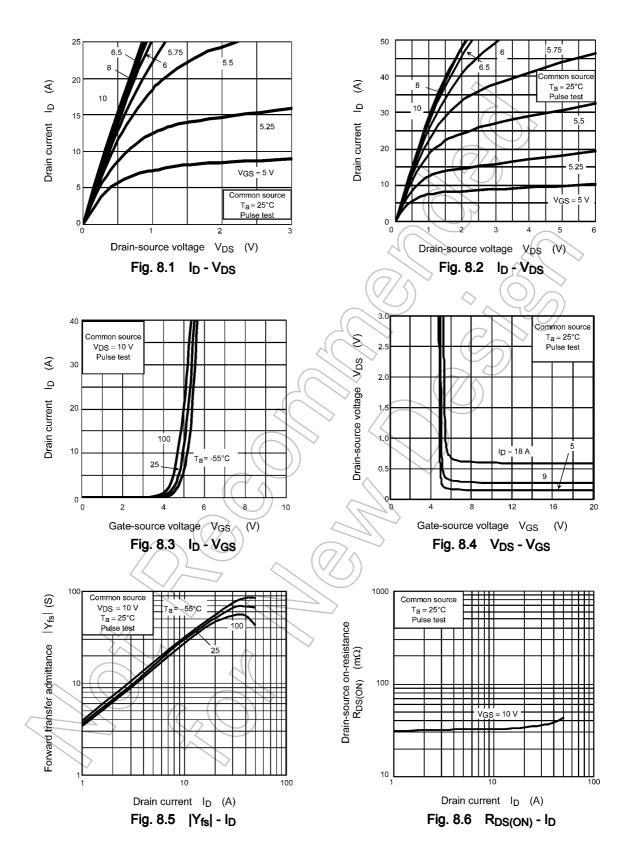
Characteristics	\searrow	Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 4)	I _{DR}	—	_	_	18	A
Reverse drain current (pulsed)	(Note 4)	I _{DRP}	—	_	_	54	
Diode forward voltage		V _{DSF}	I _{DR} = 18 A, V _{GS} = 0 V	_	—	-1.2	V
Reverse recovery time		t _{rr}	I _{DR} = 18 A, V _{GS} = 0 V	_	53	_	ns
Reverse recovery charge		Q _{rr}	-dI _{DR} /dt = 50 A/μs	_	58	_	nC

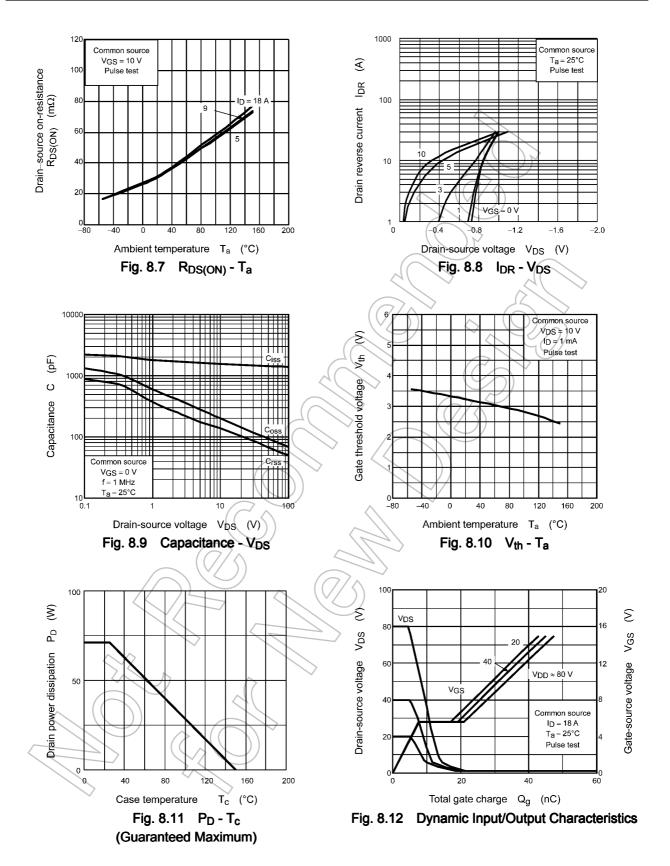
Note 4: Ensure that the channel temperature does not exceed 150°C.

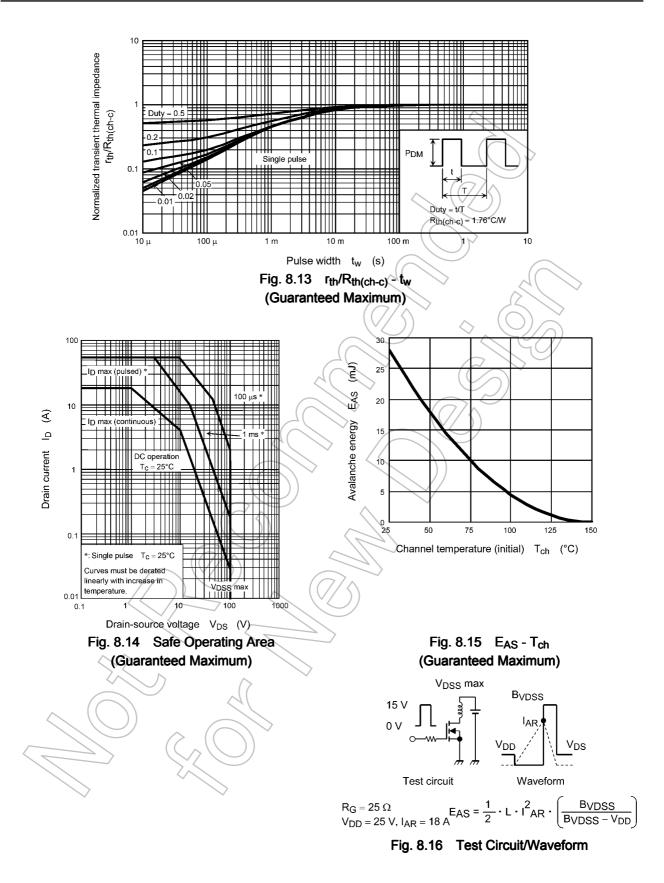
7. Marking



8. Characteristics Curves (Note)





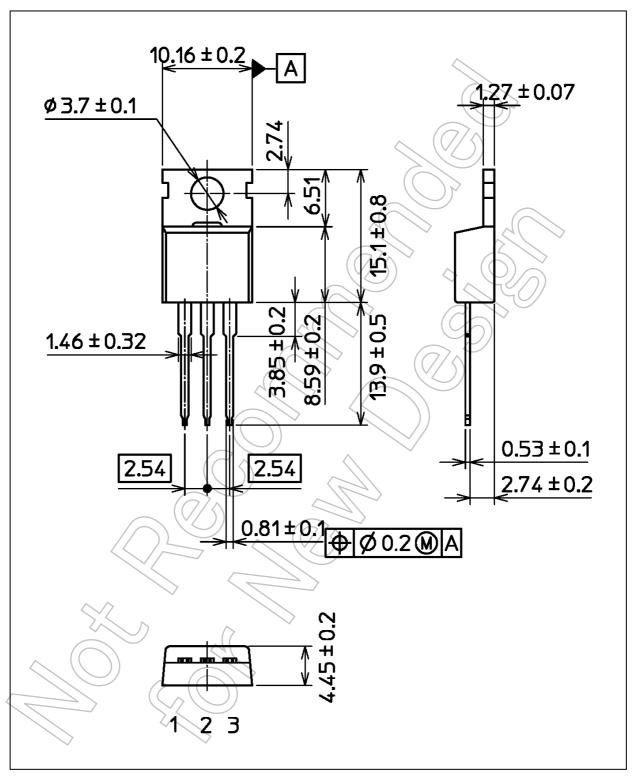


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Package Dimensions

TK18E10K3

Unit: mm



Weight: 1.93 g (typ.)

	Package Name(s)	
TOSHIBA: 2-10X1A		
Nickname: TO-220		

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