TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSVII)

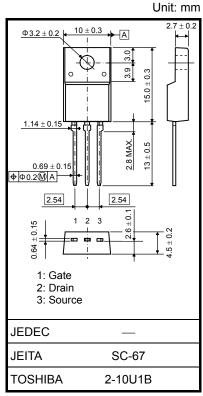
TK13A50D

Switching Regulator Applications

- Low drain-source ON resistance: $RDS(ON) = 0.31 \Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 7.5 \text{ S}$ (typ.)
- Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 500 \ V)$
- Enhancement-mode: $V_{th} = 2.0$ to 4.0 V ($V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	500	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	۱ _D	13	А	
	Pulse (Note 1)	I _{DP}	52	~	
Drain power dissipation	on (Tc = 25°C)	PD	45	W	
Single pulse avalanche energy (Note 2)		E _{AS}	390	mJ	
Avalanche current		I _{AR}	13	A	
Repetitive avalanche	energy (Note 3)	E _{AR}	4.5	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

Absolute Maximum Ratings (Ta = 25°C)



Weight : 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

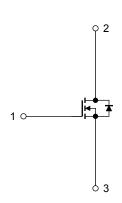
Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to case	R _{th (ch-c)}	2.78	°C/W	
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W	

Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 3.92 mH, $R_G = 25 \Omega$, $I_{AR} = 13 \text{ A}$ Note 3: Repetitive rating: pulse width limited by maximum channel temperature This transistor is an electrostatic sensitive device. Please handle with caution.

Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Internal Connection



Start of commercial production 2009-01

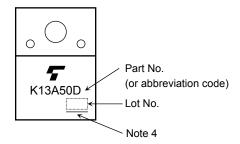
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS}=\pm 30~V,~V_{DS}=0~V$	_	—	±1	μA
Drain cut-off current		I _{DSS}	$V_{DS} = 500 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	_		10	μA
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	500			V
Gate threshold v	oltage	V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.0		4.0	V
Drain-source ON	resistance	R _{DS (ON)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 6.5 \text{ A}$		0.31	0.4	Ω
Forward transfer	admittance	Y _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 6.5 \text{ A}$	1.9	7.5	_	S
Input capacitance		C _{iss}		_	1800	_	pF
Reverse transfer capacitance		C _{rss}	$V_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$	_	9	_	
Output capacitance		C _{oss}			190		
Switching time	Rise time	tr	$\begin{array}{c} 10 \text{ V} \\ \text{V}_{GS} \\ 0 \text{ V} \\ 50 \Omega \end{array} \begin{array}{c} \text{I}_{D} = 6.5 \text{ A} \\ \text{V}_{OUT} \\ \text{V}_{OUT} \\ \text{V}_{DD} \approx 200 \text{ V} \\ \text{Duty} \leq 1\%, t_{W} = 10 \ \mu\text{s} \end{array}$		40		- ns
	Turn-on time	t _{on}			80		
	Fall time	t _f			15		
	Turn-off time	t _{off}			110	_	
Total gate charge		Qg		_	38		
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 13 \text{ A}$		24		nC
Gate-drain charge		Q _{gd}]	_	14	—	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	13	А
Pulse drain reverse current (Note 1)	I _{DRP}	—			52	А
Forward voltage (diode)	V _{DSF}	$I_{DR} = 13 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.7	V
Reverse recovery time	t _{rr}	$I_{DR} = 13 \text{ A}, V_{GS} = 0 \text{ V},$	_	1200	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 100 A/μs		13	_	μC

Marking



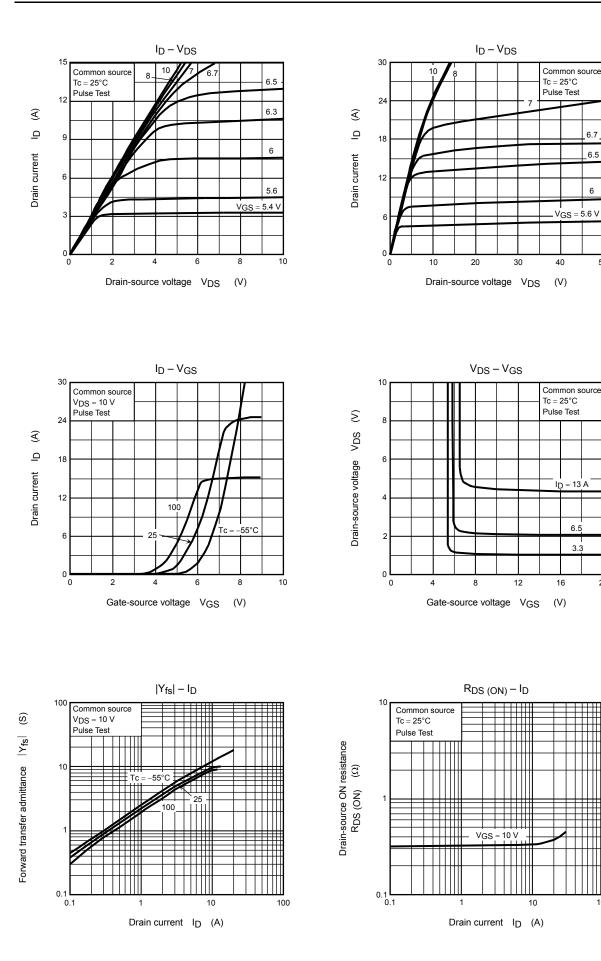
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

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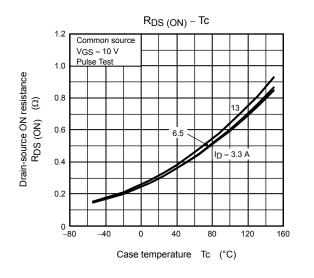
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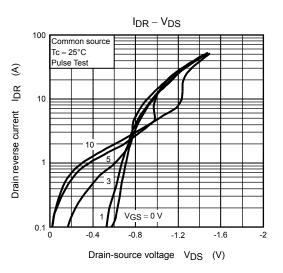
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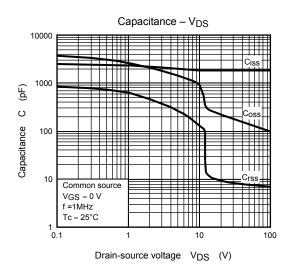


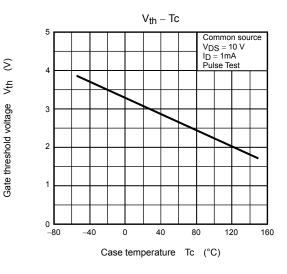
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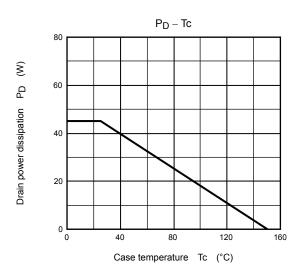
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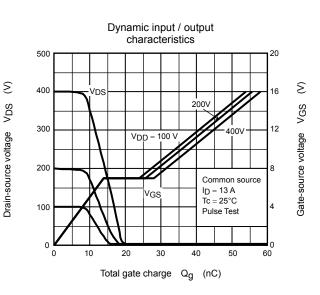


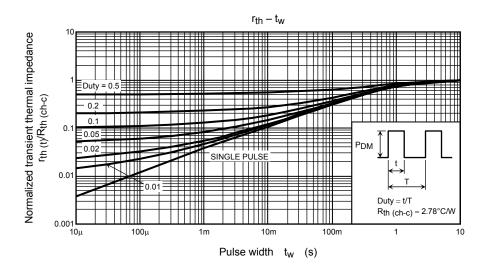


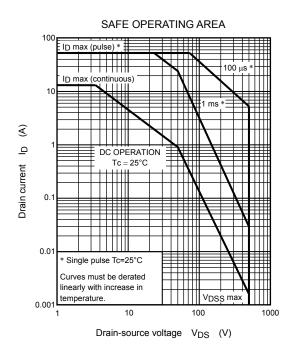


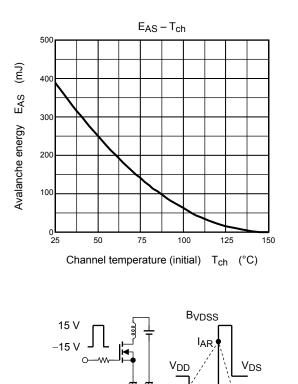












$$\begin{array}{cc} \text{Test circuit} & \text{Wave form} \\ \\ \text{R}_{G} = 25 \ \Omega \\ \text{V}_{DD} = 90 \ \text{V}, \ \text{L} = 3.92 \ \text{mH} \end{array} \qquad \text{E}_{AS} = \frac{1}{2} \cdot \text{L} \cdot \text{I}^{2} \cdot \left(\frac{\text{B}_{VDSS}}{\text{B}_{VDSS} - \text{V}_{DD}} \right) \end{array}$$

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