TOSHIBA CMOS Linear Integrated Circuit Silicon Monolithic

TCR4DG series

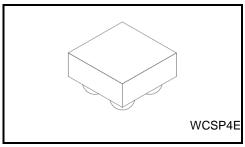
420 mA CMOS Low Drop-Out Regulator with inrush current protection circuit

The TCR4DG series are CMOS general-purpose single-output voltage regulators with an on/off control input, featuring low dropout voltage, low output noise voltage and low inrush current.

These voltage regulators are available in fixed output voltages between 1.0 V and 4.5 V and capable of driving up to 420 mA.

They feature over-current protection, over-temperature protection, Inrush current protection circuit and Auto-discharge function.

The TCR4DG series are offered in the ultra small plastic mold package WCSP4E (0.645 mm x 0.645 mm; t 0.43 mm (max)). It has a low dropout voltage of 193 mV (3.3 V output, I_{OUT} = 420 mA) with low output noise voltage of 38 μV_{rms} (2.5 V output) and a load



Weight: 0.34 mg (typ.)

transient response of only \triangle VouT = \pm 115 mV (louT = 1 mA \Leftrightarrow 420 mA, CouT =1.0 μ F).

As small ceramic input and output capacitors can be used with the TCR4DG series, these devices are ideal for portable applications that require high-density board assembly such as cellular phones.

Features

- Low Drop-Out voltage
 - VDO = 193 mV (typ.) at 3.3 V-output, IOUT = 420 mA
- · Low output noise voltage
 - V_{NO} = 38 μV_{rms} (typ.) at 2.5 V-output, I_{OUT} = 10 mA, 10 Hz $\leq f \leq$ 100 kHz
- Fast load transient response ($\triangle V_{OUT} = \pm 115 \text{ mV}$ (typ.) at $I_{OUT} = 1 \Leftrightarrow 420 \text{ mA}$, $C_{OUT} = 1.0 \mu\text{F}$)
- High ripple rejection (R.R = 70 dB (typ.) at 2.5 V-output, I_{OUT} = 10 mA, f = 1 kHz)
- · Over-current protection
- Over-temperature protection
- Inrush current protection circuit
- Auto-discharge function
- Pull down connection between CONTROL and GND
- Ceramic capacitors can be used ($C_{IN} = 1.0 \mu F$, $C_{OUT} = 1.0 \mu F$)
- Ultra small package WCSP4E (0.645 mm x 0.645 mm; t 0.43 mm (max))



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Input voltage	VIN	6.0	V
Control voltage	Vст	-0.3 to 6.0	V
Output current	lout	420	mA
Power dissipation	PD	800 (Note1)	mW
Operation temperature range	Topr	-40 to 85	°C
Junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	-55 to 150	°C

Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: Rating at mounting on a board

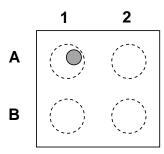
Board material: Glass epoxy(FR4)

Board dimension: 40 mm x 40 mm (both sides of board), t = 1.6 mm

Metal pattern ratio: a surface approximately 50%, the reverse side approximately 50%

Through hole: diameter 0.5 mm x 24

Pin Assignment (Top view)



	1	2
Α	VIN	VOUT
В	CONTROL GN	



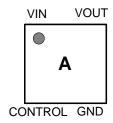
List of Products Number, Output voltage and Marking

Product No.	Output voltage (V)(typ.)	Marking	Product No.	Output voltage (V)(typ.)	Marking
TCR4DG10	1.0	Α	TCR4DG25	2.5	Т
TCR4DG105	1.05	В	TCR4DG27	2.7	U
TCR4DG11	1.1	С	TCR4DG275	2.75	V
TCR4DG115	1.15	D	TCR4DG28	2.8	W
TCR4DG12	1.2	E	TCR4DG285	2.85	Х
TCR4DG125	1.25	F	TCR4DG29	2.9	Y
TCR4DG13	1.3	Н	TCR4DG30	3.0	0
TCR4DG135	1.35	J	TCR4DG31	3.1	1
TCR4DG15	1.5	К	TCR4DG32	3.2	2
TCR4DG175	1.75	L	TCR4DG325	3.25	3
TCR4DG18	1.8	M	TCR4DG33	3.3	4
TCR4DG185	1.85	N	TCR4DG35	3.5	5
TCR4DG19	1.9	Р	TCR4DG36	3.6	6
TCR4DG23	2.3	R	TCR4DG42	4.2	7
TCR4DG245	2.45	S	TCR4DG45	4.5	8

Note: Please ask your local retailer about the devices with other output voltages.

Top Marking (top view)

Example: TCR4DG10 (1.0 V output)





Electrical Characteristics

(Unless otherwise specified, $V_{IN} = V_{OUT} + 1 \text{ V}$, $I_{OUT} = 50 \text{ mA}$, $C_{IN} = 1.0 \mu\text{F}$, $C_{OUT} = 1.0 \mu\text{F}$, $T_j = 25 ^{\circ}\text{C}$)

Characteristics	Symbol	Test Condition		Min	Тур.	Max	Unit
Outrat waltana assurant	Output voltage accuracy V _{OUT} I _O	I _{OUT} = 50 mA (Note 2)	Vout < 1.8 V	-18	_	+18	mV
Output voltage accuracy			1.8 V ≤ V _{OUT}	-1.0	_	+1.0	%
Input voltage	VIN	IOUT = 1 mA	(Note 3)	Vout + Vdo	_	5.5	V
Line regulation	Reg·line	$V_{OUT} + 0.5 \text{ V} \leq V_{IN} \leq 5.5 \text{ V},$ $I_{OUT} = 1 \text{ mA}$		_	1	15	mV
Load regulation	Reg·load	1 mA ≤ I _{OUT} ≤ 420 mA		_	6	35	mV
		IOUT = 0 mA	V _{OUT} = 1.0 V	_	65	_	μΑ
Ouissest surrent	la.		Vout = 1.8 V	_	65	_	
Quiescent current	lΒ		Vout = 2.5 V	_	68	_	
			Vout = 4.5 V	_	78	125	
Stand-by current	I _B (OFF)	V _{CT} = 0 V		_	0.1	1	μΑ
Drop-out voltage	V _{DO}	I _{OUT} = 420 mA (Note 4)		_	193	263	mV
Temperature coefficient	Tcvo	-40°C ≤ T _{opr} ≤ 85°C		_	75	_	ppm/°C
Output noise voltage	VNO	$V_{IN} = V_{OUT} + 1 V$, $I_{OUT} = 10 m$ 10 Hz \leq f \leq 100 kHz, Ta = 25°0	_	38	_	μVrms	
Ripple rejection ratio	R.R.	V_{IN} = V_{OUT} + 1 V, I_{OUT} = 10 m f = 1 kHz, V_{Ripple} = 500 mV _{p-p} , Ta = 25°C	_	70	_	dB	
Load transient response	⊿Vouτ	I _{OUT} = 1 ⇔ 420 mA, C _{OUT} = 1	_	±115	_	mV	
Control voltage (ON)	VCT (ON)	_		1.0	_	5.5	V
Control voltage (OFF)	VCT (OFF)	_		0	_	0.4	V

- Note 2: Stable state with fixed I_{OUT} condition.
- Note 3: Please refer to Dropout Voltage table (Page 4), and use it within the limits of absolute maximum rating Topr and Tj.
- Note 4: The 3.3 V output product. Dropout voltage is the voltage difference between the V_{IN} and V_{OUT} at which V_{OUT} drops to 2% below its nominal value.
- Note 5: The 2.5 V output product.

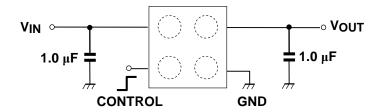
Drop-out voltage

$(I_{OUT} = 420 \text{ mA}, C_{IN} = 1.0 \mu\text{F}, C_{OUT} = 1.0 \mu\text{F}, T_j = 25^{\circ}\text{C})$

Output voltages	Symbol	Min	Тур.	Max	Unit
1.0 V, 1.05 V		_	767	991	
1.1 V, 1.15V		_	711	851	
1.2 V, 1.25V		_	627	781	
1.3 V		_	571	711	
1.35 V		_	515	683	
1.4 V		_	487	669	
1.5 V ≤ V _{OUT} < 1.8 V	VDO	_	431	571	mV
1.8 V ≤ V _{OUT} < 2.1 V		_	319	473	
2.1 V ≤ V _{OUT} < 2.5 V		_	277	403	
2.5 V ≤ V _{OUT} < 2.8 V		_	235	347	
2.8 V ≤ V _{OUT} < 3.2 V		_	221	291	
3.2 V ≤ V _{OUT} < 3.6 V		_	193	263	
3.6 V ≤ V _{OUT} ≤ 4.5 V		_	151	221	

Application Note

1. Recommended Application Circuit



CONTROL voltage	Output voltage
HIGH	ON
LOW	OFF
OPEN	OFF

The figure above shows the recommended configuration for using a Low-Dropout regulator. Insert a capacitor at VOUT and VIN pins for stable input/output operation. (Ceramic capacitors can be used).

2. Power Dissipation

Board-mounted power dissipation ratings for TCR4DG series are available in the Absolute Maximum Ratings table. Power dissipation is measured on the board condition shown below.

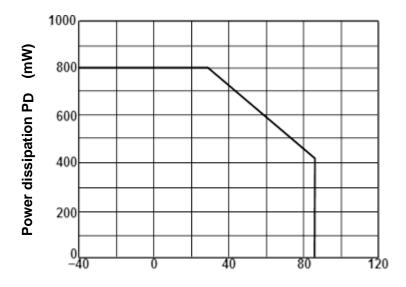
[The Board Condition]

Board material: Glass epoxy(FR4)

Board dimension: 40mm x 40mm (both sides of board), t= 1.6mm

Metal pattern ratio: a surface approximately 50%, the reverse side approximately 50%

Through hole hall: diameter 0.5mm x 24



Ambient temperature Ta (°C)

Attention in Use

Output Capacitors

Ceramic capacitors can be used for these devices. However, because of the type of the capacitors, there might be unexpected thermal features. Please consider application condition for selecting capacitors. And Toshiba recommend the ESR of ceramic capacitor is under 10 Ω .

Mounting

The long distance between IC and output capacitor might affect phase assurance by impedance in wire and inductor. For stable power supply, output capacitor need to mount near IC as much as possible. Also VIN and GND pattern need to be large and make the wire impedance small as possible.

Permissible Loss

Please have enough design patterns for expected maximum permissible loss. And under consideration of surrounding temperature, input voltage, and output current etc, we recommend proper dissipation ratings for maximum permissible loss; in general maximum dissipation rating is 70 to 80 percent.

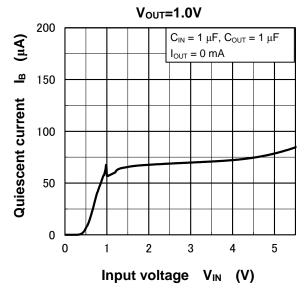
Over current Protection and Thermal shut down function

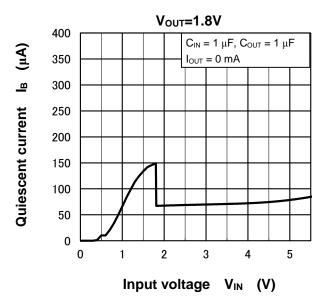
Over current protection and Thermal shut down function are designed in these products, but these are not designed to constantly ensure the suppression of the device within operation limits. Depending on the condition during actual usage, it could affect the electrical characteristic specification and reliability. Also note that if output pins and GND pins are not completely shorted out, these products might be break down.

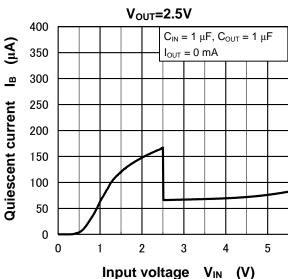
When using these products, please read through and understand the concept of dissipation for absolute maximum ratings from the above mention or our 'Semiconductor Reliability Handbook'. Then use these products under absolute maximum ratings in any condition. Furthermore, Toshiba recommend inserting failsafe system into the design.

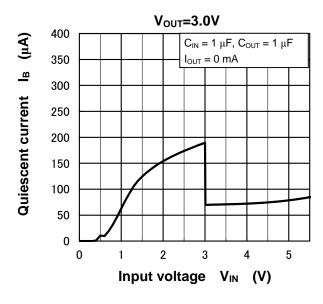
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Representative Typical Characteristics Quiescent Current vs. Input Voltage

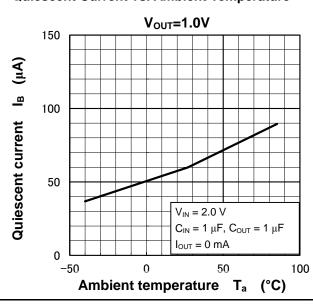


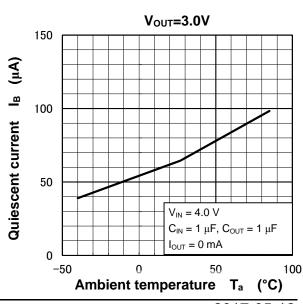




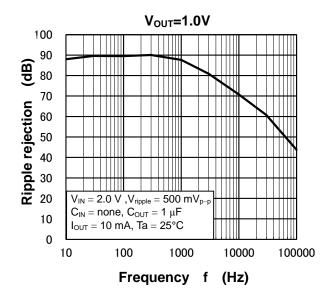


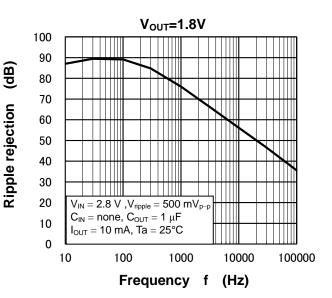
Quiescent Current vs. Ambient Temperature

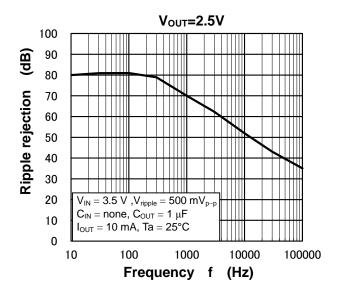


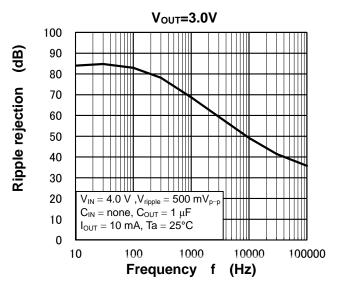


Ripple Rejection Ratio vs. Frequency

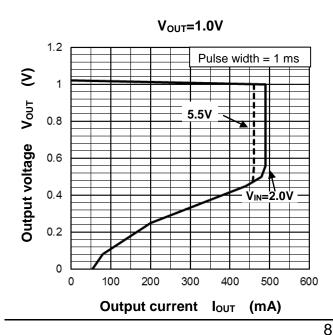


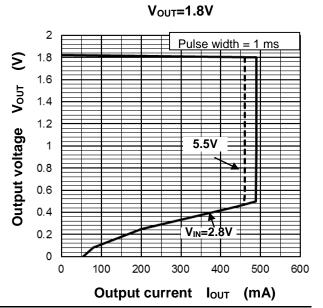


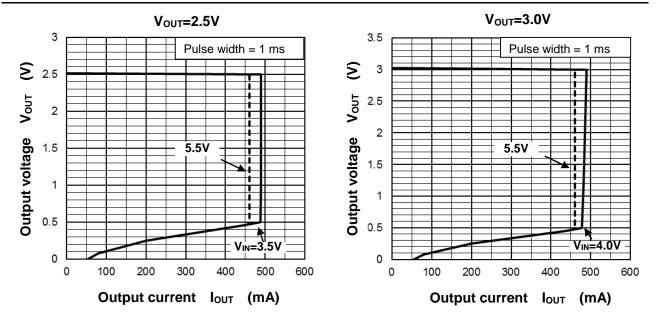




Output Voltage vs. Output Current





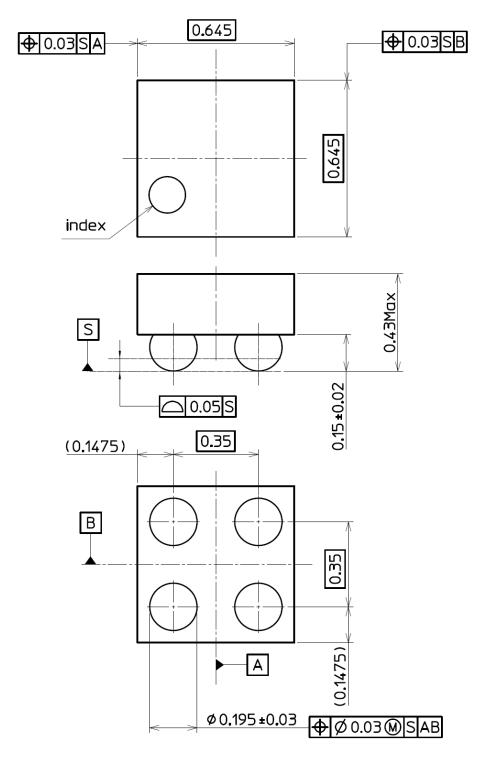


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

WCSP4E Unit: mm



Weight: 0.34 mg (typ.)

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