TOSHIBA CMOS Linear Integrated Circuit Silicon Monolithic

TCR3DG series

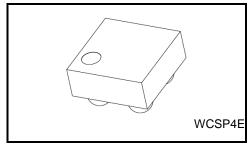
300 mA CMOS Low Drop-Out Regulator with inrush current protection circuit

The TCR3DG series are CMOS general-purpose single-output voltage regulators with an on/off control input, featuring low dropout voltage, low output noise voltage and low inrush current.

These voltage regulators are available in fixed output voltages between 1.0 V and 4.5 V and capable of driving up to 300 mA.

They feature over-current protection, thermal shut down function, Inrush current protection circuit and Auto-discharge function.

The TCR3DG series are offered in the ultra small package WCSP4E(0.645mm x 0.645mm; t 0.40mm). It has a low dropout voltage of 160 mV (3.2 V output, I_{OUT} = 300 mA) with low output noise voltage of 38 μ V_{rms} (2.5 V output) and a load transient



Weight: 0.34 mg (Typ.)

response of only $\angle V_{OUT} = \pm 80 \text{ mV}$ ($I_{OUT} = 1 \text{ mA} \Leftrightarrow 300 \text{ mA}$, $C_{OUT} = 1.0 \mu F$).

As small ceramic input and output capacitors can be used with the TCR3DG series, these devices are ideal for portable applications that require high-density board assembly such as cellular phones.

Features

Low Drop-Out voltage

VIN-VOUT = 160 mV (Typ.) at 3.2 V-output, IOUT = 300 mA

Low output noise voltage

 V_{NO} = 38 μV_{rms} (Typ.) at 2.5 V-output, I_{OUT} = 10 mA, 10 Hz \leq f \leq 100 kHz

- Fast load transient response ($\angle V_{OUT} = \pm 80 \text{ mV}$ (Typ.) at I_{OUT} = 1 \Leftrightarrow 300 mA, C_{OUT} =1.0 µF)
- High ripple rejection (R.R = 70 dB (Typ.) at 2.5V-output, I_{OUT} = 10 mA, f =1kHz)
- Over current protection
- Thermal shut down function
- Inrush current protection circuit
- Auto-discharge function
- Pull down connection between CONTROL and GND
- Ceramic capacitors can be used (C_{IN} = 1.0 $\mu F,$ C_{OUT} =1.0 μF)
- Ultra small package WCSP4E (0.645 mm x 0.645 mm ; t 0.40 mm)

Absolute Maximum Ratings (Ta = 25°C)

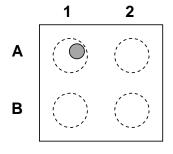
Characteristics	Symbol	Rating	Unit
Input voltage	Vin	6.0	V
Control voltage	Vст	-0.3 to 6.0	V
Output voltage	Vout	-0.3 to V _{IN} + 0.3	V
Output current	IOUT	300	mA
Power dissipation	PD	800 (Note1)	mW
Operation temperature range	Topr	-40 to 85	°C
Junction temperature	Тј	150	°C
Storage temperature range	T _{stg}	-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: Rating at mounting on a board Board material: Glass epoxy(FR4) Board dimension: 40mm x 40mm (both sides of board), t= 1.6mm Metal pattern ratio: a surface approximately 50%, the reverse side approximately 50% Through hole: diameter 0.5mm x 24

Pin Assignment (Top view)



	1	2
Α	VIN	VOUT
В	CONTROL	GND

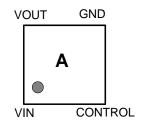
List of Products Number, Output voltage and Marking

Product No.	Output voltage (V)(Typ.)	Marking	Product No.	Output voltage (V)(Typ.)	Marking
TCR3DG10	1.0	E	TCR3DG285*	2.85	R
TCR3DG11*	1.1	F	TCR3DG30*	3.0	Т
TCR3DG12	1.2	Н	TCR3DG31*	3.1	U
TCR3DG13*	1.3	J	TCR3DG32	3.2	A
TCR3DG135*	1.35	К	TCR3DG33	3.3	В
TCR3DG18*	1.8	L	TCR3DG35*	3.5	V
TCR3DG25*	2.5	Р	TCR3DG36*	3.6	С
TCR3DG28*	2.8	W	TCR3DG45*	4.5	D

Please ask your local retailer about the devices with (*) or other output voltages.

Top Marking (top view)

Example: TCR3DG32 (3.2 V output)



Electrical Characteristics

(Unless otherwise specified, $V_{IN} = V_{OUT} + 1$ V, $I_{OUT} = 50$ mA, $C_{IN} = 1.0$ µF, $C_{OUT} = 1.0$ µF, $Ta = 25^{\circ}C$)

Characteristics	Symbol	Test Condition		Min	Тур.	Max	Unit	
Output voltage accuracy	Vout	I _{OUT} = 50 mA (Note 2)	Vout <1.8 V	-18	—	+18	mV	
	V001		1.8V ≤ Vout	-1.0	—	+1.0	%	
Input voltage	VIN	$I_{OUT} = 300 \text{ mA}$		1.75	—	5.5	V	
Line regulation	Reg·line	$ \begin{array}{l} V_{OUT} + 0.5 \ V \ \leq \ V_{IN} \ \leq \ 5.5 \ V, \\ I_{OUT} = 1 \ mA \end{array} $		_	1	15	mV	
Load regulation	Reg·load	$1 \text{ mA} \leq I_{OUT} \leq 300 \text{ mA}$		—	8	35	mV	
			$V_{OUT} = 1.0V$	_	65	—		
Quiescent current		I _{OUT} = 0 mA	$V_{OUT} = 1.8V$	_	65	—	μA	
	IB		$V_{OUT} = 2.5V$	—	68	—	— µA	
			$V_{OUT} = 4.5V$	_	78	125]	
Stand-by current	IB (OFF)	Vct = 0 V		_	0.1	1	μA	
Drop-out voltage	VIN-VOUT	I _{OUT} = 300 mA (Note 3)		_	195	275	mV	
Temperature coefficient	T _{CVO}	$-40^{\circ}C \leq T_{opr} \leq 85^{\circ}C$		_	75	—	ppm/°C	
Output noise voltage	V _{NO}	$ \begin{array}{l} V_{IN}=V_{OUT}+1~V,~I_{OUT}=10~\text{mA},\\ 10~\text{Hz}~\leq~f~\leq~100~\text{kHz},~\text{Ta}=25^\circ\text{C} \end{array} \tag{Note 3} \end{array} $		_	38	_	μV _{rms}	
Ripple rejection ratio	R.R.	$ \begin{array}{l} V_{IN}=V_{OUT}+1~V,~I_{OUT}=10~mA,\\ f=1~kHz,~V_{Ripple}=500~mV_{p\text{-}p},\\ Ta=25^{\circ}C \end{array} \tag{Note 3} $		_	70	_	dB	
Load transient response	⊿∨о∪т	I _{OUT} = 1⇔300mA, C _{OUT} = 1.0 μF		-	±80	—	mV	
Control voltage (ON)	VCT (ON)	—		1.0	_	5.5	V	
Control voltage (OFF)	VCT (OFF)	—		0	_	0.4	V	

Note 2: Stable state with fixed I_{OUT} condition.

Note 3: The 2.5 V output product.

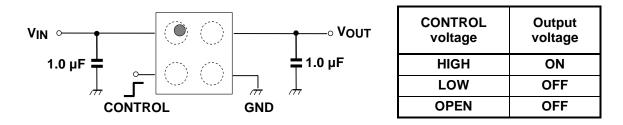
Drop-out voltage (I_{OUT} = 300 mA, C_{IN} = 1.0 µF, C_{OUT} = 1.0 µF, Ta = 25°C)

Output voltages	Symbol	Min	Тур.	Max	Unit
1.0 V, 1.05 V		—	570	750	
1.1 V		_	530	650	
1.2 V		_	470	600	
1.3 V		_	430	550	
1.35V		_	390	530	
1.4 V		_	370	520	
1.5 V ≤ V _{OUT} < 1.8 V	VIN-VOUT	_	330	450	mV
1.8 V ≤ V _{OUT} < 2.1 V		_	250	365	
2.1 V ≤ V _{OUT} < 2.5 V		_	220	315	
2.5 V ≤ V _{OUT} < 2.8 V		_	190	275	
2.8 V ≤ V _{OUT} < 3.2 V		_	180	235	
3.2 V ≤ V _{OUT} < 3.6 V	1	—	160	215	
$3.6 \text{ V} \leq \text{V}_{OUT} \leq 4.5 \text{ V}$		—	130	185	

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Application Note

1. Recommended Application Circuit



The figure above shows the recommended configuration for using a Low-Dropout regulator. Insert a capacitor at VOUT and VIN pins for stable input/output operation. (Ceramic capacitors can be used).

2. Power Dissipation

Board-mounted power dissipation ratings for TCR3DG series are available in the Absolute Maximum Ratings table. Power dissipation is measured on the board condition shown below.

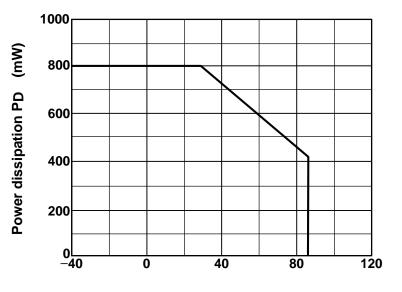
[The Board Condition]

Board material: Glass epoxy(FR4)

Board dimension: 40mm x 40mm (both sides of board), t= 1.6mm

Metal pattern ratio: a surface approximately 50%, the reverse side approximately 50%

Through hole: diameter 0.5mm x 24



Ambient temperature Ta (°C)

Attention in Use

Output Capacitors

Ceramic capacitors can be used for these devices. However, because of the type of the capacitors, there might be unexpected thermal features. Please consider application condition for selecting capacitors. And Toshiba recommend the ESR of ceramic capacitor is under 10 Ω . For stable operation, please use over 1.0µF.

Mounting

The long distance between IC and output capacitor might affect phase assurance by impedance in wire and inductor. For stable power supply, output capacitor need to mount near IC as much as possible. Also VIN and GND pattern need to be large and make the wire impedance small as possible.

Power Dissipation

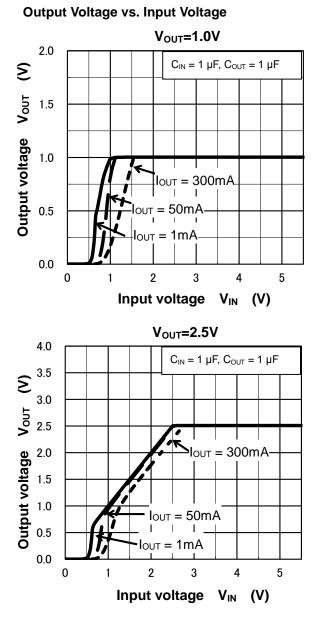
Please have enough design patterns for expected maximum power dissipation. And under consideration of surrounding temperature, input voltage, and output current etc, we recommend proper dissipation ratings for maximum power dissipation; in general maximum dissipation rating is 70 to 80 percent.

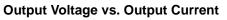
• Over current Protection and Thermal shut down function

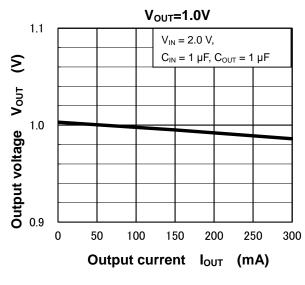
Over current protection and Thermal shut down function are designed in these products, but these are not designed to constantly ensure the suppression of the device within operation limits. Depending on the condition during actual usage, it could affect the electrical characteristic specification and reliability. Also note that if output pins and GND pins are not completely shorted out, these products might be break down.

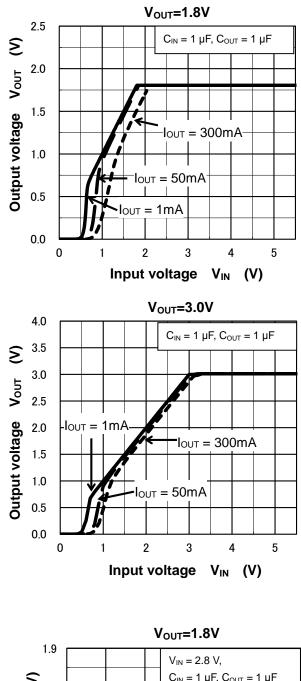
When using these products, please read through and understand the concept of dissipation for absolute maximum ratings from the above mention or our 'Semiconductor Reliability Handbook'. Then use these products under absolute maximum ratings in any condition. Furthermore, Toshiba recommend inserting failsafe system into the design.

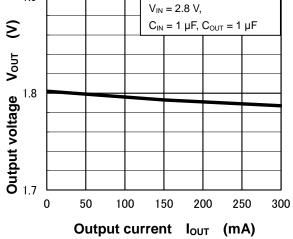
Representative Typical Characteristics



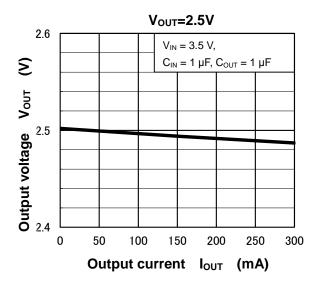


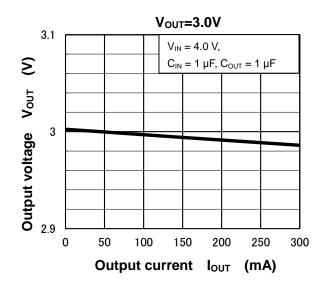




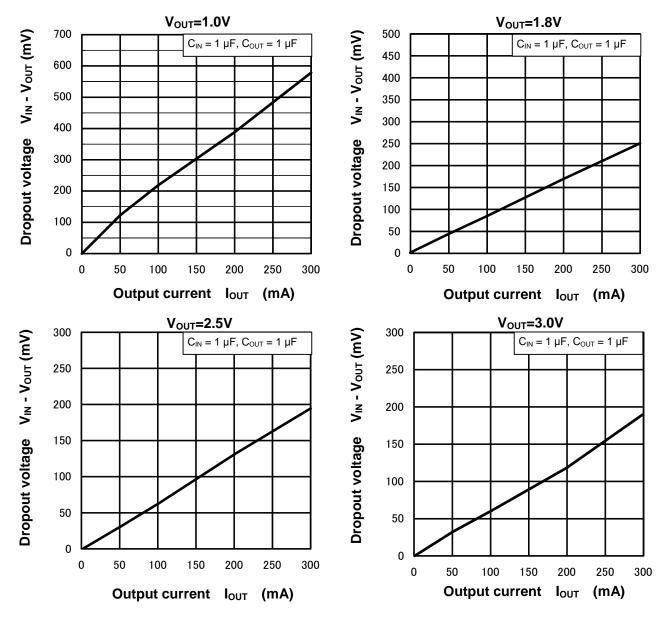


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Dropout Voltage vs. Output Current

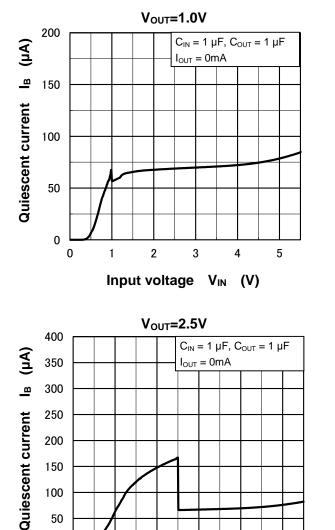


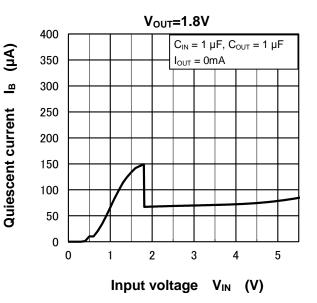
200

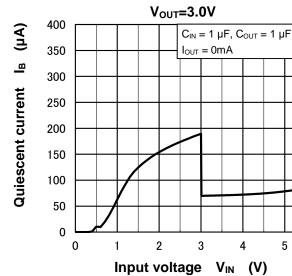
150

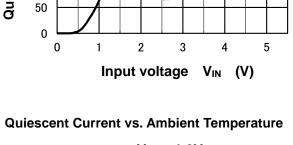
100

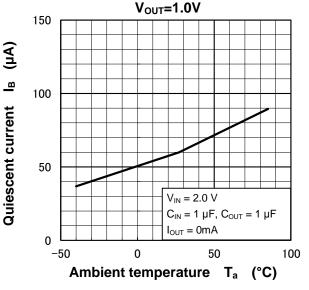
Quiescent Current vs. Input Voltage

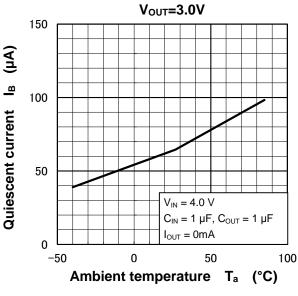




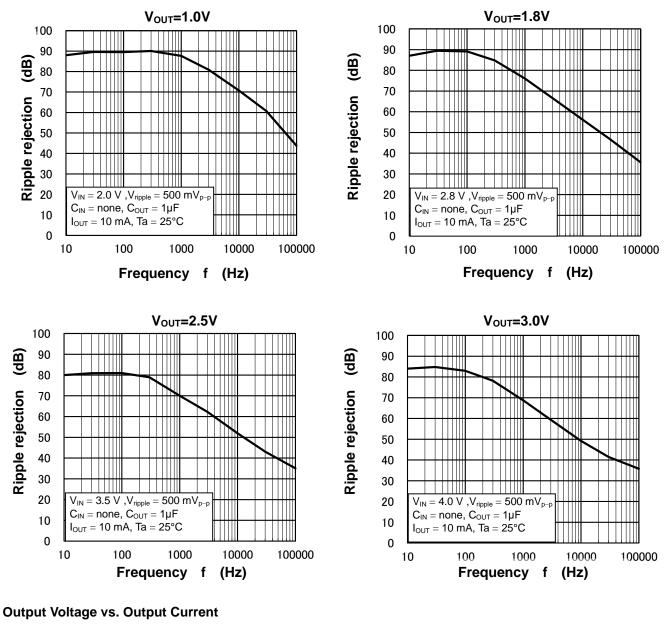


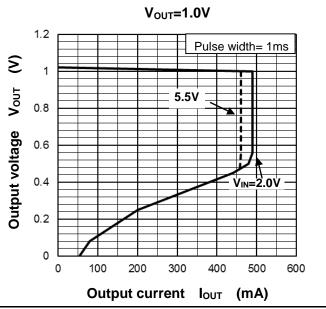




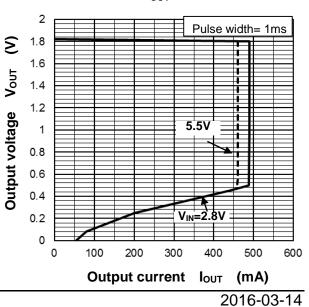


Ripple Rejection Ratio vs. Frequency

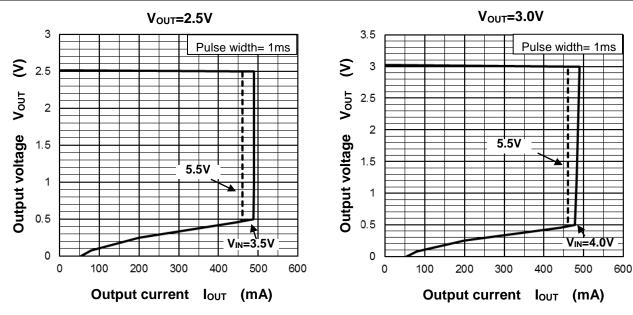




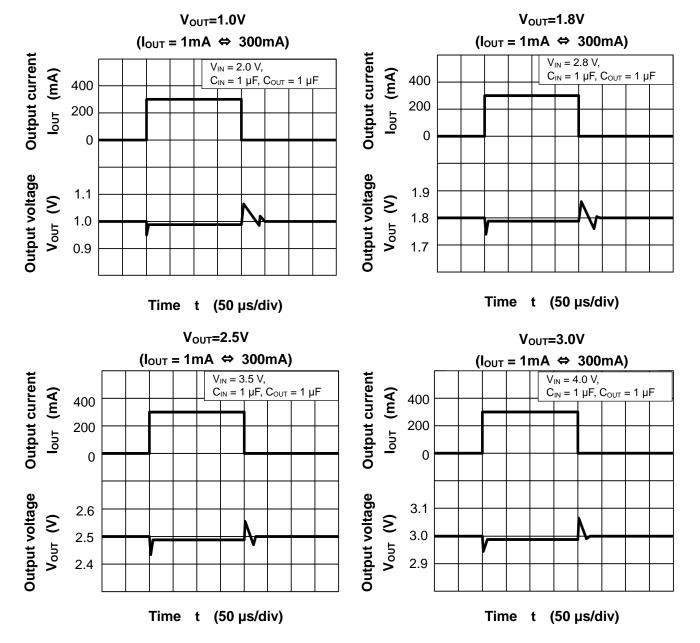
V_{оυт}=1.8V



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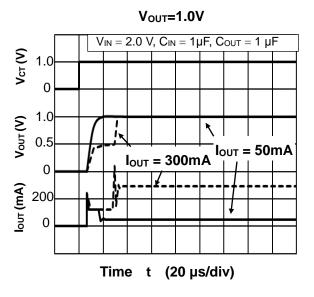


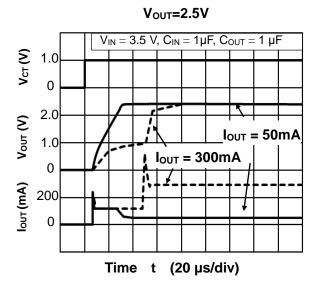




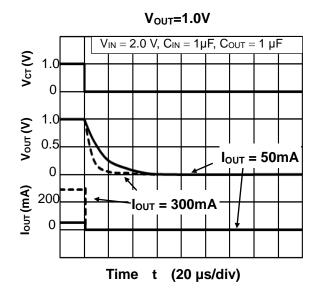
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ton Response

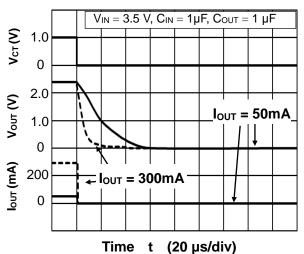




toff Response



V_{OUT}=2.5V

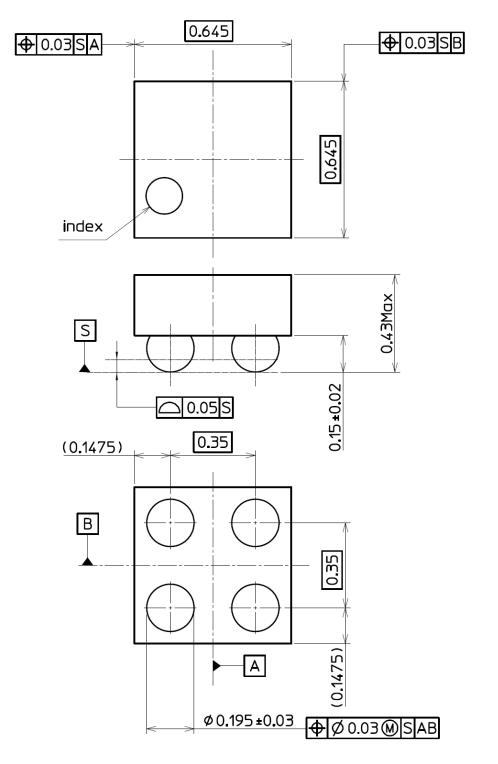


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Package Dimensions

WCSP4E

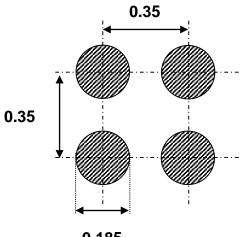
Unit: mm



Weight : 0.34mg(Typ)

Land pattern dimensions for reference only

Unit: mm



0.185

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