TOSHIBA CMOS Linear Integrated Circuit Silicon Monolithic

TCK2292xG, **TCK2297xG**

2A, 25m Ω Load Switch IC with Slew Rate Control Driver

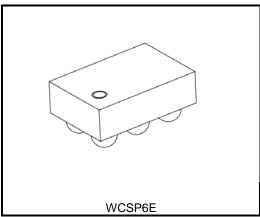
The TCK2292xG and TCK2297xG are load switch ICs for power management with slew rate control driver featuring wide input

voltage operation from 1.1 to 5.5 V. Switch ON resistance is only 25 m Ω typical at 5.0 V, -0.5 A load condition and these feature a slew rate control driver. TCK2292xG has output auto-discharge function. Output current type is available on 2 A.

These devices are available in 0.4 mm pitch ultra small package WCSP6E (0.8 mm x 1.2 mm, t: 0.55 mm) .Thus they are ideal for portable applications that require high-density board assembly such as cellular phone.

Feature

- Wide input voltage operation: V_{IN} = 1.1 to 5.5 V
- Low ON resistance:
 - $$\begin{split} & \text{R}_{\text{ON}} = 25 \text{ m}\Omega \text{ (typ.) at } \text{V}_{\text{IN}} = 5.0 \text{ V}, \text{ I}_{\text{OUT}} = -0.5 \text{ A} \\ & \text{R}_{\text{ON}} = 31 \text{ m}\Omega \text{ (typ.) at } \text{V}_{\text{IN}} = 3.3 \text{ V}, \text{ I}_{\text{OUT}} = -0.5 \text{ A} \\ & \text{R}_{\text{ON}} = 52 \text{ m}\Omega \text{ (typ.) at } \text{V}_{\text{IN}} = 1.8 \text{ V}, \text{ I}_{\text{OUT}} = -0.5 \text{ A} \\ & \text{R}_{\text{ON}} = 104 \text{ m}\Omega \text{ (typ.) at } \text{V}_{\text{IN}} = 1.2 \text{ V}, \text{ I}_{\text{OUT}} = -0.5 \text{ A} \end{split}$$
- Low Quiescent Current: I_Q = 0.1 μA (typ.) at I_{OUT} = 0 mA(TCK22921G, TCK22971G)
- Slew Rate Control circuit
- Output auto-discharge (Option)
- Reverse current blocking (V_{IN} = 0 V)
- Pull down connection between Control and GND(Option)
- Ultra small package: WCSP6E (0.8mm x 1.2mm, t: 0.55mm)



Weight: 1 mg (typ.)

Function Table

Part number	Function							
	Rise time @VIN=5V	Reverse current blocking (VIN = 0 V)	Output auto- discharge	Control pin polarity	Control pin connection	Device Marking		
TCK22921G	4.5 μs	Built in	Built in	Active High	Pull down	1R		
TCK22922G	666 μs	Built in	Built in	Active High	Pull down	2R		
TCK22923G	1364 μs	Built in	Built in	Active High	Pull down	3R		
TCK22925G	3380 μs	Built in	Built in	Active High	Pull down	4R		
TCK22971G	4.5 μs	Built in	N/A	Active High	Pull down	5R		
TCK22972G	666 μs	Built in	N/A	Active High	Pull down	6R		
TCK22973G	1364 μs	Built in	N/A	Active High	Pull down	7R		
TCK22974G	3380 μs	Built in	N/A	Active High	Pull down	8R		
TCK22975G	666 μs	Built in	N/A	Active Low	Open	9R		

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating		Unit										
Input voltage	Vin	-0.3 to 6.0		-0.3 to 6.0		-0.3 to 6.0		-0.3 to 6.0		-0.3 to 6.0		-0.3 to 6.0		V
Control voltage	V _{CT}	-0.3 to 6.0		-0.3 to 6.0		V								
Output voltage	Vout	-0.3 to 6.0		-0.3 to 6.0		V								
	Ιουτ	DC	2.0	A										
Output current		Pulse	3.0 (Note1)	А										
Power dissipation	PD		800 (Note 2)	mW										
Operating temperature range	T _{opr}	-40 to 85		°C										
Junction temeperature	Tj	150		150		°C								
Storage temperature	T _{stg}	-55 to 150		-55 to 150		-55 to 150		°C						

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: 100 µs pulse, 2% duty cycle

Note2: Rating at mounting on a board

Board material: Glass epoxy (FR4)

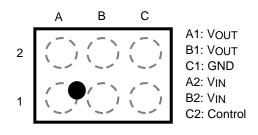
Board dimension: 40mm x 40mm (both sides of board), t=1.6mm

Metal pattern ratio: a surface approximately 50%, the reverse side approximately 50%

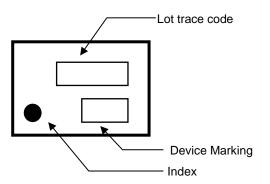
Through hole: diameter 0.5mm x 28

Characteristics	Symbol	Condition	Min	Max	Unit	
Input voltage	VIN	—	1.1	5.5	V	
Output voltage	Vout	—	_	VIN	V	
Output current	Ιουτ	1.4V < VIN	_	2.0	А	
	Maria	1.2V < V _{IN} ≤ 5.5 V	1.0	_	N	
Control High-level input voltage	VIH	$1.1V \le V_{IN} \le 1.2 V$ 0.9		_	V	
Control Low-level input voltage	VIL	—	_	0.4	V	

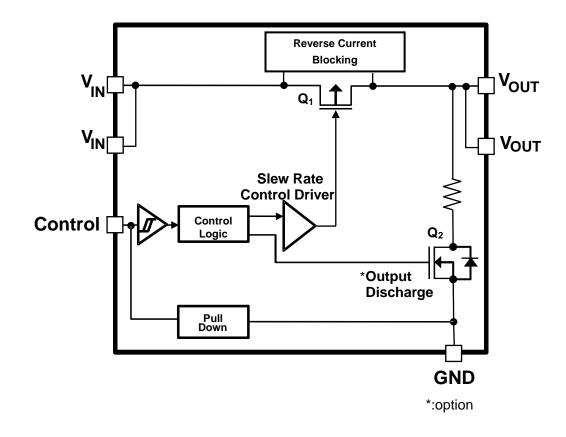
Pin Assignment(Top view)



Top marking



Block Diagram



Operation logic table (VIN = 1.1 V to 5.5 V)

		TCK22921G TCK22922G TCK22923G TCK22925G	TCK22971G TCK22972G TCK22973G TCK22974G	TCK22975G
Control	Output Q ₁	ON	ON	OFF
"High"	Discharge Q2	OFF		
Control	Output Q₁	OFF	OFF	ON
"Low"	Discharge Q2	ON	_	_

*Reverse current blocking function works when $V_{IN} = 0$ V.

Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

		Test Condition		Ta = 25°C			Ta = -40 to 85°C		
Characteristics	Symbol			Min	Тур.	Max	Min	Max	Unit
	ΙQ	IOUT = 0 mA (Note 3)	VIN = 1.8 V	_	0.1	_	—	—	μA
Quiescent current (ON state)			VIN = 3.3 V	_	0.1	_	_	_	μA
		(V _{IN} = 5.5 V — 0.1 — —	0.5	μA				
		I _{OUT} = 0 mA	V _{IN} = 1.8 V	_	1.2	_	_	_	μA
Quiescent current (ON state)	IQ		V _{IN} = 3.3 V	_	1.3	_	_	_	μA
			V _{IN} = 5.5 V		1.4		_	2.5	μA
Quiescent current (OFF state)	IQ(OFF)	V _{IN} = 5.5 V, V _{OUT} = OPEN, (Note 4)		_	0.07	_	_	0.4	μΑ
Switch leakage current(OFF state)	I _{SD(OFF)}	V_{IN} = 5.5 V, V_{OUT} = GND, current through from V_{IN} to V_{OUT} . (Note 5)		_	0.02	_	_	2	μΑ
Reverse blocking current	I _{RB}	V _{OUT} = 5.0 V, V _{IN} = 0 V		_	0.01	_	_	2	μΑ
	Ron	I _{OUT} = -0.5 A	V _{IN} = 5.0 V		25		—	43	mΩ
			V _{IN} = 3.3 V		31		—	53	
On resistance			V _{IN} = 1.8 V		52		—	83	
			V _{IN} = 1.2 V		104	—	—	185	
			V _{IN} = 1.1 V		136		—	_	
Output discharge on resistance	R _{SD}	— (Note 6)		_	100	_	—	_	Ω

Note 3: Only applies to the TCK22921G and TCK22971G

Note 4: Except OFF-state switch current

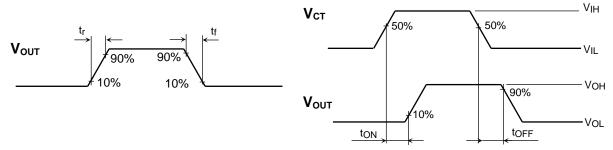
Note 5: Only applies to the TCK22971G, TCK22972G, TCK22973G, TCK22974G and TCK22975G Note 6: Only applies to the TCK22921G, TCK22922G, TCK22923G, and TCK22925G

AC Characteristics (Ta = 25°C)

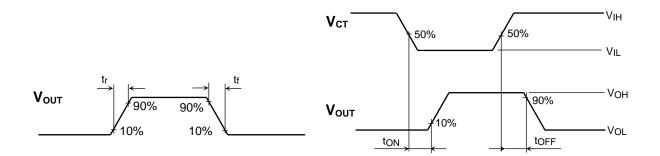
$V_{IN} = 5.0 V$

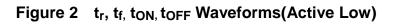
Characteristics	Symbol	Test Condition (Figure 1, Figure 2)		Min	Тур.	Max	Unit
VOUT rise time	tr	RL = 5 Ω, CL = 1.0 μF	TCK22921G TCK22971G	_	4.5	_	μs
			TCK22922G TCK22972G TCK22975G	_	666	_	
			TCK22923G TCK22973G	_	1364	_	
			TCK22925G TCK22974G	_	3380	_	
Vout fall time	tf	$R_L = 5 \ \Omega, \ C_L = 1.0 \ \mu F$		—	10	_	μS
	ton	R _L = 5 Ω, C _L = 1.0 μF	TCK22921G TCK22971G	_	3	_	
Turn on delay			TCK22922G TCK22972G TCK22975G	_	380	_	μs
			TCK22923G TCK22973G	_	750	_	
			TCK22925G TCK22974G	—	2000	_	
Turn off delay	tOFF	$R_L = 5 \Omega$, $C_L = 1.0 \mu F$		_	10	_	μS

AC Waveform





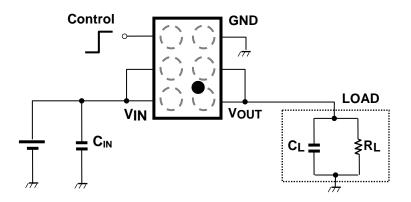




Application Note

1. Application circuit example (top view)

The figure below shows the example of configuration



1) Input capacitor

An input capacitor be sure to use CIN for the stable operation. And it is effective to reduce voltage overshoot or undershoot due to sharp changes in output current and also for improved stability of the power supply. When used, place CIN more than 1.0μ F as close to VIN pin to improve stability of the power supply.

2) Output capacitor

An output capacitor (COUT) is not necessary for the guaranteed operation. However, there is a possibility of overshoot or undershoot caused by output load transient response, board layout and parasitic components of load switch IC. In this case, an output capacitor with COUT more than 0.1μ F is recommended.

3) Control pin

The Control pin controls both the pass-through p-ch MOSFET and the discharge n-ch MOSFET (only for TCK2292xG), operated by the control voltage. Each control pin is equipped with Schmitt trigger. Also, pull down resistance equivalent to a few M Ω is connected between Control and GND, thus the load switch IC is in OFF state even when Control pin is OPEN. (except TCK22975G). A control pin for TCK22975G is Active low and does not have a pull down resistor, please use be sure to fix the potential of the Control pin to High or Low.

2. Reverse current blocking

This device has a built-in Reverse current blocking (SW OFF state) circuit to block reverse current from VOUT to VIN when output p-ch MOSEFT turned off and input voltage is 0V.

3. Instructions and directions for use

This device has built-in several functions, but these do not assure for the suppression of uprising device operation. In use of these products, please read through and understand dissipation idea for absolute maximum ratings from the above mention or our 'Semiconductor Reliability Handbook'. Then use these products under absolute maximum ratings in any condition. Furthermore, Toshiba recommends inserting failsafe system into the design.

4. Power Dissipation

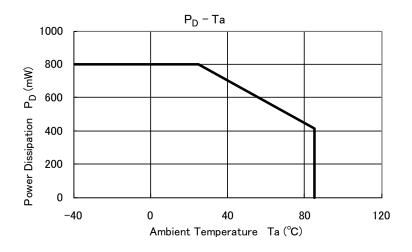
Power dissipation is measured on the board condition shown below.

[The Board Condition]

Board material: Glass epoxy (FR4)

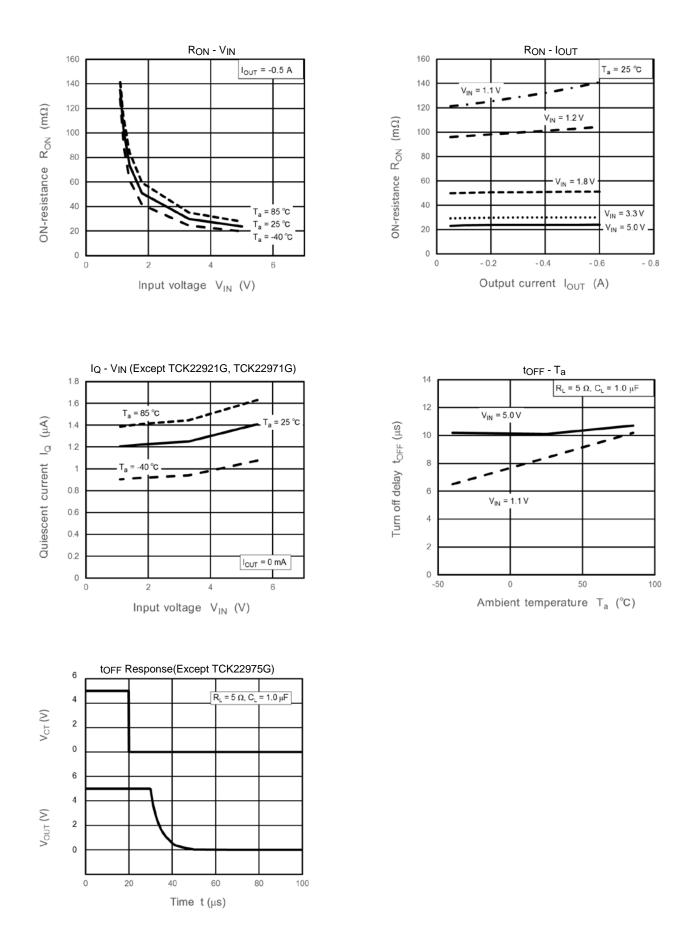
Board dimension: 40mm x 40mm (both sides of board), t=1.6mm

Metal pattern ratio: a surface approximately 50%, the reverse side approximately 50% Through hole: diameter 0.5mm x 28

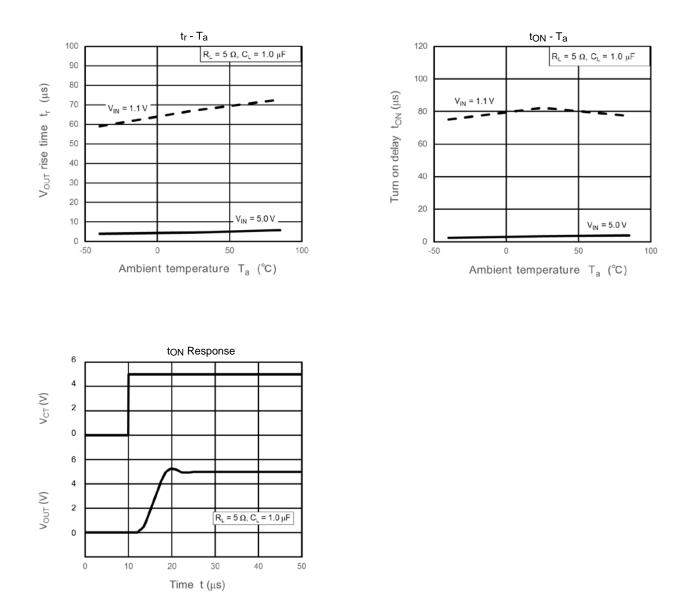


Please allow sufficient margin when designing a board pattern to fit the expected power dissipation. Also take into consideration the ambient temperature, input voltage, output current etc. and applying the appropriate derating for allowable power dissipation during operation.

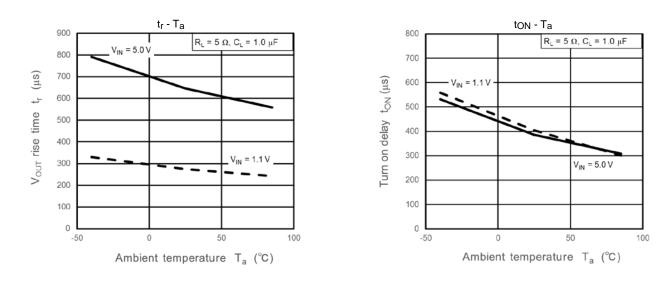
TCK2292xG, TCK2297xG Representative Typical Characteristics



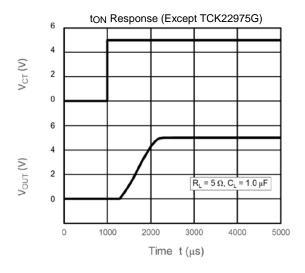
TCK22921G, TCK22971G Representative Typical Characteristics



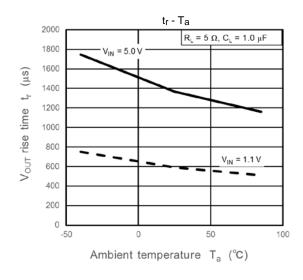
TCK22922G, TCK22972G, TCK22975G Representative Typical Characteristics

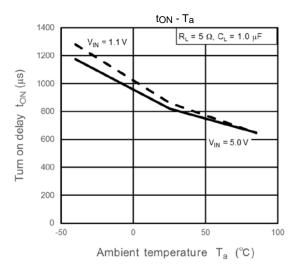


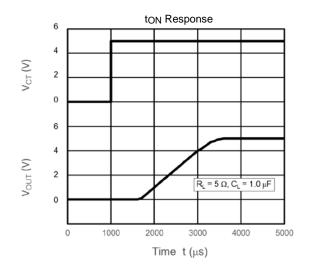




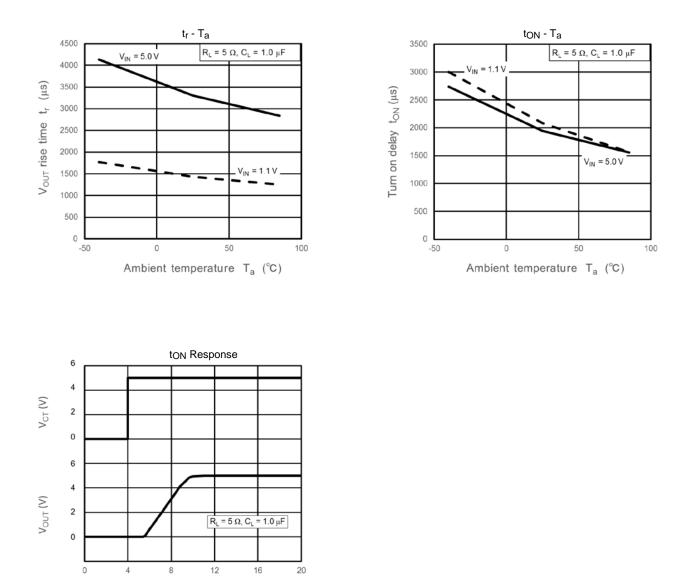
TCK22923G, TCK22973G Representative Typical Characteristics







TCK22925G, TCK22974G Representative Typical Characteristics



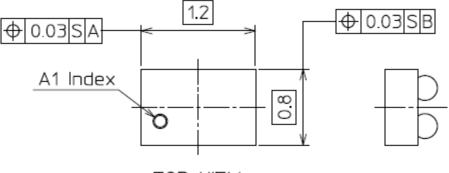
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Time t (ms)

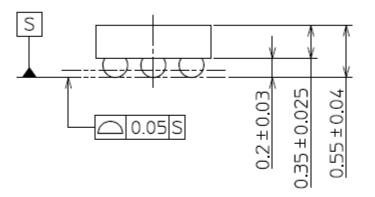


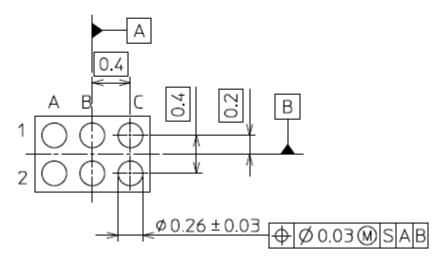
Package dimension

Unit: mm







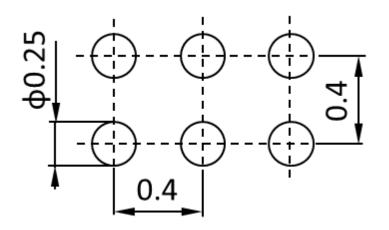


BOTTOM VIEW

Weight: 1 mg (typ.)

Land pattern dimensions (for reference only)

Unit: mm



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