

TOSHIBA CMOS Linear Integrated Circuit Silicon Monolithic

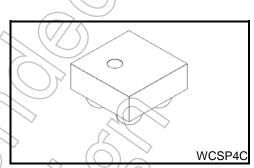
TCK206G, TCK207G, TCK208G

0.75 V, 2A Load Switch IC with Reverse Current Blocking in Ultra Small Package

The TCK206G, TCK207G and TCK208G are 0.75 V ultra low voltage load switch ICs for a power management with slew rate control driver and reverse current blocking(SW OFF state) function.

Switch ON resistance is only 18.4 m Ω (typ.) at 0.75 V, -1.5 A condition and output current is available up to 2.0 A (DC). TCK207G and TCK208G feature output auto-discharge function.

These devices are available in 0.5 mm pitch ultra small package WCSP4C (0.9 mm x 0.9 mm, t: 0.5 mm (typ.)). Thus, these devices are ideal for portable applications that require high-density board assembly such as cellular phone.



Weight: 0.9 mg (typ.)

Feature

- Low input voltage operation: V_{IN} = 0.75 to 3.6 V
- High output current: I_{OUT} (DC) = 2.0 A
- Low ON resistance :

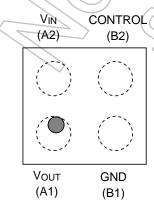
 R_{ON} = 18.1 $m\Omega$ (typ.) at V_{IN} = 3.3 V, -1.5 A

 $R_{ON} = 18.2 \text{ m}\Omega$ (typ.) at $V_{IN} = 1.2 \text{ V}$, -1.5 A

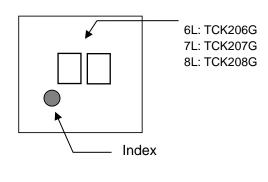
 $R_{ON} = 18.4 \text{ m}\Omega$ (typ.) at $V_{IN} = 0.75 \text{ V}$, -1.5 A

- Built in Slew rate control driver
- Built in Reverse current blocking(SW OFF state)
- Built in Auto-discharge (TCK207G and TCK208G)
- Active High and Pull down connection between CONTROL and GND (TCK206G and TCK207G)
- Active Low (TCK208G)
- Ultra small package: WCSP4C (0.9 mm x 0.9 mm, t: 0.5 mm)

Pin Assignment(Top view)



Top marking



Start of commercial production 2014-06



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol		Rating	Unit
Input voltage	VIN		-0.3 to 4.0	V
Control voltage	V _C T		-0.3 to 4.0	V
Output voltage	Vout		-0.3 to V _{IN} +0.3	
Output current	lout	DC	2.0	A
		Pulse	3.0 (Note 1)	A
Power dissipation	PD	800 (Note 2)		mW
Operating temperature range	T _{opr}	-40 to 85		°C
Junction temeperature	Tj	150		°C
Storage temperature	T _{stg}		°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note1: 100 µs pulse, 2% duty cycle

Note2: Rating at mounting on a board

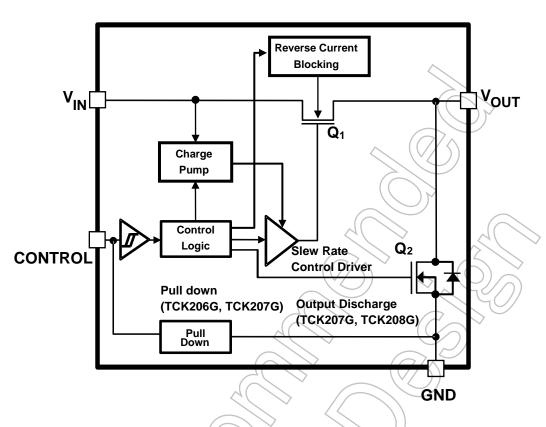
(Glass epoxy(FR4) board dimension: 40mm x 40mm, both sides of board

Metal pattern ratio: a surface approximately 50%, the reverse side approximately 50%

Through hole: diameter 0.5mm x 28)



Block Diagram



Function table

Part number			
	Reverse current blocking	Output auto-discharge	Control pin connection
TCK206G	Bulit in	(// N/A	Pull down (Active High)
TCK207G	Bulit in	Bulit in	Pull down (Active High)
TCK208G	Bulit in	Bulit in	OPEN (Active Low)

Operation Logic table

 $0.75 \text{ V} \le \text{Vin} \le 3.6 \text{V} \text{ (Ta} = -40 \text{ to } 85^{\circ}\text{C})$

		TCK206G	TCK207G	TCK208G
	Output Q₁	ON	ON	OFF
Control voltage "High"	Discharge Q ₂	_	OFF	ON
nigii	Reverse current block	Disable	Disable	Active
Control voltage "Low"	Output Q₁	OFF	OFF	ON
	Discharge Q ₂	_	ON	OFF
	Reverse current block	Active	Active	Disable
Control voltage "OPEN"	Output Q₁	OFF	OFF	_
	Discharge Q₂	<u> </u>	ON	_
	Reverse current block	Active	Active	_

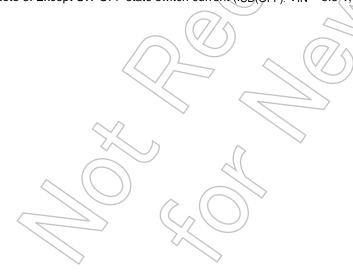


Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

Observatorists		Total Constition		Ta = 25°C			Ta = −40 to 85°C			
Characteristics Syml		Test Condition		1	Min	Тур.	Max	Min	Max	Unit
Input voltage	VIN	_	_				3.6	0.75	3.6	V
CONTROL High-level input voltage	ViH		0.9 V ≤V	IN	0.9	1)>	0.9	_	V
CONTROL High-level input voltage	VIH	_	V _{IN} < 0.9	9V	VIN	7/^	_	VIN	_	V
CONTROL Low-level input voltage	VIL		0.9 V ≤V	in <	/-(/	()	0.4	_	0.4	V
CONTROL Low-level input voltage	VIL.	_	V _{IN} < 0.9	9V)	0.3	_	0.3	V
Quiescent current (ON state)	lo	V _{IN} = V _{CT} = 3.6 '	V,	TCK206G TCK207G		22			35	μА
Quiescent current (ON state)	IQ	$V_{IN} = 3.6 \text{ V, V}_{CT}$ $I_{OUT} = 0 \text{ mA}$	= 0 V,	TCK208G		22			33	μΛ
Standby ourrant (SIM OFF state)	IQ(OFF)	V _{IN} = 3.6 V, V _{CT} V _{OUT} = OPEN			\rightarrow	0.7	>	5		
Standby current (SW OFF state)		V _{IN} = V _{CT} = 3.6 V V _{OUT} = OPEN	V, (Note 3)	TCK208G		0.7		7	5	μА
Reverse blocking current	I _{RB}	V _{OUT} = 3.6 V, V _{IN} = 0 V, V _{CT} = 0 V			- (0.1	\	_	10	μА
	R _{ON}	V _{IN} = 3.3 V, I _{OUT}	= -1.5 A			18.1	_	_	28	
On resistance		VIN = 1.8 V, IOUT	r = -1.5 A	_	$(\forall /$	18.1	_	_	28	
		$V_{IN} = 1.2 \text{V}, I_{OUT}$	r = -1.5 A			18.2	_	_	28	mΩ
		VIN = 1.0 V, IOUT	-1.5 A)+	18.2	_		28	
		V _{IN} = 0.75 V, lot	JT = -1.5 A		//_	18.4		_	28	
Discharge on resistance	RsD	(TCK207G and TCK208G)			_	380	_	_		Ω

Note 3: Except SW OFF state switch current (ISD(OFF): VIN = 5.5 V, VCT = 0 V, VOUT : GND).





AC Characteristics (Ta = 25°C)

$V_{IN} = 0.75 V$

Characteristics	Symbol	Test Condition(Figure 1)		Min	Тур.	Max	Unit
V _{OUT} rise time	t _r	R _L =500Ω , C _L =0.1μF		_	180	_	μS
V _{OUT} fall time	t _f	R _L =500Ω , C _L =0.1μF	TCK207G TCK208G	_<	50	_	μS
			TCK206G	- (80	_	
Turn on delay	toN	R _L =500Ω , C _L =0.1μF			75	_	μS
Turn off delay	tOFF	R _L =500Ω , C _L =0.1μF	TCK206G TCK207G		8	_	μS
		,	TCK208G	1	14	_	•

$V_{IN} = 1.2 V$

<u> </u>						
Characteristics	Symbol	Test Condition(Figure 1)	Min	Тур.	Max	Unit
Vout rise time	t _r	RL=500Ω , CL=0.1μF	_	(240)	\sim	μS
Vout fall time	tf	R _L =500Ω , C _L =0.1μF TCK208 TCK208	G –	50	<u>)</u>	μS
Turn on delay	ton	R _L =500Ω , C _L =0.1μF		65	_	μS
Turn off delay	tOFF	TCK206 TCK207		8	_	μs
		TCK208	G –	13	_	

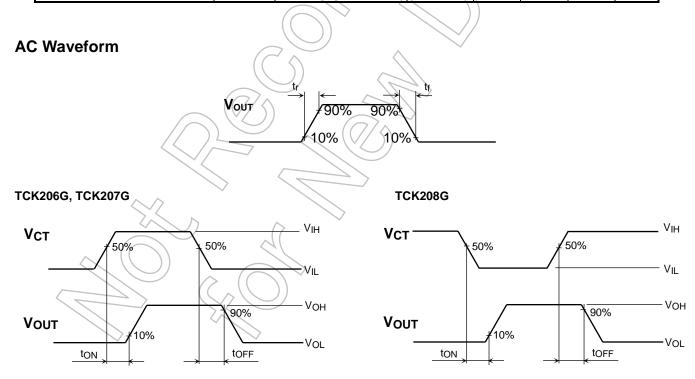


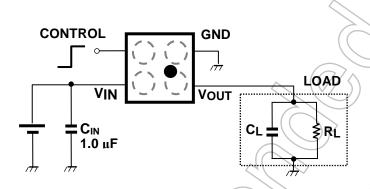
Figure 1 t_r, t_f, t_{ON}, t_{OFF} Waveforms



Application Note

1. Application circuit example (top view)

The figure below shows the example of configuration for TCK206G, TCK207G and TCK208G.



1) Input capacitor

An input capacitor (CIN) is necessary for the stable operation of TCK206G, TCK207G and TCK208G. And it is effective to reduce voltage drop due to sharp changes in output current and also for improved stability of the power supply. When used, place CIN more than 1.0 μ F as close to VIN pin to improve stability of the power supply.

2) Output capacitor

An output capacitor (COUT) is not necessary for the guaranteed operation of TCK206G, TCK207G and TCK208G. However, there is a possibility of overshoot or undershoot caused by output load transient response, board layout and parasitic components of load switch IC. In this case, an output capacitor COUT more than 0.1µF is recommended.

3) Control pin

Control pins for TCK206G and TCK207G are both Active High and TCK208G is Active Low. These control both the pass-through n-ch MOSFET and the discharge n-ch MOSFET (except TCK206G), operated by the control voltage. Each control pin is equipped with Schmitt trigger. When the control voltage level is High (Low; TCK208G), Output n-ch MOSFET is ON state and discharge n-ch MOSFET is SW OFF state. When control voltage level is Low (High; TCK208G), and the state of the MOSFETs is reversed. Also, pull down resistance equivalent to a few MΩ is connected between CONTROL and GND, thus the load switch IC is in SW OFF state even when CONTROL pin is OPEN(except TCK208G). In addition, CONTROL pin has a tolerant function such that it can be used even if the control voltage is higher than the input voltage.

2. Reverse current blocking

Reverse current blocking(SW OFF state) function is designed in these products. This function is active at output n-ch MOSEFT turned off.

However it does not assure for the suppression of uprising device operation. In use of these products, please read through and understand dissipation idea for absolute maximum ratings from the above mention or our 'Semiconductor Reliability Handbook'. Then use these products under absolute maximum ratings in any condition. Furthermore, Toshiba recommends inserting failsafe system into the design.

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3. Power Dissipation

Board-mounted power dissipation ratings for TCK206G, TCK207G and TCK208G are available in the Absolute Maximum Ratings table

Power dissipation is measured on the board condition shown below.

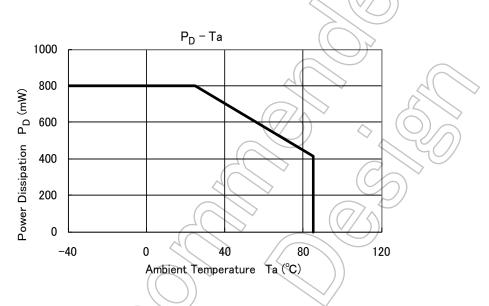
[The Board Condition]

Board material: Glass epoxy (FR4)

Board dimension: 40mm x 40mm (both sides of board), t=1.8mm

Metal pattern ratio: a surface approximately 50%, the reverse side approximately 50%

Through hole: diameter 0.5mm x 28

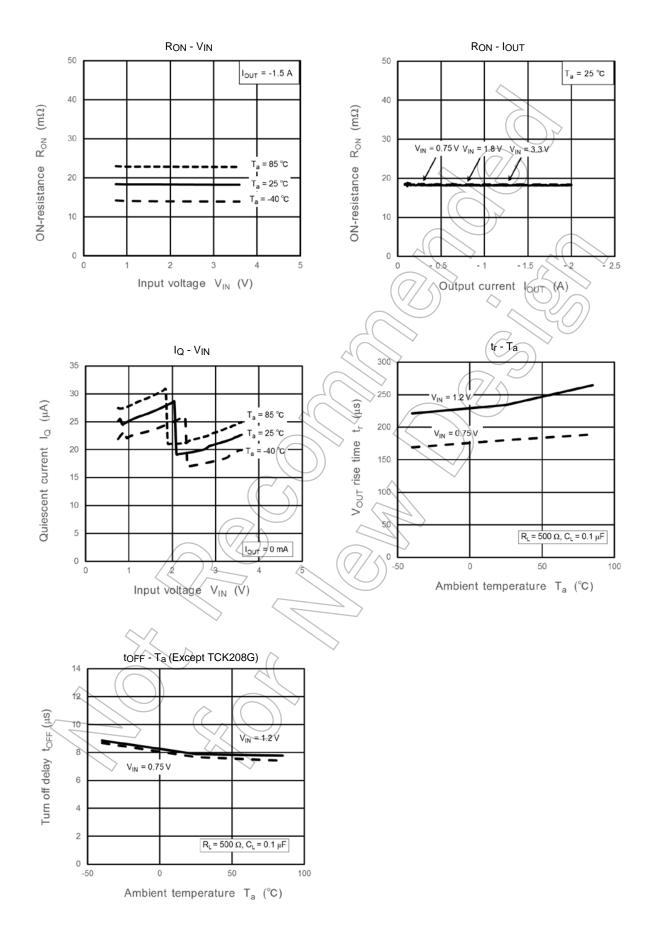


Please allow sufficient margin when designing a board pattern to fit the expected power dissipation. Also take into consideration the ambient temperature, input voltage, output current etc. and applying the appropriate derating for allowable power dissipation during operation.



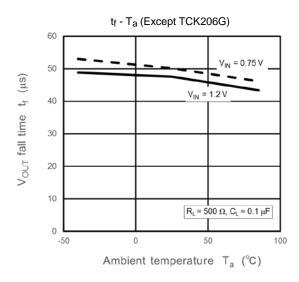


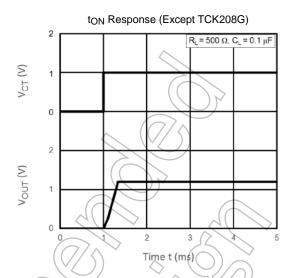
Representative Typical Characteristics



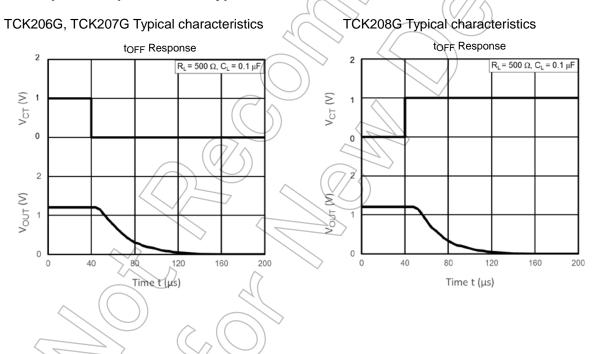
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toff Response Representative Typical Characteristics

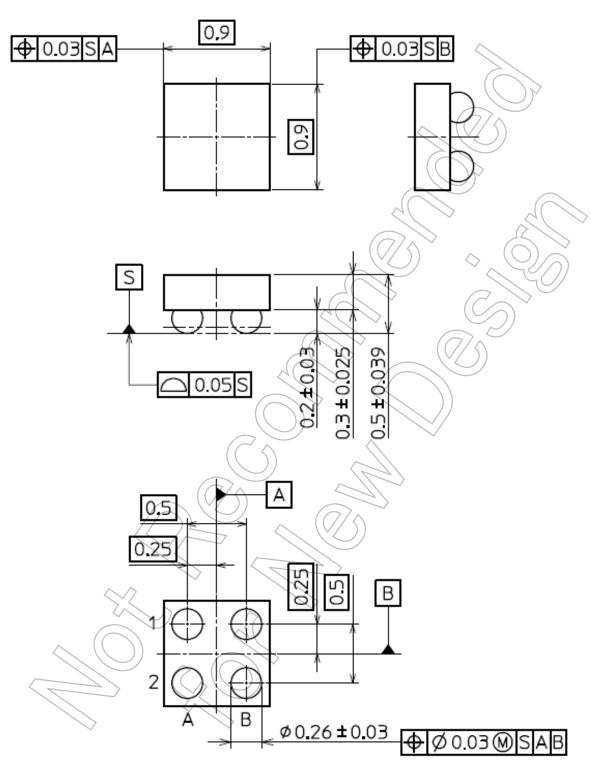


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Information

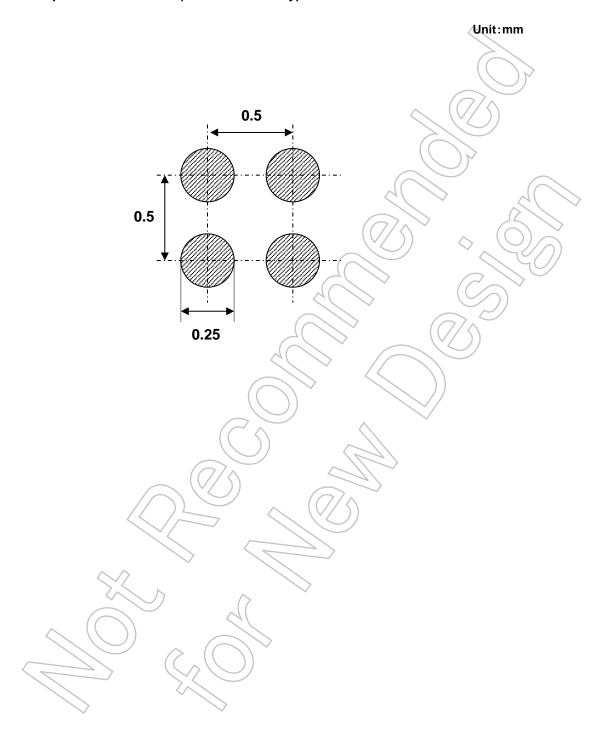
WCSP4C Unit: mm



Weight: 0.9 mg (typ.)



Land pattern dimensions (for reference only)





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