TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SA08F,TC7SA08FU

#### 2-Input AND Gate

#### **Features**

• Low voltage operation: V<sub>CC</sub> = 1.8 to 3.6 V

• High speed operation :  $t_{pd}$  = 2.8 ns (max) ( $V_{CC}$  = 3.0 to 3.6 V)

:  $t_{pd} = 3.7 \text{ ns (max) (V}_{CC} = 2.3 \text{ to } 2.7 \text{ V})$ 

 $: t_{pd} = 7.4 \text{ ns (max) (V}_{CC} = 1.8 \text{ V})$ 

High output current : I<sub>OH</sub>/I<sub>OL</sub> = ±24 mA (min) (V<sub>CC</sub> = 3.0 V)

:  $I_{OH}/I_{OL}$  = ±18 mA (min) ( $V_{CC}$  = 2.3 V)

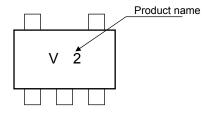
 $: I_{OH}/I_{OL} = \pm 6 \text{ mA (min) (V}_{CC} = 1.8 \text{ V)}$ 

• 3.6-V tolerant inputs.

• 3.6-V power down protection output.

TC74VCX08FT equivalent.

#### Marking



# Weight

SSOP5-P-0.95 : 0.016 g (typ.)

# SSOP5-P-0.65A : 0.006 g (typ.)

TC7SA08F

TC7SA08FU

## Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	-0.5 to 4.6	) v
DC input voltage	VIN	-0.5 to 4.6	V
DC output voltage	V <sub>OUT</sub>	-0.5 to 4.6 (Note 1)	V
De output voltage	V001	-0.5 to V <sub>CC</sub> +0.5 (Note 2)	<b>v</b>
Input diode current	) l <sub>IK</sub>	-50	mA
Output diode current	lok	-50 (Note 3)	mA
DC output current	lout	±50	mA
Power dissipation	(PD	200	mW
DC V <sub>CC</sub> /ground current	Içc	±100	mA
Storage temperature range	T <sub>stg</sub>	-65 to 150	°C

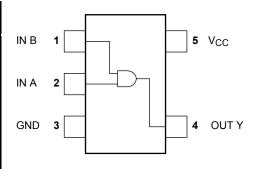
### Pin Assignment (top view)

SSOP5-P-0.95

SSOP5-P-0.65A

(SMV)

(USV)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{CC} = 0 V$ 

Note 2: High or Low State. IOUT absolute maximum rating must be observed.

Note 3: V<sub>OUT</sub> < GND

#### **IEC Logic Symbol**



#### **Truth Table**

Α	В	Υ
L	L	L
L	Н	L
Н	L	L
Н	Н	н ((

# **Operating Ranges**

Characteristics	Symbol	Rating Unit
Supply voltage	V	1.8 to 3.6
Supply voltage	V <sub>CC</sub>	1.2 to 3.6 (Note 4)
Input voltage	V <sub>IN</sub>	-0.3 to 3.6
Output voltage	Vout	0 to 3.6 (Note 5)
Cutput voltage		0 to V <sub>CC</sub> (Note 6)
		± 24 (Note 7)
Output current	I <sub>OH</sub> /I <sub>OL</sub>	± 18 (Note 8) mA
	<	± 6 (Note 9)
Operating temperature range	T <sub>opr</sub>	-40 to 85 °C
Input rise and fall time	dt/dv	0 to 10 (Note 10) ns/V

Note 4: Data retention only

Note 5:  $V_{CC} = 0 V$ 

Note 6: High or low state

Note 7:  $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ 

Note 8:  $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$ 

Note 9:  $V_{CC} = 1.8 \text{ V}$ 

Note 10:  $V_{IN} = 0.8$  to 2.0 V,  $V_{CC} = 3.0$  V

#### **Electrical Characteristics**

# DC Characteristics (Ta = -40 to 85°C, 2.7 V < $V_{CC} \le 3.6$ V)

Charac	cteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Max	Unit		
Innut voltage	High level	V <sub>IH</sub>		_	2.7 to 3.6	2.0	_	V		
Input voltage	Low level	V <sub>IL</sub>		_	2.7 to 3.6	_	0.8	V		
			$V_{IN} = V_{IH}$	I <sub>OH</sub> = -100 μA	2.7 to 3.6	V <sub>CC</sub> - 0.2	_			
	High level	V <sub>OH</sub>		$I_{OH} = -12 \text{ mA}$	2.7	2.2	_			
Output voltage				$I_{OH} = -18 \text{ mA}$	3.0	2.4	_			
				$I_{OH} = -24 \text{ mA}$	3.0	2.2	_	V		
		V <sub>OL</sub>	$V_{IN} = V_{IH}$ or $V_{IL}$	I <sub>OL</sub> = 100 μA	2.7 to 3.6		0.2			
	Low level			\/\/or\/	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	I <sub>OL</sub> = 12 mA	2.7	*	0.4	
	Low level			I <sub>OL</sub> = 18 mA	3.0		0.4			
				I <sub>OL</sub> = 24 mA	3.0		0.55			
Input leakage curre	ent	I <sub>IN</sub>	V <sub>IN</sub> = 0 to 3.6 V		2.7 to 3.6	H	±5.0	μΑ		
Power off leakage	current	I <sub>OFF</sub>	V <sub>IN</sub> , V <sub>OUT</sub> = 0 to 3.6 V		0	>)	10.0	μΑ		
Quiescent supply current		l lcc –	$V_{IN} = V_{CC}$ or $GN$	D)	2.7 to 3.6		20.0			
			$V_{CC} \le (V_{IN}, V_{OUT}) \le 3.6 \text{ V}$		2.7 to 3.6		±20.0	μΑ		
Increase in I <sub>CC</sub> pe	r input	Δl <sub>CC</sub>	V <sub>IH</sub> = V <sub>CC</sub> - 0.6 V		2.7 to 3.6	_	750			

# DC Characteristics (Ta = -40 to 85°C, 2.3 V $\leq$ V<sub>CC</sub> $\leq$ 2.7 V)

Characteristics		Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Max	Unit
Input voltage	High level	VIH	7/2	===	2.3 to 2.7	1.6	_	V
input voitage	Low level	// SVIL			2.3 to 2.7	_	0.7	V
				I <sub>OH</sub> = -100 μA	2.3 to 2.7	V <sub>CC</sub> - 0.2		
	High level	VoH	VIN = VIH	$I_{OH} = -6 \text{ mA}$	2.3	2.0		
		1		$I_{OH} = -12 \text{ mA}$	2.3	1.8	_	
Output voltage	√?			$I_{OH} = -18 \text{ mA}$	2.3	1.7	_	V
4		$\wedge$		I <sub>OL</sub> = 100 μA	2.3 to 2.7	_	0.2	
	Low level	Vol	$V_{IN} = V_{IH}$ or $V_{IL}$	I <sub>OL</sub> = 12 mA	2.3	_	0.4	
				I <sub>OL</sub> = 18 mA	2.3	_	0.6	
Input leakage curre	ent	( IIN)	$V_{IN} = 0 \text{ to } 3.6 \text{ V}$		2.3 to 2.7	_	±5.0	μА
Power off leakage	current	OFF	V <sub>IN</sub> , V <sub>OUT</sub> = 0 to 3	3.6 V	0	_	10.0	μА
			V <sub>IN</sub> = V <sub>CC</sub> or GNE	)	2.3 to 2.7	_	20.0	^
Quiescent supply c	urrent	Icc	V <sub>CC</sub> ≤ (V <sub>IN</sub> , V <sub>OUT</sub>	·) ≤ 3.6 V	2.3 to 2.7		±20.0	μА

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#### DC Characteristics (Ta = -40 to 85°C, 1.8 V $\leq$ V<sub>CC</sub> < 2.3 V)

Charac	teristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Max	Unit
Input voltage	High level	V <sub>IH</sub>			1.8 to 2.3	V <sub>CC</sub> × 0.7		V
Input voltage	Low level	V <sub>IL</sub>		_		_	V <sub>CC</sub> × 0.2	V
	High level	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IH</sub>	$I_{OH} = -100 \mu A$	1.8	Vcc - 0.2	_	
Output voltage				I <sub>OH</sub> = -6 mA	7/1,8	1.4	_	V
			$V_{IN} = V_{IH}$ or $V_{IL}$	$I_{OL} = 100 \mu A$	(1.8)	_	0.2	
	Low level	V <sub>OL</sub>		I <sub>OL</sub> = 6 mA	1.8	_	0.3	
Input leakage curre	ent	I <sub>IN</sub>	V <sub>IN</sub> = 0 to 3.6 V		1.8	_	±5.0	μА
Power off leakage	current	l <sub>OFF</sub>	V <sub>IN</sub> , V <sub>OUT</sub> = 0 to 3.6 V		0	)	10.0	μΑ
Quiescent cumply current		I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		1.8	4	20.0	μА
Quiescent supply o	Quiescent supply current		$V_{CC} \le (V_{IN}, V_{OUT}) \le 3.6 \text{ V}$		1.8	5-1	±20.0	μΛ

# AC Characteristics (Ta = -40 to 85°C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500$ $\Omega$ )

Characteristics	Symbol	Test Condition	Vce (V)	Min	Max	Unit
	_		))1.8	1.5	7.4	
Propagation delay time	t <sub>pLH</sub> t <sub>pHL</sub>	Figure 1, Figure 2	$2.5 \pm 0.2$	1.0	3.7	ns
	PITE		$3.3 \pm 0.3$	0.8	2.8	

For  $C_L = 50$  pF, add approximately 300 ps to the AC maximum specification.

# Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Тур.	Unit
Input capacitance	CIN			1.8, 2.5, 3.3	6	pF
Power dissipation capacitance	C <sub>PD</sub> (	f <sub>IN</sub> = 10 MHz	(Note 11)	1.8, 2.5, 3.3	20	pF

Note 11: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

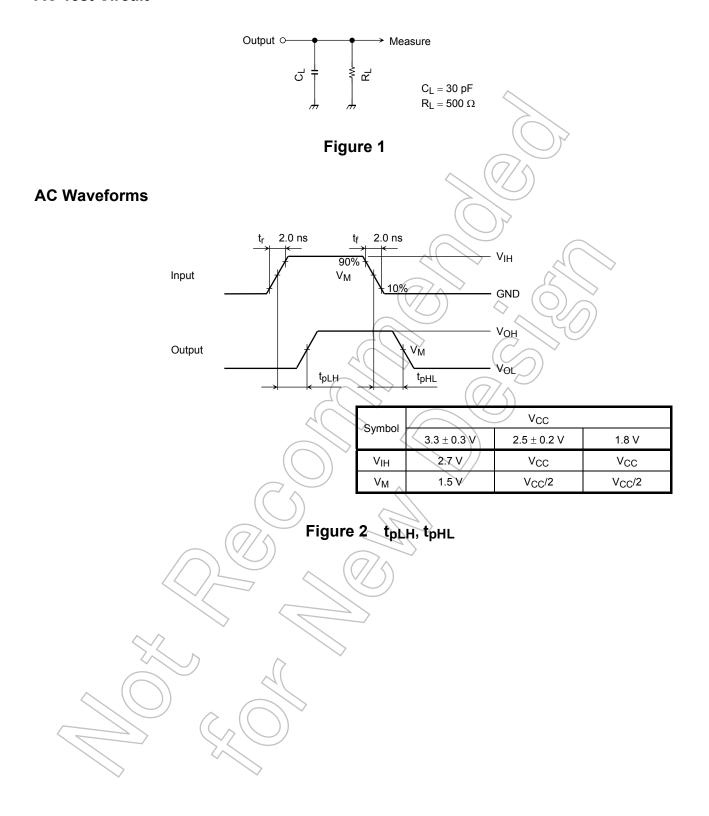
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Average operating current can be obtained by the equation.

ICC (opr) = CPD·VCC·fIN + ICC

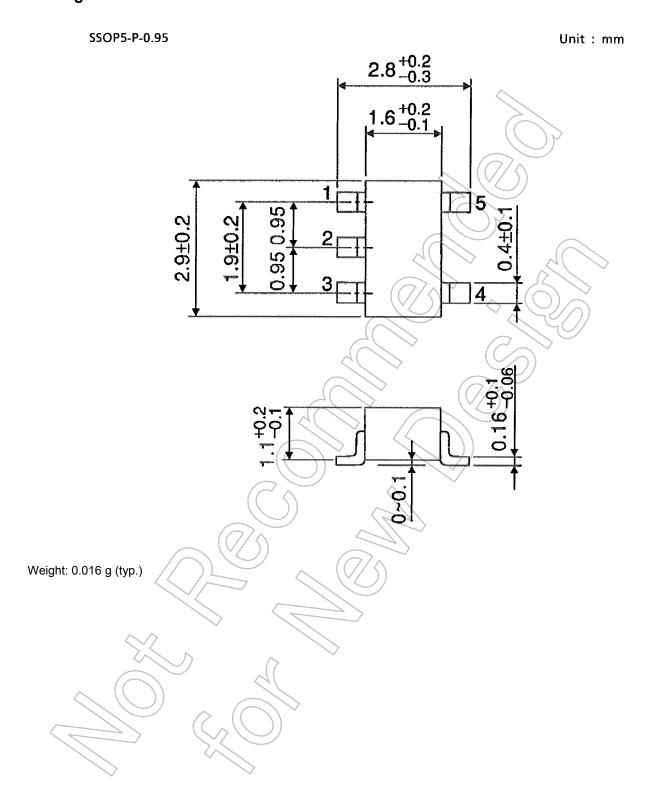
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#### **AC Test Circuit**



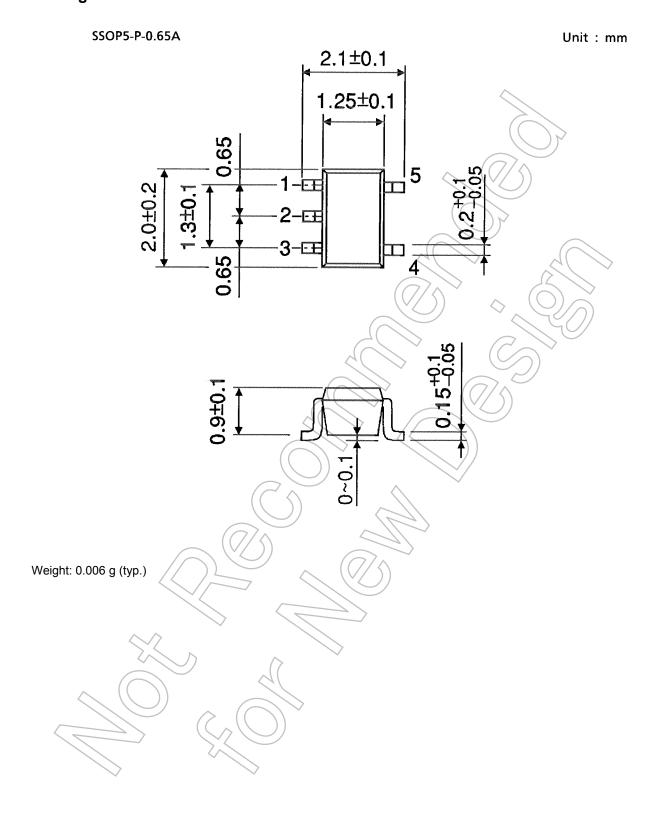
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#### **Package Dimensions**



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#### **Package Dimensions**



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