TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7PG34AFE

Dual NON-Inverter

Features

• High output current : ±8 mA (min) at V_{CC} = 3 V

• Super high speed operation : t_{pd} = 2.8 ns (typ.)

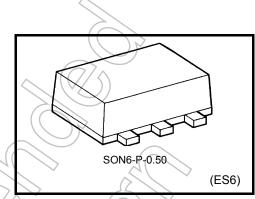
at V_{CC} = 3.3 V, C_L = 15pF

• Operating voltage range : V_{CC} = 0.9 to 3.6 V

• 5.5-V tolerant inputs

• ESD performance : Machine model ≥ ±200 V

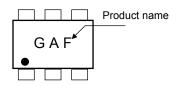
Human body model ≥ ±2000 V

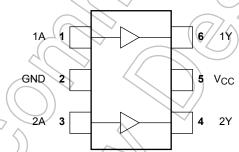


Weight: 0.003 g (typ.)

Marking

Pin Assignment (top view)





Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Supply voltage	V _{CC}	-0.5 to 4.6	V
DC input voltage	V _{IN}	-0.5 to 7.0	V
DC output voltage	V _{OUT}	−0.5 to V _{CC} + 0.5	V
Input diode current	l _{IK}	-20	mA
Output diode current	lok	±20 (Note 1)	mA
DC output current	OUT	±25	mA
DC V _{CC} /GND current) ICC	±100	mA
Power dissipation	$P_{\mathbb{Q}}$	150	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V_{OUT} < GND, V_{OUT} > V_{CC}

Start of commercial production 2006-12

IEC Logic Symbol

IN A OUT Y

Truth Table

А	Y
L	L
Н	H

Operating Ranges

Characteristic	Symbol	Rating
Supply voltage	V _{CC}	0.9 to 3.6
Input voltage	V _{IN}	0 to 5.5
Output voltage	V _{OUT}	0 to V _{CC} V
Output current		±8.0 (Note 2)
	loh/lor	±4,0 (Note 3)
		±3.0 (Note 4)
		±1.7 (Note 5)
		±0.3 (Note 6)
		±0.02 (Note-7)
Operating temperature	T _{opr}	40 to 85 °C
Input rise and fall time	dt/dv	0 to10 (Note 8) ns/V

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Note 2: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 3: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 4: $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$

Note 5: $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$

Note 6: $V_{CC} = 1.1 \text{ to } 1.3 \text{ V}$

Note 7: $V_{CC} = 0.9 \text{ V}$

Note 8: $V_{IN} = 0.8 \text{ to } 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$

Electrical Characteristics

DC Characteristics

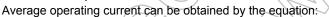
Characteristic Symbol Test Condition			Ta = 25°C			Ta = -40 to 85°C		Linit		
		resi	Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
				0.9	V_{CC}	_	7	V _{CC}		
High-level VIH				1.1 to 1.3	V _{CC} × 0.7		4	V _{CC} × 0.7	l	٧
	V _{IH}		_	1.4 to 1.6	V _{CC} × 0.65	-(V _{CC} × 0.65		
				1.65 to 1.95	V _{CC} × 0.65			V _{CC} × 0.65	_	
				2.3 to 2.7	1.7	(-)	> —	1.7	_	
				3.0 to 3.6	2.0		_	2.0	_	
				0.9	4	\rightarrow	GND	H.	GND	
				1.1 to 1.3	775	>	V _{CC} × 0.3	5	V _{CC} × 0.3	
Low-level input voltage	V _{IL}))	_	V _{CC} × 0.35		V _{CC} × 0.35	V
input voltage			1.65 to 1.95		- (V _{CC} × 0.35		V _{CC} × 0.35		
				2.3 to 2.7	_		0.7		0.7	
				3.0 to 3.6	1(\ <u>\</u>	0.8		0.8	
			I _{OH} =-0.02 mA	0.9	0.75	1	_	0.75	_	
			$I_{OH} = -0.3 \text{ mA}$	1.1 to 1.3	V _{CC} × 0.75))	_	V _{CC} × 0.75	_	
High-level	V _{OH}		I _{OH} = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75	-	l	V _{CC} × 0.75	l	V
output voltage			I _{OH} = -3.0 mA	1.65 to 1.95	V _{CC} -0.45	_	-	V _{CC} -0.45		-
			1 _{OH} = -4.0 mA	2.3 to 2.7	2.0	_		2.0	1	
			I _{OH} = -8.0 mA	3.0 to 3.6	2.48	_	_	2.48	_	
Low-level voltage			$I_{OL} = 0.02 \text{ mA}$	0.9		_	0.1	_	0.1	
	I _{OL} =0	$I_{OL} = 0.3 \text{ mA}$	1.1 to1.3	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25		
	VOL	V _{IN} = V _{IL}	I _{OL} = 1.7 mA	1.4 to 1.6	_	_	V _{CC} × 0.25		V _{CC} × 0.25	V
		I _{OL} = 3.0 mA		_	_	0.45	_	0.45		
			$I_{OL} = 4.0 \text{ mA}$	2.3 to 2.7		_	0.4	_	0.4	
		I _{OL} = 8.0 mA		3.0 to 3.6		_	0.4	_	0.4	
Input leakage current	I _{IN}	$V_{IN} = 0$ to 5.5V		0 to 3.6	_	_	±0.1	_	±1.0	μΑ
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		3.6	_	_	1.0	_	10.0	μА

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AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristic Symbol		Test Condition	Ta = 25°C		Ta = $-40 \text{ to } 85^{\circ}$		to 85°C	Unit	
Griaracteristic Symbol	Symbol	rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time		C_L = 10 pF, R_L = 1 $M\Omega$	0.9	_	27.2	_	_	_	ns
			1.1 to 1.3	_	12.2	23.2	1.0	42.6	
			1.4 to 1.6		6.5	10.2	1.0	12.0	
			1.65 to 1.95		4.7	7.0	1.0	7.6	
			2.3 to 2.7		3.1	4.4	1.0	4.9	
			3.0 to 3.6	\ -	2.4	3.5	1.0	4.1	
		C_L = 15 pF, R_L = 1 $M\Omega$	0.9		29.8))	_		
	^t pLH ^t pHL		1.1 to 1.3		13.5	26.0	1.0	44.5	
			1.4 to 1.6	((7.2	11.4	1.0	13.6	
			1.65 to 1.95	Ŧ	5.2	7.5	1.0	7.7	
			2.3 to 2.7	/ (r	3.4	4.8	21.0	5.5	
			3.0 to 3.6	//-\)	2.8	3.8 (1).0	4.4	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	$) \Big)$	40.7		4	/ —	
			1.1 to 1.3	<u>\</u>	17.8	33.9	1.0	64.1	
			1.4 to 1.6	_	9.1	14.3)	1.0	17.4	
			1.65 to 1.95	_	6.6	9.8	1.0	10.2	
			2.3 to 2.7		4.1	6.2	1.0	6.6	
			3.0 to 3.6		3.3	4.8	1.0	5.2	
Input capacitance	C _{IN}		3.6	+/	3		_	_	pF
Power dissipation capacitance	C_{PD}	(Note 9)	0.9 to 3.6	_//	6	_	_	_	pF

Note 9: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

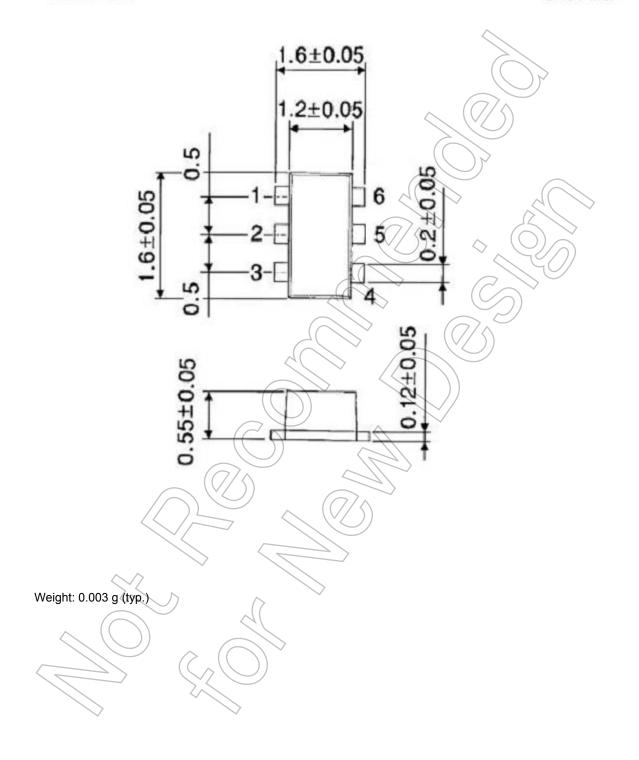


ICC (opr.) = CPD·VCC·fIN + ICC/2



Package Dimensions

SON6-P-0.50 Unit: mm



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