

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74VHC540F, TC74VHC540FK TC74VHC541F, TC74VHC541FK

Octal Bus Buffer

TC74VHC540F/FK TC74VHC541F/FK Inverted, 3-State Outputs
Non-Inverted, 3-State Outputs

The TC74VHC540/TC74VHC541 are advanced high speed CMOS OCTAL BUS BUFFERs fabricated with silicon gate C<sup>2</sup>MOS technology.

They achieve the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

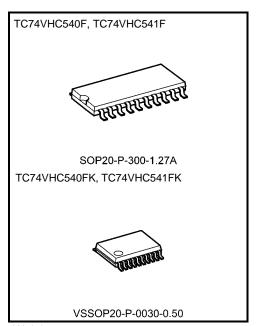
The TC74VHC540 is an inverting type, and the TC74VHC541 is a non-inverting type.

When either  $\overline{G}1~$  or  $\overline{G}2~$  are high, the terminal outputs are in the high-impedance state.z

An input protection circuit ensures that 0 to  $5.5\ V$  can be applied to the input pins without regard to the supply voltage. This device can be used to interface  $5\ V$  to  $3\ V$  systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

#### **Features**

- High speed: tpd = 3.7 ns (typ.) at VCC = 5 V
- Low power dissipation: ICC = 4 μA (max) at Ta = 25°C
- High noise immunity: VNIH = VNIL = 28% VCC (min)
- Power down protection is provided on all inputs.
- Balanced propagation delays: tplH ≃ tpHL
- Wide operating voltage range: VCC (opr) = 2 V to 5.5 V
- Low noise: VOLP = 1.0 V (max)
- Pin and function compatible with 74ALS540/541



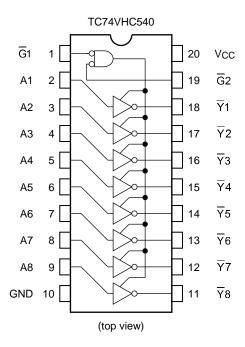
Weight

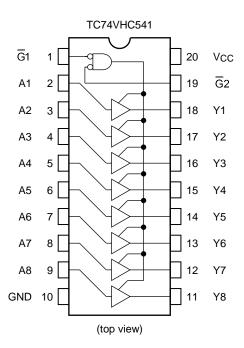
SOP20-P-300-1.27A : 0.22 g (typ.) VSSOP20-P-0030-0.50 : 0.03 g (typ.)

Start of commercial production 1991-05

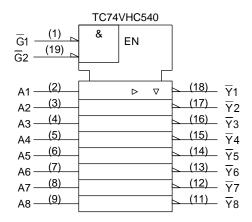


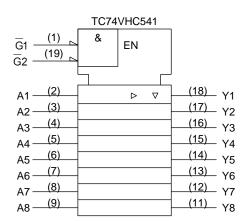
#### **Pin Assignment**





### **IEC Logic Symbol**





#### **Truth Table**

	Inputs	Outputs			
G1	G2	An	Yn	$\overline{Y}_n$	
Н	Х	Χ	Z	Z	
Х	Н	Х	Z	Z	
L	L	Н	Н	L	
L	L	L	L	Н	

X: Don't care

Z: High impedance

Yn: TC74VHC541

Yn: TC74VHC540



#### **Absolute Maximum Ratings (Note)**

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	−0.5 to 7.0	V
DC input voltage	VIN	−0.5 to 7.0	V
DC output voltage	Vout	-0.5 to Vcc + 0.5	V
Input diode current	lıĸ	-20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC Vcc/ground current	Icc	±75	mA
Power dissipation	PD	180	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Operating Ranges (Note)**

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	2.0 to 5.5	V
Input voltage	VIN	0 to 5.5	V
Output voltage	Vout	0 to VCC	V
Operating temperature	Topr	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 100 (V <sub>CC</sub> = $3.3 \pm 0.3$ V) 0 to 20 (V <sub>CC</sub> = $5 \pm 0.5$ V)	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.



#### **Electrical Characteristics**

#### **DC Characteristics**

Characteristics Symbol		Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit		
	- <b>,</b>			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max		
High-level input				2.0	1.50	_	_	1.50	_		
High-level input VIH		_		3.0 to 5.5	Vcc × 0.7	_	ı	Vcc × 0.7	I	V	
Low-level input		_		2.0	_	_	0.50	_	0.50	V	
voltage	VIL			3.0 to 5.5	_	_	V <sub>CC</sub> × 0.3	_	V <sub>CC</sub> × 0.3		
				2.0	1.9	2.0	_	1.9	-		
			I <sub>OH</sub> = -50 μA	3.0	2.9	3.0	_	2.9	_	V	
High-level output voltage	Voн	VIN = VIH or VIL		4.5	4.4	4.5	_	4.4	_		
Voltage			I <sub>OH</sub> = −4 mA	3.0	2.58	_	_	2.48	_		
			I <sub>OH</sub> = −8 mA	4.5	3.94	_	_	3.80	_		
	VoL	VIN = VIH or VIL		2.0	_	0.0	0.1	_	0.1		
			$I_{OL} = 50 \mu A$	3.0	_	0.0	0.1	_	0.1		
Low-level output voltage				4.5	_	0.0	0.1	_	0.1	V	
voltage			IoL = 4 mA	3.0	_	_	0.36	_	0.44		
			IOL = 8 mA	4.5	_	_	0.36	_	0.44		
3-state output off- state current	loz	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub> V <sub>OUT</sub> = V <sub>CC</sub> or GND		5.5	_	_	±0.25	_	±2.50	μΑ	
Input leakage current	liN	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5	_		±0.1	_	±1.0	μΑ	
Quiescent supply current	lcc	VIN = VCC or GND		5.5	_	_	4.0	_	40.0	μΑ	



#### AC Characteristics (input: tr = tf = 3 ns)

Characteristics	Symbol	Tes	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	<b>C</b> y		V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Тур.	Max	Min	Max	Onne
	<sup>t</sup> pLH		3.3 ± 0.3	15	_	4.8	7.0	1.0	8.5	ns
Propagation delay time				50	_	7.3	10.5	1.0	12.0	
(TC74VHC540)	$t_{pHL}$	_	5.0 ± 0.5	15	_	3.7	5.0	1.0	6.0	113
			3.0 ± 0.3	50	_	5.2	7.0	1.0	8.0	
			3.3 ± 0.3	15	_	5.0	7.0	1.0	8.5	
Propagation delay time	$t_{pLH}$	_	3.3 ± 0.3	50	_	7.5	10.5	1.0	12.0	ns
(TC74VHC541)	t <sub>pHL</sub>		5.0 ± 0.5	15	_	3.5	5.0	1.0	6.0	115
				50	_	5.0	7.0	1.0	8.0	
	t <sub>P</sub> ZL t <sub>P</sub> ZH	$R_L = 1 \text{ k}\Omega$	3.3 ± 0.3	15	_	6.8	10.5	1.0	12.5	- ns
3-state output enable				50	_	9.3	14.0	1.0	16.0	
time			5.0 ± 0.5	15	_	4.7	7.2	1.0	8.5	
				50	_	6.2	9.2	1.0	10.5	
3-state output disable	tpLZ	R <sub>L</sub> = 1 kΩ	$3.3 \pm 0.3$	50	_	11.2	15.4	1.0	17.5	ns
time	tpHZ		$5.0 \pm 0.5$	50	_	6.0	8.8	1.0	10.0	115
Output to output skew	tosHL tosLH	(Note 1)	$3.3 \pm 0.3$	50	_	_	1.5	_	1.5	ns
Output to output skew		(Note 1)	$5.0 \pm 0.5$	50	_	_	1.0	_	1.0	115
Input capacitance	CIN		_		_	4	10	_	10	pF
Output capacitance	C <sub>OUT</sub>	_			_	6	_	_	_	pF
Power dissipation	0	TC74VHC540 TC74VHC541		_	17	_	_	_	pF	
capacitance (Note 2)	CPD			_	18	_	_	_	þΓ	

Note 1: Parameter guaranteed by design.

tosLH = |tpLHm - tpLHn|, tosHL = |tpHLm - tpHLn|

Note 2: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

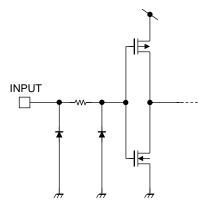
ICC (opr) = CPD·VCC·fIN + ICC/8 (per bit)

#### Noise Characteristics (input: $t_r = t_f = 3 \text{ ns}$ )

Characteristics	Cumbal	Test Condition	Ta =	l lait		
Characteristics	Symbol		V <sub>CC</sub> (V)	Тур.	Limit	Unit
Quiet output maximum dynamic V <sub>OL</sub>	VOLP	C <sub>L</sub> = 50 pF	5.0	0.7	1.0	V
Quiet output minimum dynamic VOL	V <sub>OLV</sub>	C <sub>L</sub> = 50 pF	5.0	-0.7	-1.0	V
Minimum high level dynamic input voltage	VIHD	C <sub>L</sub> = 50 pF	5.0	_	3.5	V
Maximum low level dynamic input voltage	VILD	C <sub>L</sub> = 50 pF	5.0	_	1.5	V



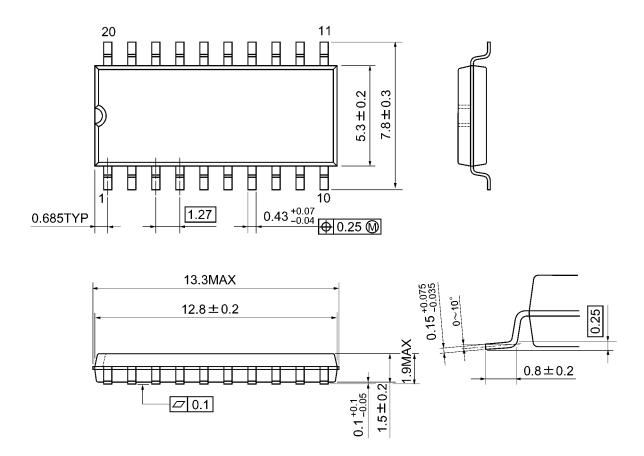
## **Input Equivalent Circuit**





### **Package Dimensions**

SOP20-P-300-1.27A Unit: mm

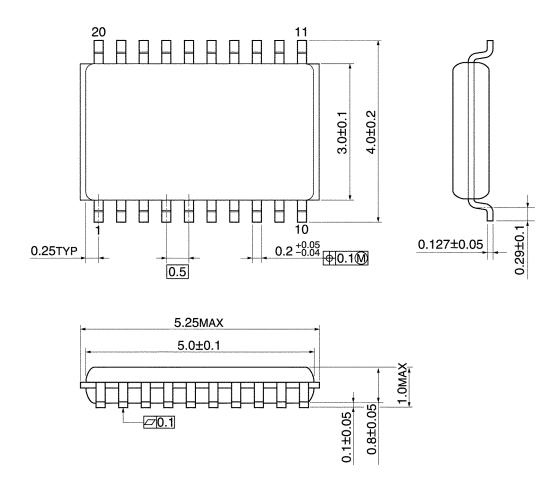


Weight: 0.22 g (typ.)



## **Package Dimensions**

VSSOP20-P-0030-0.50 Unit: mm



Weight: 0.03 g (typ.)



#### RESTRICTIONS ON PRODUCT USE

Toshiba Corporation and its subsidiaries and affiliates are collectively referred to as "TOSHIBA". Hardware, software and systems described in this document are collectively referred to as "Product".

- TOSHIBA reserves the right to make changes to the information in this document and related Product without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA's written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product's quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before customers use the Product, create designs including the Product, or incorporate the Product into their own applications, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application with which the Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications. TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.
- PRODUCT IS NEITHER INTENDED NOR WARRANTED FOR USE IN EQUIPMENTS OR SYSTEMS THAT REQUIRE
  EXTRAORDINARILY HIGH LEVELS OF QUALITY AND/OR RELIABILITY, AND/OR A MALFUNCTION OR FAILURE OF WHICH MAY
  CAUSE LOSS OF HUMAN LIFE, BODILY INJURY, SERIOUS PROPERTY DAMAGE AND/OR SERIOUS PUBLIC IMPACT
  ("UNINTENDED USE"). Except for specific applications as expressly stated in this document, Unintended Use includes, without limitation,
  equipment used in nuclear facilities, equipment used in the aerospace industry, lifesaving and/or life supporting medical equipment,
  equipment used for automobiles, trains, ships and other transportation, traffic signaling equipment, equipment used to control combustions or
  explosions, safety devices, elevators and escalators, and devices related to power plant. IF YOU USE PRODUCT FOR UNINTENDED USE,
  TOSHIBA ASSUMES NO LIABILITY FOR PRODUCT. For details, please contact your TOSHIBA sales representative or contact us via our
  website
- Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any
  applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any
  infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any
  intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE FOR
  PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY WHATSOEVER,
  INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING
  WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (2)
  DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO SALE, USE OF PRODUCT, OR
  INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE,
  ACCURACY OF INFORMATION, OR NONINFRINGEMENT.
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). Product and related software and technology may be controlled under the applicable export laws and regulations including, without limitation, the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export Administration Regulations. Export and re-export of Product or related software or technology are strictly prohibited except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. Please
  use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including
  without limitation, the EU RoHS Directive. TOSHIBA ASSUMES NO LIABILITY FOR DAMAGES OR LOSSES OCCURRING AS A RESULT
  OF NONCOMPLIANCE WITH APPLICABLE LAWS AND REGULATIONS.

#### TOSHIBA ELECTRONIC DEVICES & STORAGE CORPORATION

https://toshiba.semicon-storage.com/

# **Mouser Electronics**

**Authorized Distributor** 

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

## Toshiba:

TC74VHC541FK TC74VHC541FK(EL,K)