

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VHC238F, TC74VHC238FK

3-to-8 Line Decoder

The TC74VHC238 is an advanced high speed CMOS 3-to-8 DECODER fabricated with silicon gate C^2 MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

When the device is enabled, 3 Binary Select inputs (A, B and C) determine which one of the outputs (Y0-Y7) will go High.

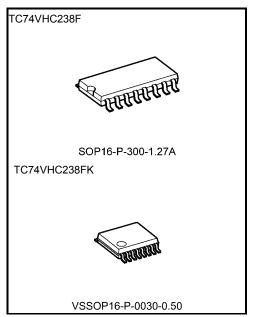
When enable input G1 is held low or either $\overline{G}2A$ or $\overline{G}2B$ is held high, decoding function is inhibited and all outputs go Low.

G1 $\overline{G}2A$, and $\overline{G}2B$ inputs are provided to ease cascade connection and for use as an address decoder for memory systems.

An input protection circuit ensures that 0 to 5.5 V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5 to 3 V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

Features

- High speed: tpd = 5.5 ns (typ.) at VCC = 5 V
- Low power dissipation: ICC = 4 μA (max) at Ta = 25°C
- High noise immunity: VNIH = VNIL = 28% VCC (min)
- Power down protection is provided on all inputs.
- Balanced propagation delays: tpLH ≃ tpHL
- Wide operating voltage range: VCC (opr) = 2 to 5.5 V
- Pin and function compatible with 74ALS238



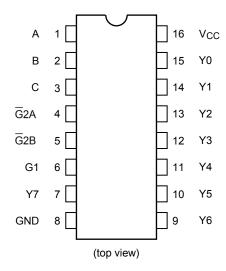
Weight

SOP16-P-300-1.27A : 0.18 g (typ.) VSSOP16-P-0030-0.50 : 0.02 g (typ.)

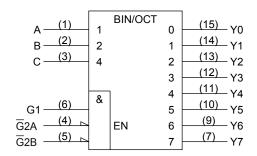
Start of commercial production 1991-11

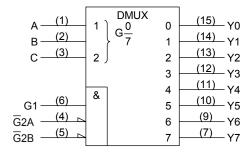


Pin Assignment



IEC Logic Symbol





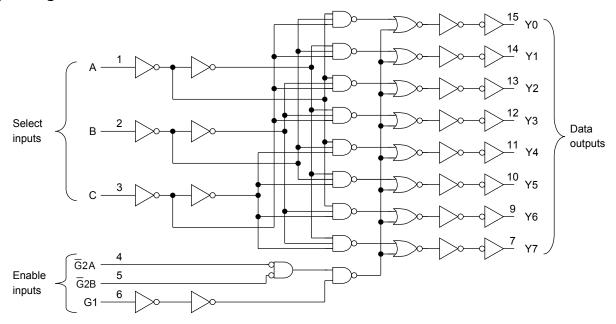
Truth Table

Inputs					Outputs										
Enable			Select		Y0	Y1	Y2	Y3	Y4	Y5	Y6	Y7	Selected Output		
G1	G ₂ A	G ₂ B	С	В	Α	10	11	12	13	14	13	10	1 /	7	
L	Х	Х	Х	Х	Х	L	L	L	L	L	L	L	L	None	
Х	Н	Х	Х	Х	Х	L	L	L	L	L	L	L	L	None	
Х	Х	Н	Х	Х	Х	L	L	L	L	L	L	L	L	None	
Н	L	L	L	L	L	Н	L	L	L	L	L	L	L	Y0	
Н	L	L	L	L	Н	L	Н	L	L	L	L	L	L	Y1	
Н	L	L	L	Н	L	L	L	Н	L	L	L	L	L	Y2	
Н	L	L	L	Н	Н	L	L	L	Н	L	L	L	L	Y3	
Н	L	L	Н	L	L	L	L	L	L	Н	L	L	L	Y4	
Н	L	L	Н	L	Н	L	L	L	L	L	Н	L	L	Y5	
Н	L	L	Н	Н	L	L	L	L	L	L	L	Н	L	Y6	
Н	L	L	Н	Н	Н	L	L	L	L	L	L	L	Н	Y7	

X: Don't care



Logic Diagram



Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	-0.5 to 7.0	V
DC input voltage	VIN	-0.5 to 7.0	V
DC output voltage	Vout	-0.5 to V _{CC} + 0.5	V
Input diode current	lıK	-20	mA
Output diode current	Іок	±20	mA
DC output current	Іоит	±25	mA
DC V _{CC} /ground current	Icc	±75	mA
Power dissipation	PD	180	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Range (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	2.0 to 5.5	V
Input voltage	VIN	0 to 5.5	V
Output voltage	Vout	0 to V _{CC}	V
Operating temperature	Topr	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 100 (V _{CC} = 3.3 ± 0.3 V) 0 to 20 (V _{CC} = 5 ± 0.5 V)	ns/V

Note: The operating range must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.



Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = −40 to 85°C		Unit
				V _{CC} (V)	Min	Тур.	Max	Min	Max	
High-level input voltage	VIH	_		2.0 3.0 to 5.5	1.50 V _{CC} × 0.7	1 1	1 1	1.50 V _{CC} × 0.7	1 1	٧
Low-level input voltage	VIL	_		2.0 3.0 to 5.5	1 1	1 1	0.50 V _{CC} × 0.3		0.50 V _{CC} × 0.3	٧
High-level output voltage	Vон	VIN = VIH or VIL	I _{OH} = -50 μA	2.0 3.0 4.5	1.9 2.9 4.4	2.0 3.0 4.5		1.9 2.9 4.4		V
			$I_{OH} = -4 \text{ mA}$ $I_{OH} = -8 \text{ mA}$	3.0 4.5	2.58 3.94		_	2.48 3.80	_	
Low-level output voltage	V _O L	VIN = VIH or VIL	I _{OL} = 50 μA	2.0 3.0 4.5		0.0 0.0 0.0	0.1 0.1 0.1	_ _ _	0.1 0.1 0.1	٧
Tonago			I_{OL} = 4 mA I_{OL} = 8 mA	3.0 4.5	1 1		0.36 0.36	_ _	0.44 0.44	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5		ı	±0.1		±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		5.5	_	_	4.0	_	40.0	μА



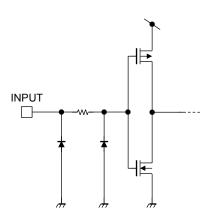
AC Characteristics (input: tr = tf = 3 ns)

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = −40 to 85°C		Unit
	Cymbol		V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Onne
	tpLH		3.3 ± 0.3	15	_	8.0	12.3	1.0	14.5	ns ns
Propagation delay time		_		50	_	10.5	15.8	1.0	18.0	
(A, B, C-Y)	tpHL		5.0 ± 0.5	15	_	5.5	8.1	1.0	9.5	
				50	_	7.0	10.1	1.0	11.5	
	t _P LH t _P HL		3.3 ± 0.3	15		8.1	12.8	1.0	15.0	
Propagation delay time		_		50	_	10.6	16.3	1.0	18.5	
(G1-Y)			50+05	15	1	5.4	8.1	1.0	9.5	
			5.0 ± 0.5	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	1.0	11.5				
		_	3.3 ± 0.3	15	_	8.1	12.3	1.0	14.5	ns
Propagation delay time	t _{pLH} t _{pHL}			50	_	10.6	15.8	1.0	18.0	
(G 2 -Y)			5.0 ± 0.5	15	_	5.7	8.1	1.0	9.5	
				50	_	7.2	10.1	1.0	11.5	
Input capacitance	CIN					4	10	_	10	pF
Power dissipation capacitance	C _{PD}			(Note)		37	_	_	_	pF

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

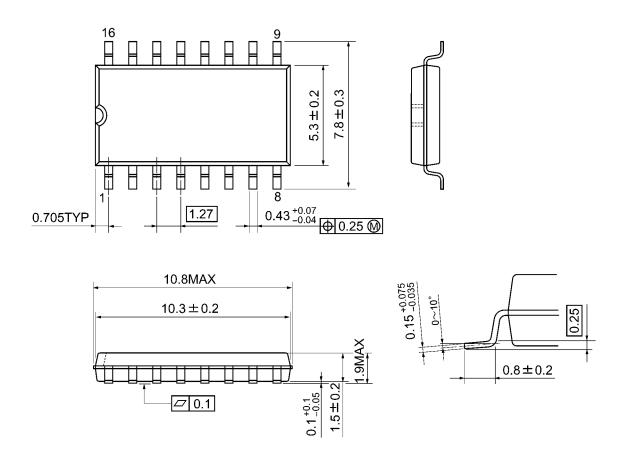
Input Equivalent Circuit





Package Dimensions

SOP16-P-300-1.27A Unit: mm

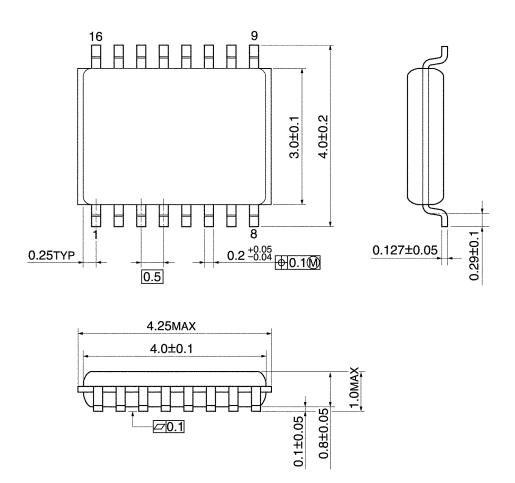


Weight: 0.18 g (typ.)



Package Dimensions

VSSOP16-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)



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