

CMOS Digital Integrated Circuits Silicon Monolithic

TC74VCX257FT

1. Functional Description

· Low-Voltage Quad 2-Channel Multiplexer with 3.6-V Tolerant Inputs and Outputs

2. General

The TC74VCX257FT is a high performance CMOS multiplexer which is guaranteed to operate from 1.2 V to 3.6 V. Designed for use in 1.5 V, 1.8 V, 2.5 V or 3.3 V systems, it achieves high speed operation while maintaining the CMOS low power dissipation.

It is also designed with over voltage tolerant inputs and outputs up to 3.6 V.

It consists of four 2-input digital multiplexers with common select and output enable inputs.

If \overline{OE} is set high the outputs are held in a high-impedance state. The SELECT decoding determines whether the A or B inputs get routed to their corresponding Y outputs.

All inputs are equipped with protection circuits against static discharge.

3. Features

- (1) Wide operating temperature range: $T_{opr} = -40$ to 125 °C (Note 1)
- (2) Low-voltage operation: $V_{CC} = 1.2$ to 3.6 V
- (3) High-speed operation: $t_{pd} = 3.0 \text{ ns (max)} (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$

$$t_{pd} = 4.0 \text{ ns (max) (V}_{CC} = 2.3 \text{ to } 2.7 \text{ V})$$

$$t_{pd} = 8.0 \text{ ns (max)} (V_{CC} = 1.65 \text{ to } 1.95 \text{ V})$$

$$t_{pd} = 16.0 \text{ ns (max) (V}_{CC} = 1.4 \text{ to } 1.6 \text{ V})$$

$$t_{pd} = 40.0 \text{ ns (max) (V}_{CC} = 1.2 \text{ V})$$

(4) Output current: $I_{OH}/I_{OL} = \pm 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$

$$I_{OH}/I_{OL} = \pm 18 \text{ mA (min) (V}_{CC} = 2.3 \text{ V)}$$

$$I_{OH}/I_{OL} = \pm 6 \text{ mA (min) (V}_{CC} = 1.65 \text{ V)}$$

$$I_{OH}/I_{OL}$$
 = ±2 mA (min) (V_{CC} = 1.4 V)

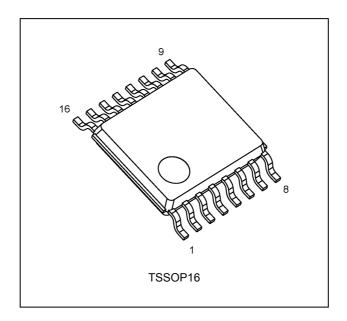
(5) 3.6 V tolerant function and power-down protection provided on all inputs and outputs.

Note 1: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.

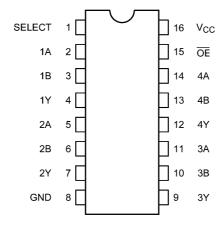
Start of commercial production



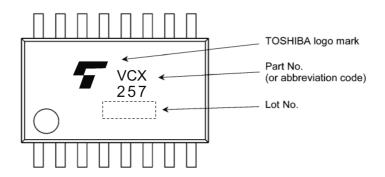
4. Packaging



5. Pin Assignment

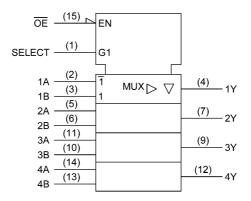


6. Marking





7. IEC Logic Symbol



8. Truth Table

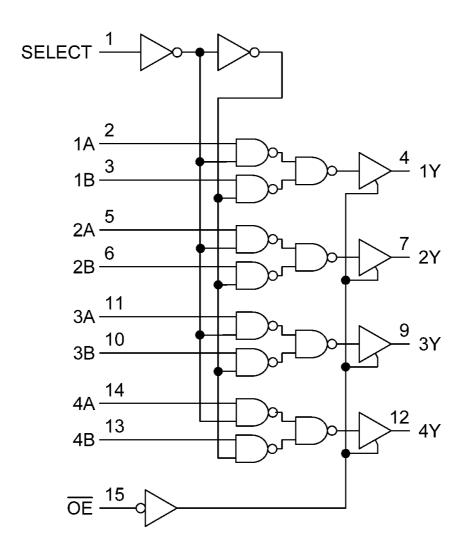
Input OE	Input SELECT	Inputs A	Inputs B	Outputs Y
Н	X	X	Х	Z
L	L	L	X	L
L	L	Н	Х	Н
L	Н	X	L	L
L	Н	Х	Н	Н

X: Don't care

Z: High impedance



9. System Diagram





10. Absolute Maximum Ratings (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CC}		-0.5 to 4.6	V
Input voltage	V _{IN}		-0.5 to 4.6	V
Output voltage	V _{OUT}	(Note 1)	-0.5 to 4.6	V
		(Note 2)	-0.5 to V _{CC} + 0.5	
Input diode current	I _{IK}		-50	mA
Output diode current	I _{OK}	(Note 3)	±50	mA
Output current	I _{OUT}		±50	mA
Power dissipation	P _D	(Note 4)	180	mW
V _{CC} /ground current	I _{CC} /I _{GND}		±100	mA
Storage temperature	T _{stg}		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0 V$

Note 2: High (H) or Low (L) state. I_{OUT} absolute maximum rating must be observed.

Note 3: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$

Note 4: 180 mW in the range of T_a = -40 to 85 °C. From T_a = 85 to 125 °C a derating factor of -3.25 mW/°C shall be applied until 50 mW.

11. Operating Ranges (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CC}		1.2 to 3.6	V
Input voltage	V_{IN}		-0.3 to 3.6	V
Output voltage	V _{OUT}	(Note 1)	0 to 3.6	V
		(Note 2)	0 to V _{CC}	
Output current	I_{OH},I_{OL}	(Note 3)	±24	mA
		(Note 4)	±18	mA
		(Note 5)	±6	
		(Note 6)	±2	
Operating temperature	T _{opr}	(Note 7)	-40 to 125	°C
Input rise and fall times	dt/dv	(Note 8)	0 to 10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Note 1: Output in OFF state.

Note 2: High (H) or Low (L) state.

Note 3: V_{CC} = 3.0 to 3.6 V

Note 4: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 5: V_{CC} = 1.65 to 1.95 V

Note 6: $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$

Note 7: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.

Note 8: V_{IN} = 0.8 to 2.0 V , V_{CC} = 3.0 V



12. Electrical Characteristics

12.1. DC Characteristics (Unless otherwise specified, T_a = -40 to 85 °C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	_		1.2 to 1.4	$V_{CC} \times 0.8$	_	V
				1.4 to 1.65	V _{CC} × 0.65	_	
				1.65 to 2.3	V _{CC} × 0.65	_	
				2.3 to 2.7	1.6	_	
				2.7 to 3.6	2.0	_	
Low-level input voltage	V _{IL}	_		1.2 to 1.4	_	V _{CC} × 0.05	V
				1.4 to 1.65	_	$V_{CC} \times 0.05$	
				1.65 to 2.3	_	V _{CC} × 0.2	
				2.3 to 2.7	_	0.7	
				2.7 to 3.6	_	0.8	
High-level output voltage	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -100 μA	1.2	V _{CC} - 0.1	_	V
				1.4 to 1.65	V _{CC} - 0.2	_	
				1.65 to 3.6	V _{CC} - 0.2	_	
			I _{OH} = -2 mA	1.4	1.05	_	
			I _{OH} = -6 mA	1.65	1.25	_	
				2.3	2.0	_	
			I _{OH} = -12 mA	2.3	1.8	_	
				2.7	2.2	_	
			I _{OH} = -18 mA	2.3	1.7	_	
				3.0	2.4	_	
			I _{OH} = -24 mA	3.0	2.2	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 100 μA	1.2	_	0.05	V
				1.4 to 1.65	_	0.05	
				1.65 to 3.6	_	0.2	
			I _{OL} = 2 mA	1.4	_	0.35	
			I _{OL} = 6 mA	1.65	_	0.3	
			I _{OL} = 12 mA	2.3	_	0.4	
				2.7	_	0.4	
			I _{OL} = 18 mA	2.3	_	0.6	
				3.0	_	0.4	
			I _{OL} = 24 mA	3.0	_	0.55	
Input leakage current	I _{IN}	V _{IN} = 0 to 3.6 V		1.2 to 3.6	_	±5.0	μΑ
3-state output OFF-state leakage current	I _{OZ}	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = 0 \text{ to } 3.6 \text{ V}$		1.2 to 3.6	I	±10.0	μА
Power-OFF leakage current	l _{OFF}	$V_{IN}/V_{OUT} = 0$ to 3.6 V		0		10.0	μΑ
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		1.2 to 3.6		20.0	μА
		$V_{CC} \le (V_{IN}/V_{OUT}) \le 3.6 \text{ V}$		1.2 to 3.6		±20.0	
	Δl _{CC}	V _{IH} = V _{CC} - 0.6 V (per 1 input)		2.7 to 3.6	_	750	μА



12.2. DC Characteristics (Note) (Unless otherwise specified, T_a = -40 to 125 °C)

Characteristics	Symbol	Test Condition	1	V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	_		1.2 to 1.4	$V_{CC} \times 0.8$	_	V
				1.4 to 1.65	V _{CC} × 0.65		
				1.65 to 2.3	V _{CC} × 0.65	_	
				2.3 to 2.7	1.6	_	
				2.7 to 3.6	2.0	_	
Low-level input voltage	V _{IL}	_		1.2 to 1.4	_	V _{CC} × 0.05	V
				1.4 to 1.65	_	V _{CC} × 0.05	
				1.65 to 2.3	_	V _{CC} × 0.2	
				2.3 to 2.7	_	0.7	
				2.7 to 3.6	_	0.8	
High-level output voltage	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -100 μA	1.2	V _{CC} - 0.1	_	V
				1.4 to 1.65	V _{CC} - 0.2	_	
				1.65 to 3.6	V _{CC} - 0.2	_	
			I _{OH} = -2 mA	1.4	1.05	_	
			I _{OH} = -6 mA	1.65	1.25		
				2.3	2.0	_	
			I _{OH} = -12 mA	2.3	1.8	_	
				2.7	2.2		
			I _{OH} = -18 mA	2.3	1.6		
				3.0	2.4	_	
			I _{OH} = -24 mA	3.0	2.2		
Low-level output voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 100 μA	1.2	_	0.05	٧
				1.4 to 1.65	_	0.05	
				1.65 to 3.6	_	0.2	
			I _{OL} = 2 mA	1.4	_	0.35	
			I _{OL} = 6 mA	1.65	_	0.3	
			I _{OL} = 12 mA	2.3	_	0.4	
				2.7	_	0.4	
			I _{OL} = 18 mA	2.3	_	0.8	
				3.0	_	0.4	
			I _{OL} = 24 mA	3.0	_	0.55	
Input leakage current	I _{IN}	V _{IN} = 0 to 3.6 V	•	1.2 to 3.6	_	±20.0	μА
3-state output OFF-state leakage current	I _{OZ}	V _{IN} = V _{IH} or V _{IL} V _{OUT} = 0 to 3.6 V		1.2 to 3.6	_	±40.0	μА
Power-OFF leakage current	I _{OFF}	$V_{IN}/V_{OUT} = 0$ to 3.6 V		0	_	40.0	μΑ
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		1.2 to 3.6	_	80.0	μΑ
		$V_{CC} \le (V_{IN}/V_{OUT}) \le 3.6 \text{ V}$,	1.2 to 3.6	_	±80.0	
	Δl _{CC}	V _{IH} = V _{CC} - 0.6 V (per 1 input)		2.7 to 3.6	_	1.5	mA

Note: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.

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12.3. AC Characteristics (Unless otherwise specified, T_a = -40 to 85 °C)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}		See 12.7 AC Test Circuit,	1.2	3.0	40.0	ns
(A,B - Y)			Table 12.7.1, Fig. 12.8.1, Table 12.8.1	1.5 ± 0.1	2.0	16.0	
			Table 12.0.1	1.8 ± 0.15	1.5	8.0	
				2.5 ± 0.2	0.8	4.0	
				3.3 ± 0.3	0.6	3.0	
Propagation delay time	t _{PLH} ,t _{PHL}		See 12.7 AC Test Circuit,	1.2	3.0	48.0	ns
(SELECT - Y)			Table 12.7.1, Fig. 12.8.1, Table 12.8.1	1.5 ± 0.1	2.0	19.2	
			Table 12.0.1	1.8 ± 0.15	1.5	9.6	
				2.5 ± 0.2	0.8	4.8	
				3.3 ± 0.3	0.6	4.0	
3-state output enable time	t _{PZL} ,t _{PZH}		See 12.7 AC Test Circuit, Table 12.7.1, Fig. 12.8.2, Table 12.8.1	1.2	3.0	46.0	ns
				1.5 ± 0.1	2.0	18.4	
				1.8 ± 0.15	1.5	9.2	
				2.5 ± 0.2	0.8	4.6	
				3.3 ± 0.3	0.6	3.5	
3-state output disable time	t_{PLZ}, t_{PHZ}		See 12.7 AC Test Circuit,	1.2	3.0	34.0	ns
			Table 12.7.1, Fig. 12.8.2, Table 12.8.1	1.5 ± 0.1	2.0	13.6	
			Table 12.6.1	1.8 ± 0.15	1.5	6.8	
				2.5 ± 0.2	0.8	3.8	
				3.3 ± 0.3	0.6	3.5	
Output skew	t _{osLH} ,t _{osHL}	(Note 1)	_	1.2	_	1.5	ns
				1.5 ± 0.1	_	1.5	
				1.8 ± 0.15		0.5	
				2.5 ± 0.2	1	0.5	
				3.3 ± 0.3	_	0.5	

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Note 1: Parameter guaranteed by design. ($t_{osLH} = |t_{PLH}m - t_{PLH}n|$, $t_{osHL} = |t_{PHL}m - t_{PHL}n|$)



12.4. AC Characteristics (Note) (Unless otherwise specified, T_a = -40 to 125 °C)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}		See 12.7 AC Test Circuit,	1.2	3.0	52.0	ns
(A,B - Y)			Table 12.7.1, Fig. 12.8.1, Table 12.8.1	1.5 ± 0.1	2.0	20.4	
			Table 12.0.1	1.8 ± 0.15	1.5	9.5	
				2.5 ± 0.2	0.8	4.8	
				3.3 ± 0.3	0.6	3.6	
Propagation delay time	t _{PLH} ,t _{PHL}		See 12.7 AC Test Circuit,	1.2	3.0	62.4	ns
(SELECT - Y)			Table 12.7.1, Fig. 12.8.1, Table 12.8.1	1.5 ± 0.1	2.0	24.4	
			Table 12.0.1	1.8 ± 0.15	1.5	11.4	
				2.5 ± 0.2	0.8	5.7	
				3.3 ± 0.3	0.6	4.8	
3-state output enable time	t_{PZL}, t_{PZH}		See 12.7 AC Test Circuit, Table 12.7.1, Fig. 12.8.2, Table 12.8.1	1.2	3.0	60.0	ns
				1.5 ± 0.1	2.0	23.4]
				1.8 ± 0.15	1.5	10.9	
				2.5 ± 0.2	0.8	5.5]
				3.3 ± 0.3	0.6	4.2	
3-state output disable time	t_{PLZ}, t_{PHZ}		See 12.7 AC Test Circuit,	1.2	3.0	45.0	ns
			Table 12.7.1, Fig. 12.8.2, Table 12.8.1	1.5 ± 0.1	2.0	17.3	
			Table 12.6.1	1.8 ± 0.15	1.5	8.1	
				2.5 ± 0.2	0.8	4.5	
				3.3 ± 0.3	0.6	4.2	
Output skew	t _{osLH} ,t _{osHL}	(Note 1)	_	1.2	_	2.0	ns
				1.5 ± 0.1	_	2.0	
				1.8 ± 0.15	-	1.0	
				2.5 ± 0.2		1.0	
				3.3 ± 0.3	1	1.0	

Note: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.

Note 1: Parameter guaranteed by design. $(t_{osLH} = |t_{PLH}m-t_{PLH}n|, t_{osHL} = |t_{PHL}m-t_{PHL}n|)$

12.5. Dynamic Switching Characteristics (Note) (Unless otherwise specified, $T_a = 25$ °C, Input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic V _{OL}	V _{OLP}	V _{IH} = 1.8 V, V _{IL} = 0 V	1.8	0.25	V
		V _{IH} = 2.5 V, V _{IL} = 0 V	2.5	0.6	
		$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	
Quiet output minimum dynamic V _{OL}	V _{OLV}	V _{IH} = 1.8 V, V _{IL} = 0 V	1.8	-0.25	V
		V _{IH} = 2.5 V, V _{IL} = 0 V	2.5	-0.6	
		V _{IH} = 3.3 V, V _{IL} = 0 V	3.3	-0.8	
Quiet output minimum dynamic V _{OH}	V _{OHV}	V _{IH} = 1.8 V, V _{IL} = 0 V	1.8	1.5	V
		V _{IH} = 2.5 V, V _{IL} = 0 V	2.5	1.9	
		V _{IH} = 3.3 V, V _{IL} = 0 V	3.3	2.2	

Note: Parameter guaranteed by design.

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12.6. Capacitive Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}		_	1.8, 2.5, 3.3	6	pF
Output capacitance	C _{OUT}		_	1.8, 2.5, 3.3	7	pF
Power dissipation capacitance	C _{PD}	(Note 1)	f _{IN} = 10 MHz	1.8, 2.5, 3.3	20	pF

Note 1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.

$$I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{IN} + I_{CC}/4$$
 (per 1 gate)

12.7. AC Test Circuit

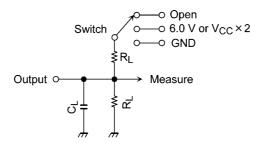


Table 12.7.1 Parameter for AC Test Circuit

Parameter	Switch	Test Condition
t _{PLH} , t _{PHL}	OPEN	_
t_{PLZ} , t_{PZL}	6.0 V	V_{CC} = 3.3 ± 0.3 V
	V _{CC} × 2	V_{CC} = 2.5 ± 0.2 V
		V _{CC} = 1.8 ± 0.15 V
		V _{CC} = 1.5 ± 0.1 V
		V _{CC} = 1.2 V
t _{PHZ} , t _{PZH}	GND	_



12.8. AC Waveform

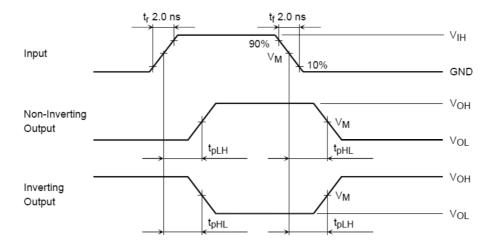


Fig. 12.8.1 t_{PLH}, t_{PHL}

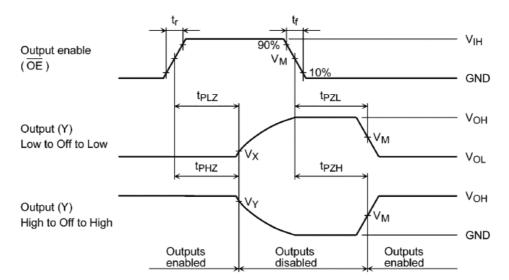


Fig. 12.8.2 t_{PLZ}, t_{PHZ}, t_{PZL}, t_{PZH}

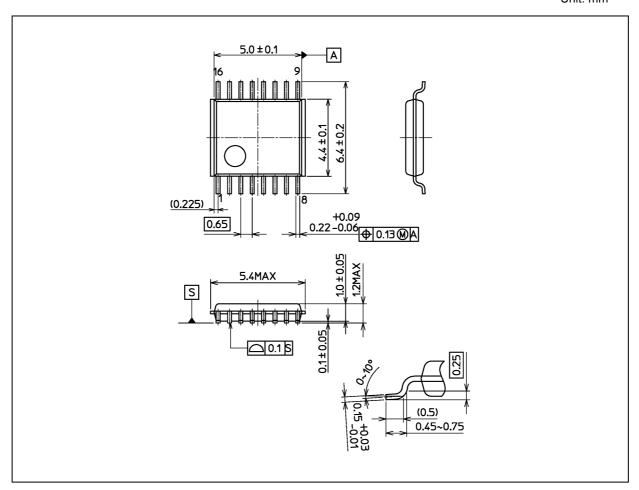
Table 12.8.1 AC Waveform Symbols

	Symbol	V_{CC} = 3.3 \pm 0.3 V	V_{CC} = 2.5 ± 0.2 V V_{CC} = 1.8 ± 0.15 V	$V_{CC} = 1.5 \pm 0.1 \text{ V}$ $V_{CC} = 1.2 \text{ V}$
Input	V _{IH}	2.7 V	V _{CC}	V _{CC}
	V_{M}	1.5 V	V _{CC} /2	V _{CC} /2
	t _r , t _f	2.0 ns	2.0 ns	2.0 ns
Output	V_{M}	1.5 V	V _{CC} /2	V _{CC} /2
	V _X	V _{OL} + 0.3 V	V _{OL} + 0.15 V	V _{OL} + 0.15 V
	V_{Y}	V _{OH} - 0.3 V	V _{OH} - 0.15 V	V _{OH} - 0.15 V
Load	C _L	30 pF	30 pF	15 pF
	R_L	500 Ω	500 Ω	2 kΩ



Package Dimensions

Unit: mm



Weight: 0.06 g (typ.)

	Package Name(s)				
Nickname: TSSOP16	Nickname: TSSOP16				



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