

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX14F, TC74LCX14FK

Low-Voltage Hex Schmitt Inverter with 5-V Tolerant Inputs and Outputs

The TC74LCX14 is a high-performance CMOS Schmitt inverter. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

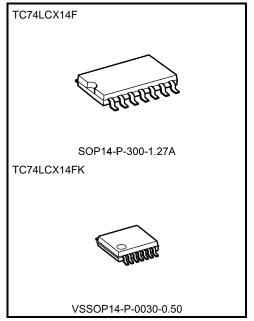
The device is designed for low-voltage (3.3 V) V_{CC} applications, but it could be used to interface to 5-V supply environment for inputs.

Pin configuration and function are the same as the TC74LCX04 but the inputs have hysteresis and with Schmitt trigger function, the TC74LCX14 can be used as line receivers which will receive slow input signals.

All inputs are equipped with protection circuits against static discharge.

Features

- Low-voltage operation: VCC = 1.65 to 3.6 V
- High-speed operation: $t_{pd} = 6.5 \text{ ns (max) (V}_{CC} = 3.0 \text{ to } 3.6 \text{ V)}$
- Ouput current: $|I_{OH}|/I_{OL} = 24 \text{ mA (min) (V}_{CC} = 3.0 \text{ V})$
- Available in JEITA SOP, VSSOP (US)
- · Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 14 type



Weight

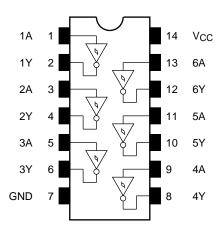
SOP14-P-300-1.27A : 0.18 g (typ.) VSSOP14-P-0030-0.50 : 0.02 g (typ.)

Note: The Electrical Characteristics of V_{CC} = 1.8 \pm 0.15 V is only applicable for products which manufactured from January 2009 onward.

Start of commercial production 1995-02



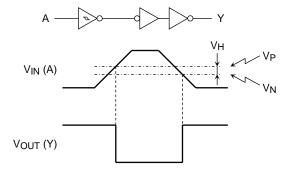
Pin Assignment (top view)



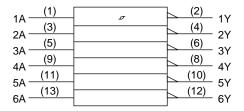
Truth Table

Inputs	Outputs
А	Y
L	Н
Н	L

System Diagram and waveform



IEC Logic Symbol





Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	nbol Rating	
Power supply voltage	Vcc	-0.5 to 7.0	V
DC input voltage	VIN	-0.5 to 7.0	V
		-0.5 to 7.0 (Note 2)	
DC output voltage	Vout	-0.5 to V _{CC} + 0.5 (Note 3)	V
Input diode current	lıĸ	-50	mA
Output diode current	lok	±50 (Note 4)	mA
DC output current	lout	±50	mA
Power dissipation	PD	180	mW
DC Vcc/ground current	ICC/IGND	±100	mA
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: VCC = 0 V

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: Vout < GND, Vout > Vcc

Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit	
Dower oupply voltage	\/oo	1.65 to 3.6	V	
Power supply voltage	Vcc	1.5 to 3.6 (Note 2)		
Input voltage	VIN	0 to 5.5	V	
Output voltage	Vout	0 to 5.5 (Note 3)	V	
Output voltage		0 to VCC (Note 4)		
Output ourropt	IOH/IOL	±24 (Note 5)	mA	
Output current	IOH/IOL	±12 (Note 6)	ША	
Operating temperature	Topr	-40 to 85	°C	

Note 1: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.

Note 2: Data retention only

Note 3: VCC = 0 V

Note 4: High or low state Note 5: VCC = 3.0 to 3.6 V Note 6: VCC = 2.7 to 3.0 V



Electrical Characteristics

DC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Characteristics		Symbol	Test Condi	tion		Min	Max	Unit
	T	ĺ		Vcc				
					1.65	0.7	1.35	
	H-level	VP	_		2.3	0.95	1.7	
Threshold voltage					3.0	1.2	2.2	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
Threshold voltage					1.65	0.3	0.8	V
	L-level	VN	_		2.3	0.45	1.15	
					3.0	0.6	1.5	
					1.65	0.3	0.8	
Hysteresis voltage		VH	_		2.3	0.35	1.0	V
					3.0	0.4	1.2	
				I _{OH} = -100 μA	1.65 to 3.6	V _{CC} -0.2		
		Vон	$V_{IN} = V_{IL}$	I _{OH} = -4 mA	1.65	1.05	_	- V
	l			IOH = -8 mA	2.3	1.7	_	
	H-level			I _{OH} = -12 mA	2.7	2.2	_	
				IOH = -18 mA	3.0	2.4	_	
Outrout walts as				I _{OH} = -24 mA	3.0	2.2	_	
Output voltage				$I_{OL} = 100 \mu A$	1.65 to 3.6	_	0.2	
				IOL = 4 mA	1.65	_	0.45	
	1.11	1/	Maria Mari	I _{OL} = 8 mA	2.3	_	0.7	
	L-level	VoL	VIN = VIH	I _{OL} = 12 mA	2.7	_	0.4	
				I _{OL} = 16 mA	3.0	_	0.4	
				I _{OL} = 24 mA	3.0	_	0.55	
Input leakage currer	nt	liN	V _{IN} = 0 to 5.5 V		1.65 to 3.6	_	±5.0	μΑ
Power-off leakage c	urrent	loff	V _{IN} /V _{OUT} = 5.5 V		0	_	10.0	μΑ
		laa	ICC $\frac{V_{IN} = V_{CC} \text{ or GND}}{V_{IN} = 3.6 \text{ to } 5.5 \text{ V}}$		1.65 to 3.6	_	10.0	
Quiescent supply cu	Quiescent supply current				1.65 to 3.6	_	±10.0	μА
Increase in I _{CC} per i	nput	Δlcc	V _{IH} = V _{CC} - 0.6 V (per 1	V _{IH} = V _{CC} - 0.6 V (per 1 input)			500	



AC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Characteristics	Symbol	Test Condition Vcc (V)		Min	Max	Unit
			1.8 ± 0.15		25.0	ns
Propagation delay time	tpLH tpHL	Figure 4 Figure 2	2.5 ± 0.2		8.5	
		Figure 1, Figure 2	2.7		7.5	
			3.3 ± 0.3	1.5	6.5	
Output to output skew	tosLH tosHL	(Nlata)	2.7	_	_	20
Output to output skew		(Note)	3.3 ± 0.3		1.0	ns

Note: Parameter guaranteed by design.

(tosLH = |tpLHm - tpLHn|, tosHL = |tpHLm - tpHLn|)

Dynamic Switching Characteristics (Ta = 25°C, input: tr = tf = 2.5 ns, CL = 50 pF, RL = 500 Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic VoL	VOLP	V _{IH} = 3.3 V, V _{IL} = 0 V	3.3	0.8	V
Quiet output minimum dynamic V _{OL}	Volv	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	CIN	_	3.3	7	pF
Output capacitance	Соит	_	0	8	pF
Power dissipation capacitance	CPD	f _{IN} = 10 MHz (Not	9) 3.3	25	pF

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $ICC (opr) = CPD \cdot VCC \cdot fIN + ICC/6 (per gate)$



AC Test Circuit

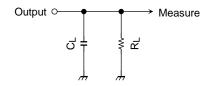


Figure 1

AC Waveform

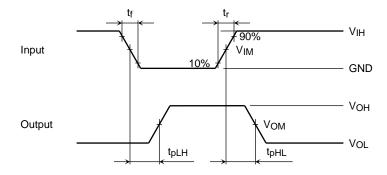


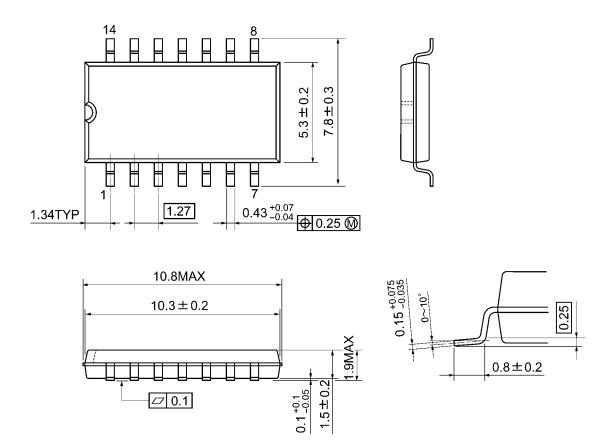
Figure 2 t_{pLH}, t_{pHL}

		Vcc				
	Symbol	$\begin{array}{c c} 3.3 \pm 0.3 \ V \\ 2.7 \ V \end{array} \hspace{1cm} 2.5 \pm 0.2 \ V$		1.8 ± 0.15 V		
Input	VIH	2.7 V	Vcc	Vcc		
	V _{IM}	1.5 V	V _{CC} /2	V _{CC} /2		
	t _r , t _f	2.5 ns	2.0 ns	2.0 ns		
Output	V _{OM}	1.5 V	V _{OH} /2	V _{OH} /2		
Load	CL	50 pF	30 pF	30 pF		
	RL	500 Ω	500 Ω	1 kΩ		



Package Dimensions

SOP14-P-300-1.27A Unit: mm

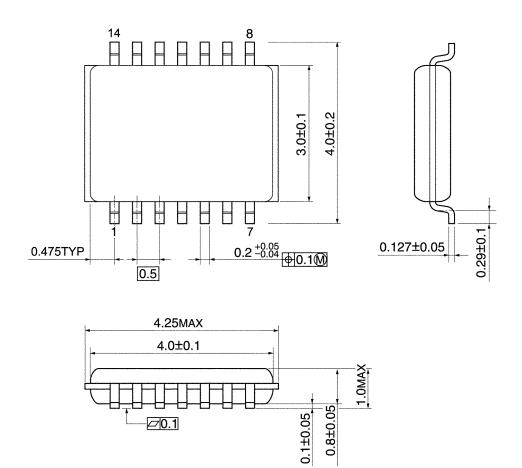


Weight: 0.18 g (typ.)



Package Dimensions

VSSOP14-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)



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