

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HCT540AP, TC74HCT540AF TC74HCT541AP, TC74HCT541AF

Octal Bus Buffer with TTL Input Level

TC74HCT540AP/AF	Inverting, 3-State Outputs
TC74HCT541AP/AF	Non-Inverting, 3-State Outputs

The TC74HCT540A/TC74HCT541A are high speed CMOS OCTAL BUS BUFFERS fabricated with silicon gate C²MOS technology.

These devices may be used as a level converter for interfacing TTL or NMOS to High Speed CMOS. The inputs are compatible with TTL, NMOS and CMOS output voltage levels.

They achieve the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

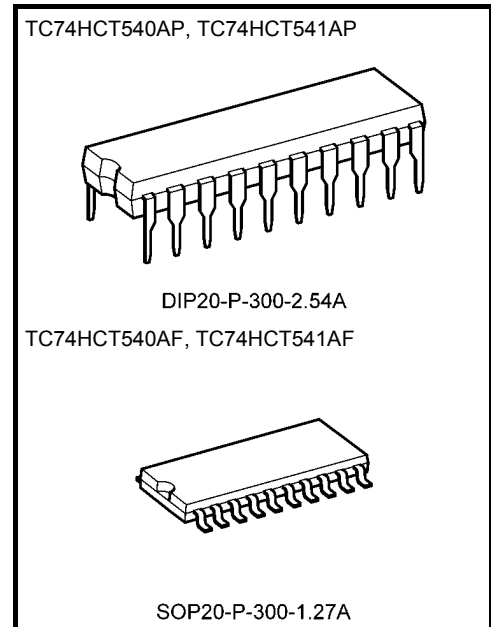
The TC74HCT540A is an inverting type, and the TC74HCT541A is a non-inverting type.

When either $\overline{G1}$ or $\overline{G2}$ are high, the terminal outputs are in the high-impedance state.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

- High speed: $t_{pd} = 10 \text{ ns}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 4 \mu\text{A}$ (max) at $T_a = 25^\circ\text{C}$
- Compatible with TTL outputs: $V_{IL} = 0.8 \text{ V}$ (max)
 $V_{IH} = 2.0 \text{ V}$ (min)
- Wide interfacing ability: LSTTL, NMOS, CMOS
- Output drive capability: 15 LSTTL loads
- Symmetrical output impedance: $|I_{OH}| = I_{OL} = 6 \text{ mA}$ (min)
- Balanced propagation delays: $t_{pLH} \approx t_{pHL}$
- Pin and function compatible with 74LS540/541



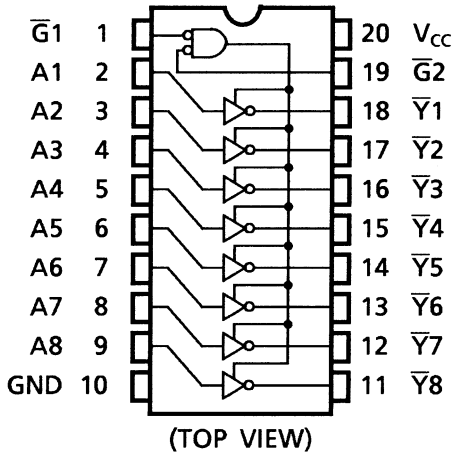
Weight

DIP20-P-300-2.54A	: 1.30 g (typ.)
SOP20-P-300-1.27A	: 0.22 g (typ.)

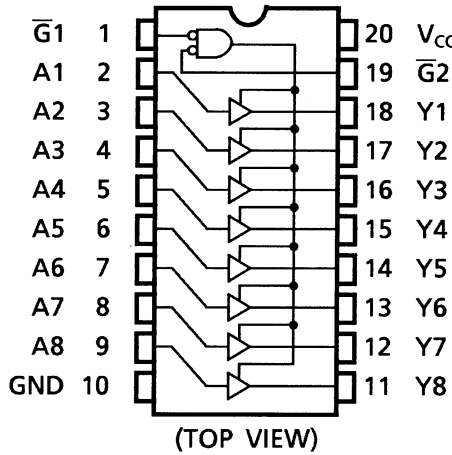
Start of commercial production
1988-11

Pin Assignment

TC74HCT540A

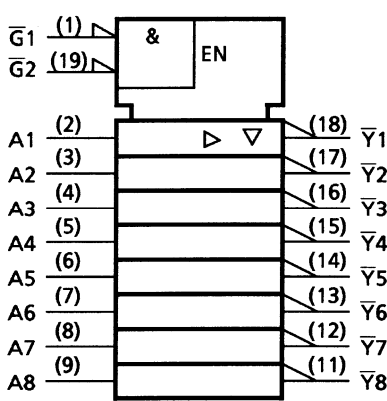


TC74HCT541A

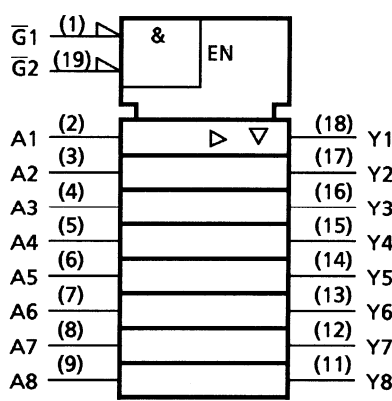


IEC Logic Symbol

TC74HCT540A



TC74HCT541A



Truth Table

Inputs			Outputs	
G1	G2	An	Yn*	Yn *
H	X	X	Z	Z
X	H	X	Z	Z
L	L	H	H	L
L	L	L	L	H

X: Don't care

Z: High impedance

*: Yn..... HCT541A

Yn HCT540A

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V_{CC}	-0.5 to 7	V
DC input voltage	V_{IN}	-0.5 to $V_{CC} + 0.5$	V
DC output voltage	V_{OUT}	-0.5 to $V_{CC} + 0.5$	V
Input diode current	I_{IK}	± 20	mA
Output diode current	I_{OK}	± 20	mA
DC output current	I_{OUT}	± 35	mA
DC V_{CC} /ground current	I_{CC}	± 75	mA
Power dissipation	P_D	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T_{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 2: 500 mW in the range of $T_a = -40$ to 65°C . From $T_a = 65$ to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V_{CC}	4.5 to 5.5	V
Input voltage	V_{IN}	0 to V_{CC}	V
Output voltage	V_{OUT}	0 to V_{CC}	V
Operating temperature	T_{opr}	-40 to 85	°C
Input rise and fall time	t_r, t_f	0 to 500	ns

Note: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either V_{CC} or GND.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit	
				VCC (V)	Min	Typ.	Max	Min		Max
High-level input voltage	VIH	—		4.5 to 5.5	2.0	—	—	2.0	—	V
Low-level input voltage	VIL	—		4.5 to 5.5	—	—	0.8	—	0.8	V
High-level output voltage	VOH	VIN = VIH or VIL	I _{OH} = -20 μA	4.5	4.4	4.5	—	4.4	—	V
			I _{OH} = -6 mA	4.5	4.18	4.31	—	4.13	—	
Low-level output voltage	VOL	VIN = VIH or VIL	I _{OL} = 20 μA	4.5	—	0.0	0.1	—	0.1	V
			I _{OL} = 6 mA	4.5	—	0.17	0.26	—	0.33	
3-state output off-state current	I _{OZ}	VIN = VIH or VIL VOUT = VCC or GND		5.5	—	—	±0.5	—	±5.0	μA
Input leakage current	I _{IN}	VIN = VCC or GND		5.5	—	—	±0.1	—	±1.0	μA
Quiescent supply current	I _{CC}	VIN = VCC or GND		5.5	—	—	4.0	—	40.0	μA
	I _C	Per input: VIN = 0.5 V or 2.4 V Other input: VCC or GND		5.5	—	—	2.0	—	2.9	mA

AC Characteristics (input: $t_r = t_f = 6 \text{ ns}$)

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = −40 to 85°C		Unit
			CL (pF)	VCC (V)	Min	Typ.	Max	Min	Max	
Output transition time	t _{TLH} t _{THL}	—	50	4.5 5.5	— —	7 6	12 11	— —	15 14	ns
Propagation delay time (TC74HCT540A)	t _{pLH} t _{pHL}	—	50 150	4.5 5.5 4.5 5.5	— — — —	12 9 17 14	20 18 26 24	— — — —	25 23 33 30	ns
Propagation delay time (TC74HCT541A)	t _{pLH} t _{pHL}	—	50 150	4.5 5.5 4.5 5.5	— — — —	14 11 19 16	23 21 29 27	— — — —	29 27 36 33	ns
Output enable time	t _{pZL} t _{pZH}	R _L = 1 kΩ	50 150	4.5 5.5 4.5 5.5	— — — —	18 16 23 21	30 27 36 33	— — — —	38 35 45 41	ns
Output disable time	t _{pLZ} t _{pHZ}	R _L = 1 kΩ	50	4.5 5.5	— —	18 16	30 27	— —	38 35	ns
Input capacitance	C _{IN}	—			—	5	10	—	10	pF
Output capacitance	C _{OUT}	—			—	10	—	—	—	pF
Power dissipation capacitance	C _{PD} (Note)	TC74HCT540A			—	35	—	—	—	pF
		TC74HCT541A			—	31	—	—	—	

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

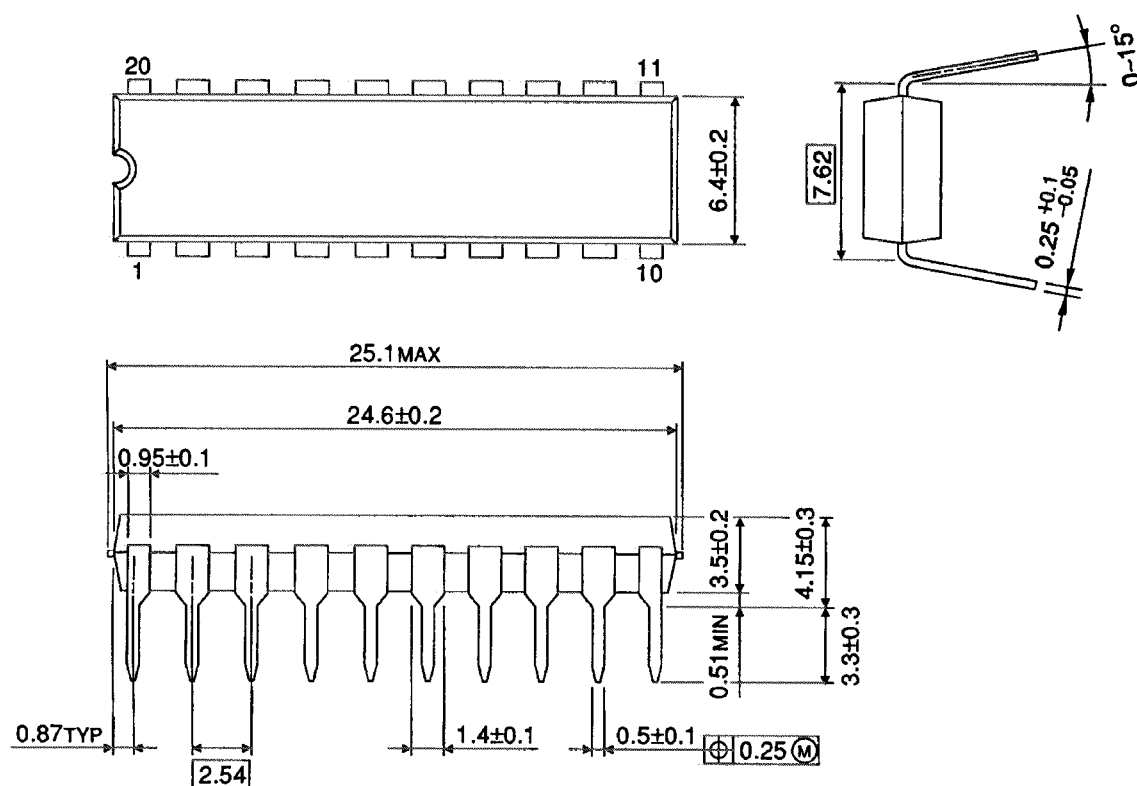
Average operating current can be obtained by the equation:

$$I_{CC}(\text{opr}) = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8 \text{ (per bit)}$$

Package Dimensions

DIP20-P-300-2.54A

Unit : mm

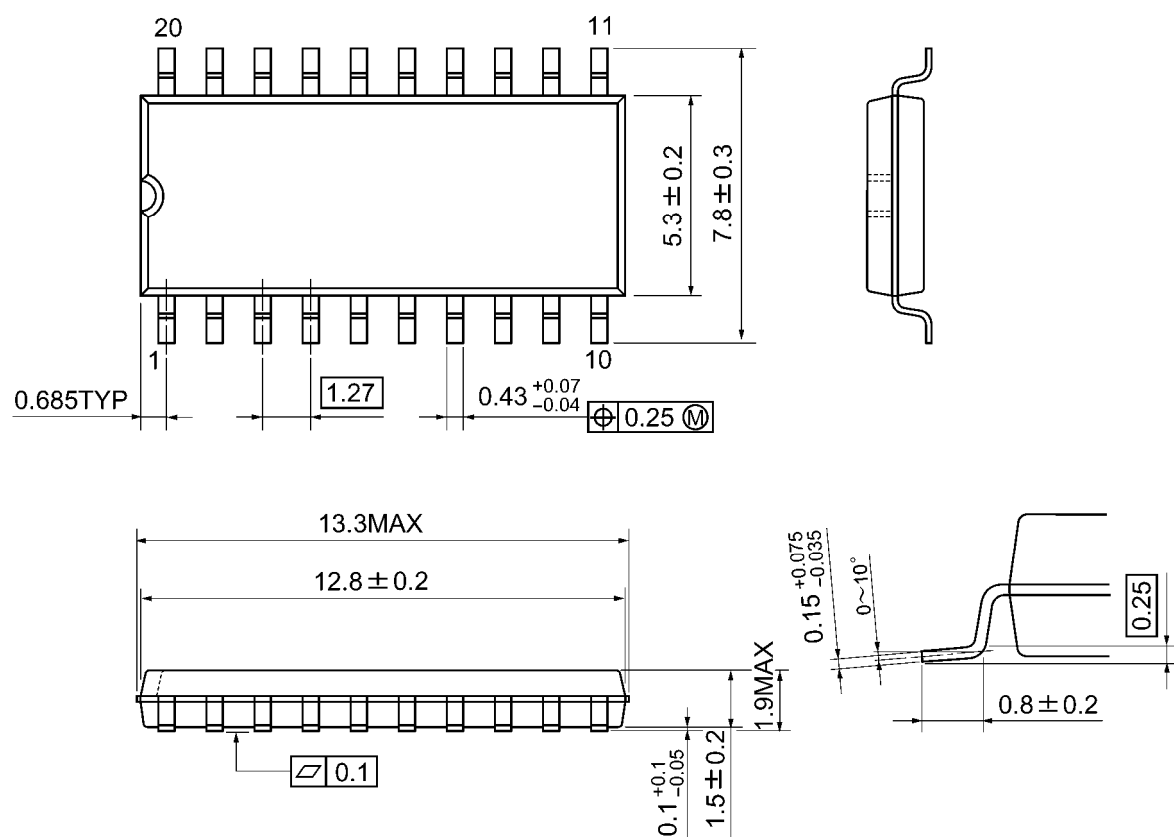


Weight: 1.30 g (typ.)

Package Dimensions

SOP20-P-300-1.27A

Unit: mm



Weight: 0.22 g (typ.)

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