TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74HC540AP, TC74HC540AF TC74HC541AP, TC74HC541AF

Octal Bus Buffer

TC74HC540AP/AF

TC74HC541AP/AF

Inverting, 3-State Outputs Non-Inverting, 3-State Outputs

The TC74HC540A/TC74HC541A are high speed CMOS OCTAL BUS BUFFERs fabricated with silicon gate  $\rm C^2MOS$  technology.

They achieve the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

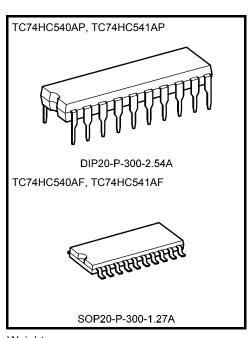
The TC74HC540A is an inverting type, and the TC74HC541A is a non-inverting type.

When either  $\overline{\overline{G1}}$  or  $\overline{G2}$  are high, the terminal outputs are in the high-impedance state.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

## Features

- High speed:  $t_{pd} = 10 \text{ ns}$  (typ.) at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $I_{CC} = 4 \ \mu A \ (max)$  at  $Ta = 25^{\circ}C$
- High noise immunity:  $V_{NIH} = V_{NIL} = 28\% V_{CC}$  (min)
- Output Drive Capability: 15 LSTTL loads
- Symmetrical output impedance: |I<sub>OH</sub>| = I<sub>OL</sub> = 6 mA (min)
- Balanced propagation delays:  $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range:  $V_{CC}$  (opr) = 2 to 6 V
- Pin and function compatible with 74LS540/541



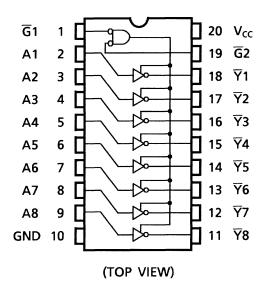
Weight DIP20-P-300-2.54A SOP20-P-300-1.27A

: 1.30 g (typ.) : 0.22 g (typ.)

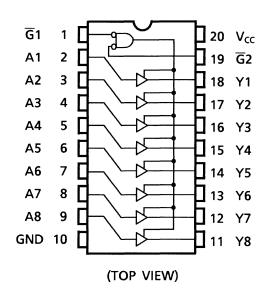
## Pin Assignment

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#### TC74HC540A

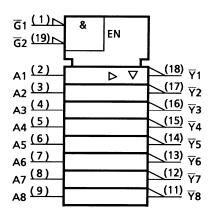


#### TC74HC541A



#### IEC Logic Symbol

#### TC74HC540A



### **Truth Table**

	Inputs	Outputs			
G1	G2 An		Yn*	∀n *	
Н	Х	Х	Z	Z	
Х	Н	Х	Z	Z	
L	L	Н	Н	L	
L	L	L	L	Н	

#### X: Don't care

Z: High impedance

\*: Yn..... HC541

Yn ..... HC540

#### TC74HC541A

<u>G1 (1)</u> <u>G2 (19)</u>	& EN	
$\begin{array}{c} A1 & (2) \\ A2 & (3) \\ A3 & (4) \\ A4 & (5) \\ A4 & (6) \\ A5 & (6) \\ A5 & (7) \\ A6 & (8) \\ A7 & (9) \\ A8 & (9) \end{array}$		(18) Y1 (17) Y2 (16) Y3 (15) Y4 (14) Y5 (13) Y6 (12) Y7 (11) Y8

## Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	–0.5 to 7	V
DC input voltage	V <sub>IN</sub>	-0.5 to V <sub>CC</sub> + 0.5	V
DC output voltage	V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> + 0.5	V
Input diode current	I <sub>IK</sub>	±20	mA
Output diode current	IOK	±20	mA
DC output current	IOUT	±35	mA
DC V <sub>CC</sub> /ground current	ICC	±75	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T <sub>stg</sub>	–65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to  $65^{\circ}$ C. From Ta = 65 to  $85^{\circ}$ C a derating factor of -10 mW/°C shall be applied until 300 mW.

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	2 to 6	V
Input voltage	V <sub>IN</sub>	0 to V <sub>CC</sub>	V
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>	-40 to 85	°C
		0 to 1000 (V <sub>CC</sub> = 2.0 V)	
Input rise and fall time	t <sub>r</sub> , t <sub>f</sub>	0 to 500 ( $V_{CC} = 4.5 \text{ V}$ )	ns
		0 to 400 ( $V_{CC} = 6.0 \text{ V}$ )	

#### **Operating Ranges (Note)**

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{CC}$  or GND.

## **Electrical Characteristics**

#### **DC Characteristics**

Characteristics Symbol		Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit	
				$V_{CC}(V)$	Min	Тур.	Max	Min	Max	
		_		2.0	1.50		_	1.50		
High-level input voltage	VIH			4.5	3.15		—	3.15		V
				6.0	4.20		—	4.20	_	
				2.0	_		0.50		0.50	
Low-level input voltage	VIL		_		_		1.35	_	1.35	V
· · · · · · · · · · · · · · · · · · ·					—		1.80	—	1.80	
	Voн	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>		2.0	1.9	2.0	_	1.9		
			I <sub>OH</sub> = -20 μA	4.5	4.4	4.5	_	4.4		
High-level output voltage				6.0	5.9	6.0	—	5.9		V
			I <sub>OH</sub> = -6 mA	4.5	4.18	4.31	_	4.13		
			I <sub>OH</sub> = -7.8 mA	6.0	5.68	5.80	—	5.63		
	V <sub>OL</sub>	VIN = VIH or VIL		2.0	_	0.0	0.1		0.1	
			I <sub>OL</sub> = 20 μA	4.5	_	0.0	0.1	_	0.1	
Low-level output voltage				6.0	—	0.0	0.1	—	0.1	V
Voltage			I <sub>OL</sub> = 6 mA	4.5	_	0.17	0.26		0.33	
			l <sub>OL</sub> = 7.8 mA	6.0	—	0.18	0.26	—	0.33	
3-state output off-state current	I <sub>OZ</sub>	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = V_{CC} \text{ or } GND$		6.0	_	_	±0.5	_	±5.0	μA
Input leakage current	I <sub>IN</sub>	$V_{IN} = V_{CC}$ or GND		6.0		_	±0.1	_	±1.0	μA
Quiescent supply current	ICC	$V_{IN} = V_{CC}$ or GND		6.0			4.0	_	40.0	μΑ

## AC Characteristics (input: $t_r = t_f = 6 \text{ ns}$ )

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit	
	0,		CL (pF)	$V_{CC}(V)$	Min	Тур.	Max	Min	Max	
				2.0	_	25	60	_	75	
Output transition time	t <sub>TLH</sub>	—	50	4.5		7	12	—	15	ns
	t <sub>THL</sub>			6.0		6	10	_	13	
				2.0		36	90		115	
			50	4.5	_	12	18	—	23	
Propagation delay	t <sub>pLH</sub>			6.0		10	15	_	20	ns
time	t <sub>pHL</sub>			2.0	_	51	130	—	165	115
			150	4.5	_	17	26	—	33	
				6.0	_	14	22	_	28	
	<sup>t</sup> pZL R <sub>L</sub> = 1 t <sub>pZH</sub>	R <sub>L</sub> = 1 kΩ	50	2.0	_	45	125	—	155	- ns
				4.5	—	14	25	—	31	
Output enable time				6.0	_	12	21	_	26	
			150	2.0	_	60	165	—	205	
				4.5	_	19	33	—	41	
				6.0	_	16	28	_	35	
	$t_{pLZ}$ $R_L = 1 k\Omega$			2.0	_	40	125	—	155	
Output disable time		$R_L = 1 \ k\Omega$	50	4.5	_	16	25	—	31	ns
				6.0	_	14	21	_	26	
Input capacitance	C <sub>IN</sub>	-	_		_	5	10	_	10	pF
Output capacitance	C <sub>OUT</sub>	-	_			10	_			pF
Power dissipation	C <sub>PD</sub>	TC74HC540A			32	_		_	- pF	
capacitance	(Note)	TC74HC541A			_	35	—		_	Ч

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

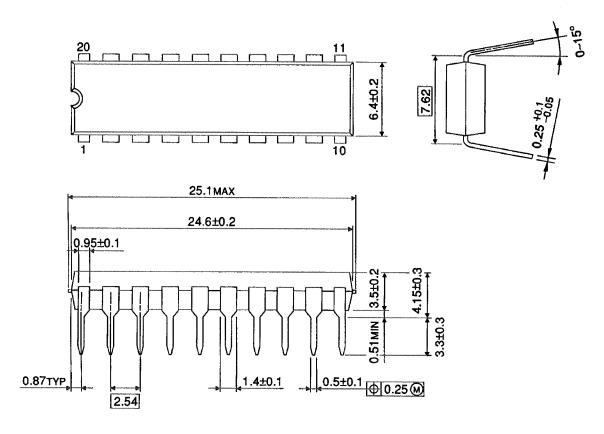
 $I_{CC}$  (opr) =  $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8$  (per bit)

## **TOSHIBA**

### **Package Dimensions**

DIP20-P-300-2.54A

Unit : mm



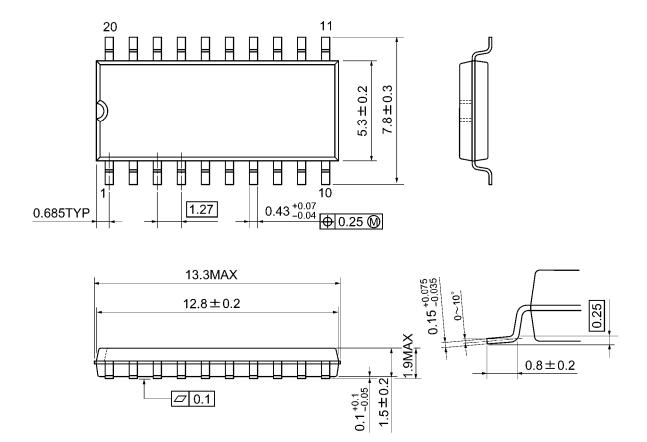
Weight: 1.30 g (typ.)



### **Package Dimensions**

SOP20-P-300-1.27A

Unit: mm



Weight: 0.22 g (typ.)

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