TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

# T A 7 5 S 3 9 3 F

### SINGLE VOLTAGE COMPARATOR

This device of voltage comparator that designed to operate from a single power supply over a wide range of voltage.

Normal operation from dual supplies is also to be guaranteed on voltage range from  $\pm 1V$  to  $\pm 18V$ . V<sub>CC</sub> is necessary at least more 1.5V volts than the input

common mode voltage.

The output can be connected to other open collector outputs to achieve Wired-OR relation ship.

#### FEATURES

Weight : 0.014g (Typ.)

SSOP5-P-0.95

- Compatible to TA75393.
- Small Package
- Single supply voltage range or dual supplies : 2VDC to 36VDC or ±1VDC to ±18VDC

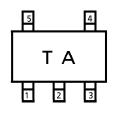
: 0.4mA (Typ.)

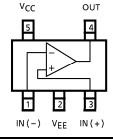
: ±2mV (Typ.)

- Low supply current
- Low input offset voltage
- Wide input common mode voltage range  $: 0V_{DC}$  to  $V_{CC} 1.5V_{DC}$
- Output compatible with TTL, DTL, MOS and CMOS logic system.
- The output can be connected to achieve Wired-OR relation.

#### MARKING (TOP VIEW)

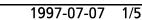




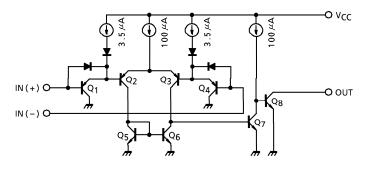


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#### EQUIVALENT CIRCUIT



#### **MAXIMUM RATINGS** (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V <sub>CC</sub> , V <sub>EE</sub>	±18 or 36	V
Differential Input Voltage	DVIN	±36	V
Input Voltage	VIN	– 0.3~V <sub>CC</sub>	V
Power Dissipation	PD	200	mW
Operating Temperature	T <sub>opr</sub>	- 40~85	°C
Storage Temperature	T <sub>stg</sub>	- 55~125	°C

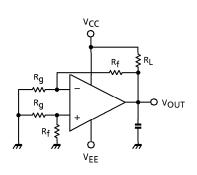
**ELECTRICAL CHARACTERISTICS** ( $V_{CC} = 5V$ ,  $V_{EE} = GND$ ,  $Ta = 25^{\circ}C$ )

CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	VIO	1	_	_	2	5	mV
Input Bias Current	li0	2	—	_	5	50	nA
Input Offset Current	н	2	_	—	25	250	nA
Common Mode Input Voltage	CMVIN	_	—	0	_	V <sub>CC</sub> – 1.5	V
Supply Current	lcc	3	No load	_	0.4	0.8	mA
Voltage Gain	GV	—	$R_L = 15k\Omega$	_	200	_	V/mA
Sink Current	l <sub>sink</sub>	4	IN (+) = 0V, IN (-) = 1V V <sub>OL</sub> = 1.5V	6	16	_	mA
Output Voltage ("L" Level)	VOL	5	IN (+) = 0V, IN (-) = 1V I <sub>sink</sub> = 3mA	_	0.2	0.4	v
Output Leak Current	ILEAK	—	IN (+) = 1V, IN (-) = 0V V <sub>O</sub> = 5V	_	0.1	_	nA
Response Time	t <sub>rsp</sub>	6	$R_L = 5.1 k\Omega$ , $C_L = 15 pF$	_	1.3	—	μs

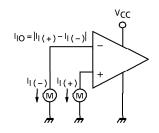
### TOSHIBA

#### TEST CIRCUIT

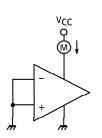
(1) V<sub>IO</sub>



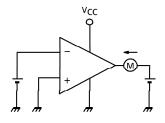




(3) I<sub>CC</sub>

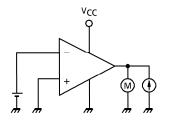


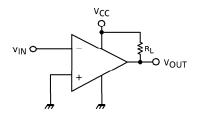
(4) l<sub>sink</sub>

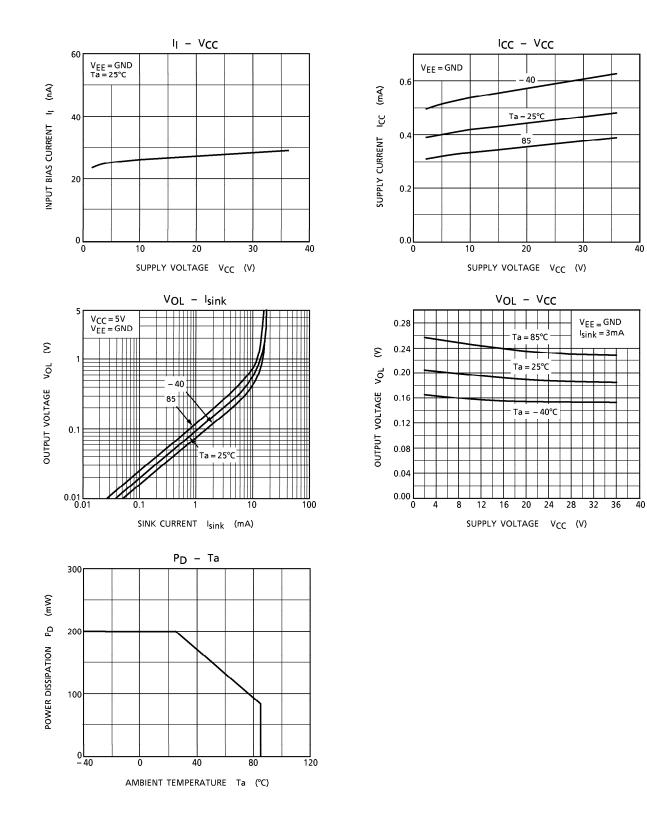


(5) V<sub>OL</sub>

(6) t<sub>rsp</sub>

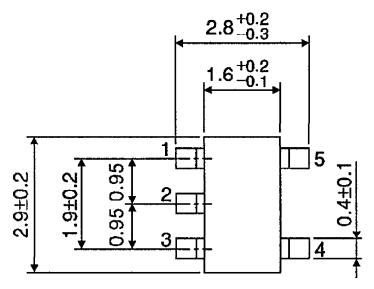


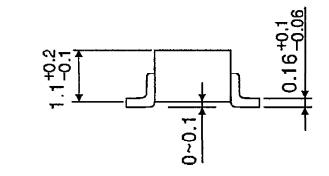




OUTLINE DRAWING SSOP5-P-0.95

Unit : mm





Weight : 0.014g (Typ.)

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