

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

SSM6N16FE

High Speed Switching Applications Analog Switching Applications

Suitable for high-density mounting due to compact package

• Low on resistance: Ron = 3.0 Ω (max) (@VGS = 4 V)

: $R_{on} = 4.0 \Omega \text{ (max) (@VGS = 2.5 V)}$

: Ron = 15 Ω (max) (@VGS = 1.5 V)

Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 Common)

Characteristics		Symbol	Rating	Unit	
Drain-Source voltage		V_{DS}	20	V	
Gate-Source voltage		Vgss	±10	V	
Drain current	DC	ΙD	100	mA	
	Pulse	I _{DP}	200		
Drain power dissipation (Ta = 25°C) (Note 1)		PD	150	mW	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

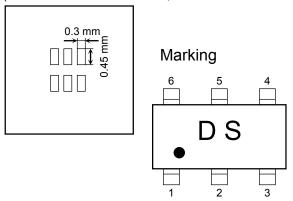
Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating

temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Total rating, mounted on FR4 board (25.4 mm \times 25.4 mm \times 1.6 mm, Cu Pad: 0.135 mm² \times 6)



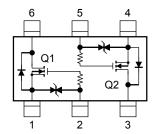
Unit: mm 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 701 | 500 + 70

2-2N1D

Weight: 3.0 mg (typ.)

TOSHIBA

Equivalent Circuit



Handling Precaution

When handling individual devices (which are not yet mounting on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

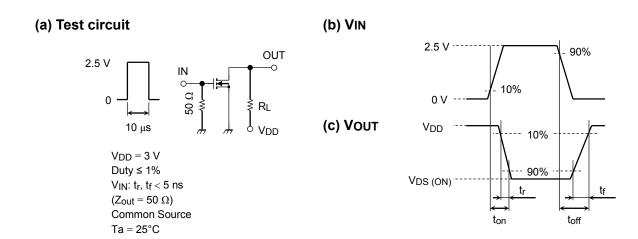
Start of commercial production 2001-03



Electrical Characteristics (Ta = 25°C) (Q1, Q2 Common)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	V _{GS} = ±10 V, V _{DS} = 0 V	_	_	±1	μΑ
Drain-Source breakdown voltage		V _{(BR)DSS}	I _D = 0.1 mA, V _{GS} = 0 V	20	_	_	V
Drain cut-off curre	ent	IDSS	V _{DS} = 20 V, V _{GS} = 0 V	_	_	1	μA
Gate threshold vo	ltage	Vth	V _{DS} = 3 V, I _D = 0.1 mA	0.6	_	1.1	V
Forward transfer a	admittance	Y _{fs}	V _{DS} = 3 V, I _D = 10 mA	40	_	_	mS
Drain-Source ON resistance		R _{DS(ON)}	I _D = 10 mA, V _{GS} = 4 V	_	1.5	3.0	Ω
			I _D = 10 mA, V _{GS} = 2.5 V	_	2.2	4.0	
			I _D = 1 mA, V _{GS} = 1.5 V	_	5.2	15	
Input capacitance		Ciss	V _{DS} = 3 V, V _{GS} = 0 V, f = 1 MHz	_	9.3	_	pF
Reverse transfer capacitance		Crss		_	4.5	_	pF
Output capacitance		Coss		_	9.8	_	pF
Switching time	Turn-on time	ton	V _{DD} = 3 V, I _D = 10 mA, V _{GS} = 0 to 2.5 V	_	70	_	ns
	Turn-off time	toff		_	125	_	

Switching Time Test Circuit



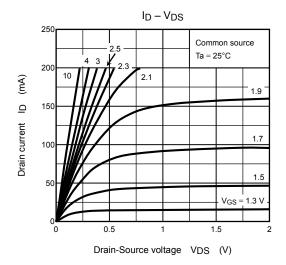
Precaution

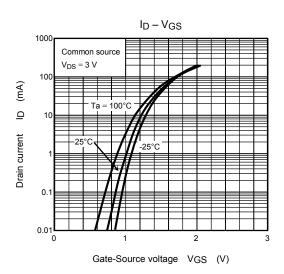
Vth can be expressed as voltage between gate and source when low operating current value is I_D = 100 μ A for this product. For normal switching operation, $V_{GS(on)}$ requires higher voltage than Vth and $V_{GS(off)}$ requires lower voltage than Vth. (Relationship can be established as follows: $V_{GS(onf)} < V_{CS(onf)}$)

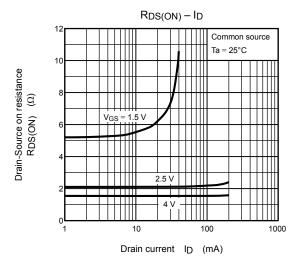
Please take this into consideration for using the device.

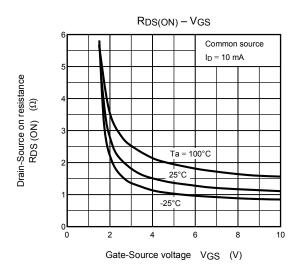


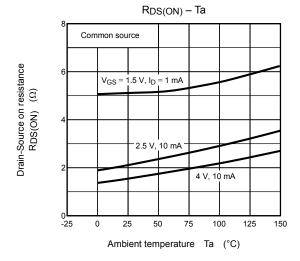
(Q1, Q2 common)

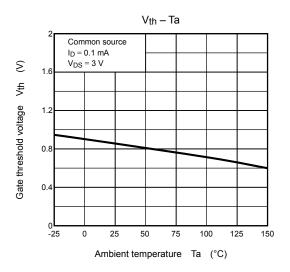






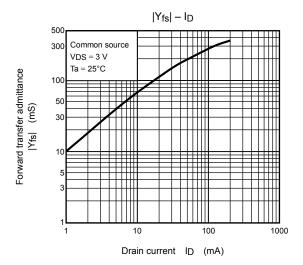


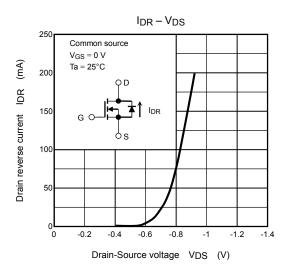


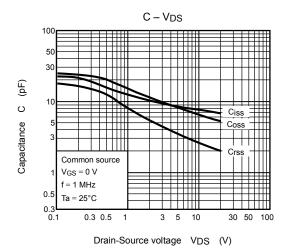


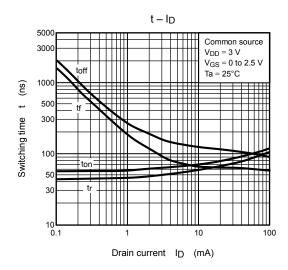


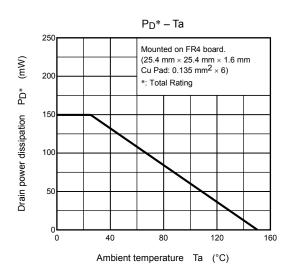
(Q1, Q2 common)













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