

MOSFETs Silicon P-Channel MOS (U-MOSVI)

SSM6J808R

1. Applications

· Power Management Switches

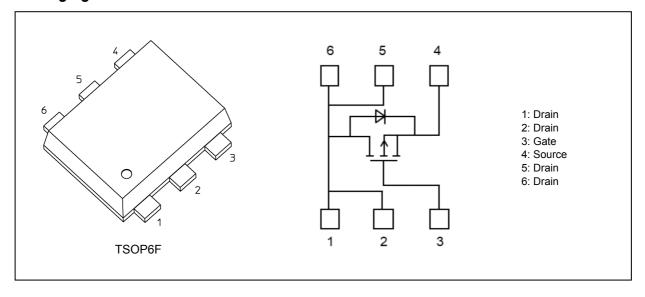
2. Features

- (1) AEC-Q101 qualified
- (2) 4-V drive
- (3) Low drain-source on-resistance
 - : $R_{DS(ON)} = 28 \text{ m}\Omega \text{ (typ.) } (V_{GS} = -10 \text{ V})$

 $R_{\rm DS(ON)} = 35~{\rm m}\Omega$ (typ.) (V_{GS} = -4.5 V)

 $R_{\rm DS(ON)}$ = 38 m Ω (typ.) (V_{GS} = -4 V)

3. Packaging and Internal Circuit



1



4. Absolute Maximum Ratings (Note) (Unless otherwise specified, T_a = 25 °C)

Characteristics			Symbol	Rating	Unit
Drain-source voltage			V_{DSS}	-40	V
Gate-source voltage			V_{GSS}	-20/+10	
Drain current (DC)		(Note 1)	I _D	-7	Α
Drain current (pulsed)		(Note 1), (Note 2)	I _{DP}	-35	
Power dissipation		(Note 3)	P_{D}	1.5	W
Power dissipation	(t < 10 s)	(Note 3)		3	
Channel temperature			T _{ch}	150	°C
Storage temperature			T _{stg}	-55 to 150	
Single-pulse avalanche energy		(Note 4)	E _{AS}	40	mJ
Single-pulse avalanche current			I _{AS}	-6.3	Α

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note 1: Ensure that the channel temperature does not exceed 150 °C.
- Note 2: Pulse width (PW) \leq 10 μ s, duty \leq 1 %
- Note 3: Device mounted on a 25.4 mm × 25.4 mm × 1.6 mm FR4 glass epoxy board (Cu pad: 645 mm²)
- Note 4: V_{DD} = -32 V, T_{ch} = 25 °C (Initial state), L = 1 mH, R_G = 25 Ω

The MOSFETs in this device are sensitive to electrostatic discharge. When handling this device, the worktables, operators, soldering irons and other objects should be protected against anti-static discharge.

Note: The channel-to-ambient thermal resistance, R_{th(ch-a)}, and the drain power dissipation, P_D, vary according to the board material, board area, board thickness and pad area. When using this device, be sure to take heat dissipation fully into account.



5. Electrical Characteristics

5.1. Static Characteristics (Unless otherwise specified, T_a = 25 °C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = -20/+10 \text{ V}$	_	_	±100	nA
Drain cut-off current		I _{DSS}	V _{DS} = -40 V, V _{GS} = 0 V	_	_	-10	μА
Drain-source breakdown voltage		V _{(BR)DSS}	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-40	_	_	٧
Drain-source breakdown voltage	(Note 1)	V _{(BR)DSX}	I _D = -10 mA, V _{GS} = 10 V	-30	_	_	
Gate threshold voltage		V _{th}	V_{DS} = -10 V, I_{D} = -0.1 mA	-0.8	_	-2	
Drain-source on-resistance	(Note 2)	R _{DS(ON)}	I _D = -2.5 A, V _{GS} = -4 V	_	38	52	mΩ
			I _D = -2.5 A, V _{GS} = -4.5 V	_	35	48	
			I _D = -2.5 A, V _{GS} = -10 V	_	28	35	

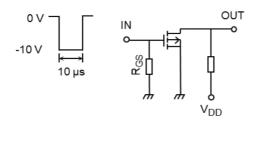
Note 1: If a reverse bias is applied between gate and source, this device enters $V_{(BR)DSX}$ mode. Note that the drain-source breakdown voltage is lowered in this mode.

Note 2: Pulse measurement.

5.2. Dynamic Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = -10 V , V _{GS} = 0 V,	1	1020	_	pF
Reverse transfer capacitance	C _{rss}	f = 1 MHz		136		
Output capacitance	C _{oss}		ı	169		
Switching time (turn-on time)	t _{on}	V_{DD} = -20 V, I_{D} = -1 A, V_{GS} = 0 to -10 V, R_{G} = 10 Ω		32		ns
Switching time (turn-off time)	t _{off}	Duty \leq 1 %, Input; t_r , t_f < 5 ns Ground source, See 5.3		151		

5.3. Switching Time Test Circuit



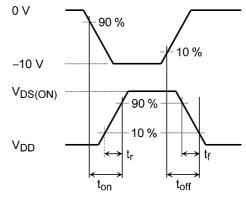


Fig. 5.3.1 Switching Time Test Circuit

Fig. 5.3.2 Input Waveform/Output Waveform

5.4. Gate Charge Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)		$V_{DD} = -32 \text{ V}, I_{D} = -5 \text{ A},$	_	24.2	_	nC
Gate-source charge 1	Q _{gs1}	V _{GS} = -10 V	_	3	_	
Gate-drain charge	Q _{gd}		_	5.3		

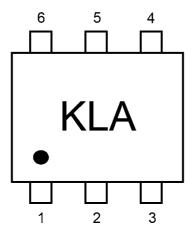
5.5. Source-Drain Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Diode forward voltage	(Note 1)	V_{DSF}	$I_D = 7 A, V_{GS} = 0 V$	_	0.86	1.2	V

Note 1: Pulse measurement.



6. Marking



Rev.6.0



7. Characteristics Curves (Note)

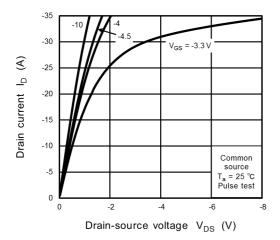


Fig. 7.1 I_D - V_{DS}

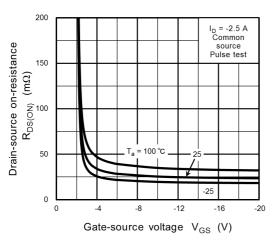


Fig. 7.3 R_{DS(ON)} - V_{GS}

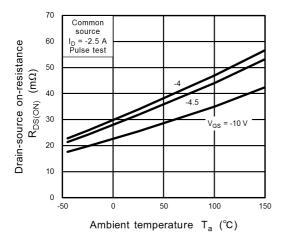


Fig. 7.5 R_{DS(ON)} - T_a

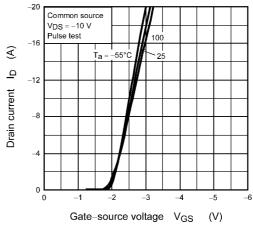


Fig. 7.2 I_D - V_{GS}

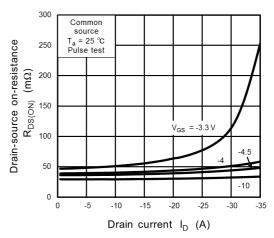


Fig. 7.4 R_{DS(ON)} - I_D

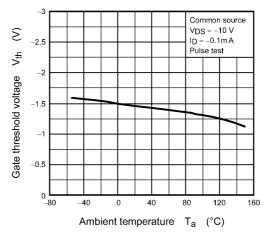
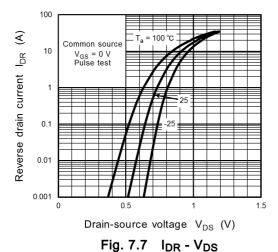


Fig. 7.6 V_{th} - T_a





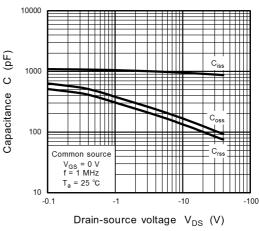
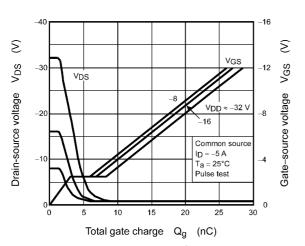


Fig. 7.8 C - V_{DS}



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Fig. 7.9 Dynamic Input Characteristics

Fig. 7.10 t - I_D

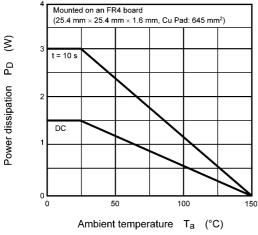


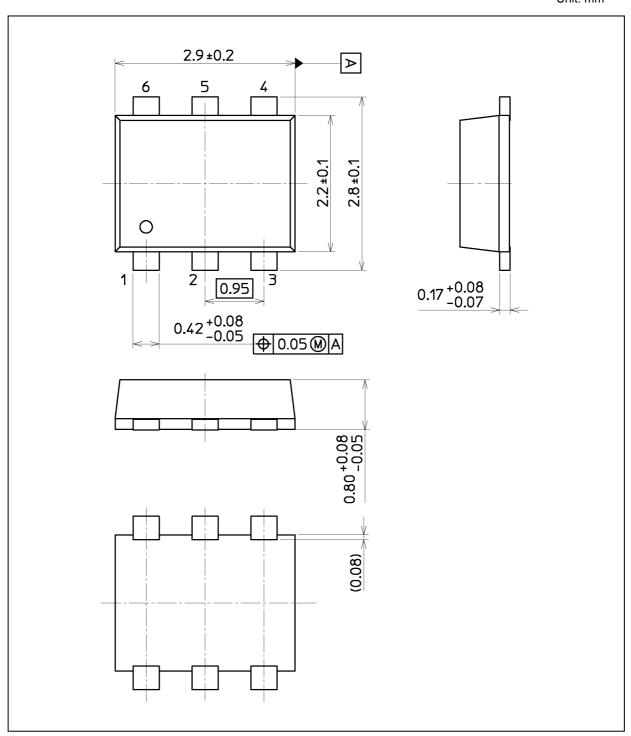
Fig. 7.11 $P_D - T_a$

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

Unit: mm



Weight: 0.016 g (typ.)

	Package Name(s)
Nickname: TSOP6F	



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