

TOSHIBA Transistor Silicon NPN/PNP Epitaxial Type (PCT Process) (Transistor with Built-in Bias Resistor)

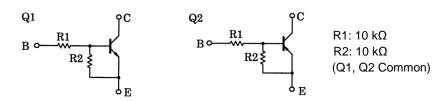
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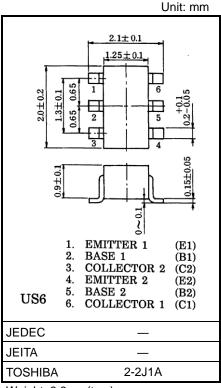
Switching, Inverter Circuit, Interface Circuit and Driver Circuit

- AEC-Q101 Qualified (Note1)
- Including two devices in US6 (ultra super mini type with 6 leads)
- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process and miniaturize equipment.

Note1: For detail information, please contact our sales.

Equivalent Circuit and Bias Resistor Values





Weight: 6.8mg (typ.)

Q1 Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	50	V
Collector-emitter voltage	VCEO	50	V
Emitter-base voltage	VEBO	10	V
Collector current	Ic	100	mA

Q2 Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	Vсво	-50	V
Collector-emitter voltage	VCEO	-50	V
Emitter-base voltage	V _{EBO}	-10	V
Collector current	Ic	-100	mA

Start of commercial production 1992-10



Q1, Q2 Common Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector power dissipation	Pc *	200	mW
Junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

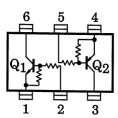
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

* Total rating

Marking



Equivalent Circuit (Top View)





Q1 Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Callagian and off annual	Ісво	V _{CB} = 50 V, I _E = 0 mA	_	_	100	A
Collector cut-off current	ICEO	VCE = 50 V, IB = 0 mA	_	_	500	nA
Emitter cut-off current	IEBO	VEB = 10 V, IC = 0 mA	0.38	_	0.71	mA
DC current gain	hFE	VCE = 5 V, IC = 10 mA	50	_	_	_
Collector-emitter saturation voltage	VCE (sat)	IC = 5 mA, I _B = 0.25 mA	_	0.1	0.3	V
Input voltage (ON)	VI (ON)	VCE = 0.2 V, IC = 5 mA	1.2	_	2.4	V
Input voltage (OFF)	VI (OFF)	VCE = 5 V, IC = 0.1 mA	1.0	_	1.5	V
Transition frequency	fT	VCE = 10 V, IC = 5 mA		250		MHz
Collector output capacitance	C _{ob}	V _{CB} = 10 V, I _E = 0 mA, f = 1 MHz		3	6	рF

Q2 Electrical Characteristics (Ta = 25°C)

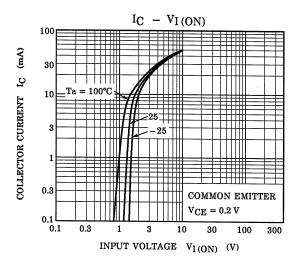
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Callagian and off annually	I _{CBO}	$V_{CB} = -50 \text{ V}, I_E = 0 \text{ mA}$	_	_	-100	^
Collector cut-off current	ICEO	V _{CE} = −50 V, I _B = 0 mA	_	_	-500	nA
Emitter cut-off current	IEBO	V _{EB} = −10 V, I _C = 0 mA	-0.38	_	-0.71	mA
DC current gain	hFE	VCE = −5 V, IC = −10 mA	50	_	_	_
Collector-emitter saturation voltage	VCE (sat)	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$	_	-0.1	-0.3	V
Input voltage (ON)	V _I (ON)	$V_{CE} = -0.2 \text{ V}, I_{C} = -5 \text{ mA}$	-1.2	_	-2.4	V
Input voltage (OFF)	VI (OFF)	VCE = −5 V, IC = −0.1 mA	-1.0	_	-1.5	V
Transition frequency	fŢ	VCE = −10 V, IC = −5 mA	_	200	_	MHz
Collector output capacitance	C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0 \text{ mA}, f = 1 \text{ MHz}$	-	3	6	рF

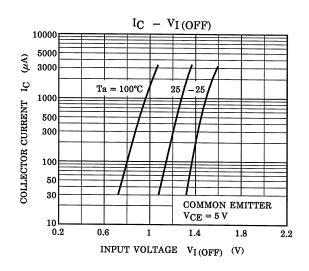
Q1, Q2 Common Electrical Characteristics (Ta = 25°C)

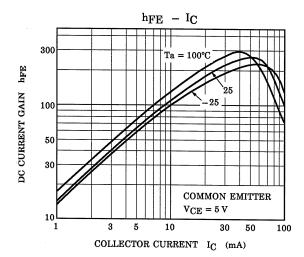
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Input resistor	R1	_	7	10	13	kΩ
Resistor ratio	R1/R2		0.9	1.0	1.1	_

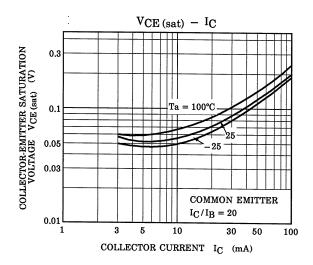


Characteristics Curves Q1





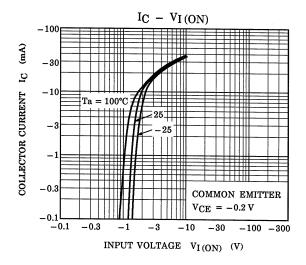


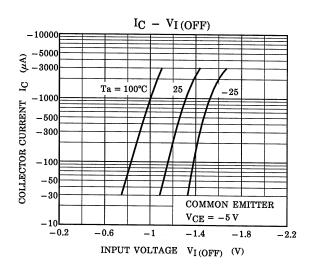


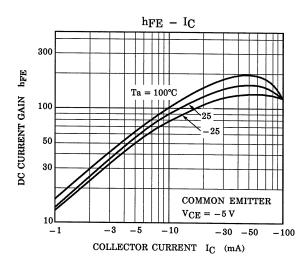
The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

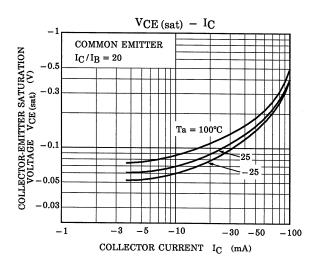


Characteristics Curves Q2









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