TOSHIBA

TOSHIBA Transistor Silicon PNP/NPN Epitaxial Type (PCT Process) (Transistor with Built-in Bias Resistor)

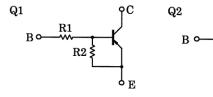


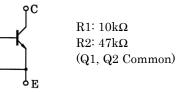
Switching, Inverter Circuit,

Interface Circuit and Driver Circuit

- Including two devices in SM6 (super mini type with 6 leads)
- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process and iniaturize equipment.

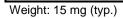
Equivalent Circuit and Bias Resistor Values





+0.22.8-0.3+0.21.6-0.10.95 1.9 ± 0.2 2.9 ± 0.2 .95 2 +0.1; 20 EMITTER 1 **(E1) B1**` LECTOR 2 3 EMITTER 2 4 5. 6. BASE 2 SM6 COLLECTOR 1 JEDEC JEITA _ TOSHIBA 2-3N1A

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Q1 Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	-50	V
Collector-emitter voltage	VCEO	-50	V
Emitter-base voltage	V _{EBO}	-6	V
Collector current	IC	-100	mA

R2 \$

Q2 Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	Vсво	50	V
Collector-emitter voltage	VCEO	50	V
Emitter-base voltage	VEBO	6	V
Collector current	lc	100	mA

2019-11-01

Q1, Q2 Common Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector power dissipation	Pc *	300	mW
Junction temperature	Тј	150	°C
Storage temperature range	T _{stg}	-55 to 150	°C

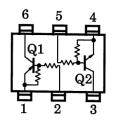
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

* Total rating

Marking

Equivalent Circuit (Top View)



Q1 Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	ICBO	-	$V_{CB} = -50 \text{ V}, \text{ IE} = 0 \text{ mA}$	_	_	-100	nA
	ICEO	_	Vce = −50 V, IB = 0 mA	_	_	-500	
Emitter cut-off current	I _{EBO}	-	$V_{EB} = -6 V$, $I_C = 0 mA$	-0.081	_	-0.15	mA
DC current gain	hFE	_	$V_{CE} = -5 V, I_C = -10 mA$	80	_	_	_
Collector-emitter saturation voltage	VCE (sat)	-	$I_{C} = -5 \text{ mA}, I_{B} = -0.25 \text{ mA}$	_	-0.1	-0.3	V
Input voltage (ON)	VI (ON)	_	Vce = −0.2 V, Ic = −5 mA	-0.7	_	-1.8	V
Input voltage (OFF)	VI (OFF)	_	$V_{CE} = -5 V, I_C = -0.1 mA$	-0.5	_	-1.0	V
Transition frequency	f⊤	_	$V_{CE} = -10 \text{ V}, \text{ I}_{C} = -5 \text{ mA}$	_	200	_	MHz
Collector output capacitance	C _{ob}	_	V _{CB} = - 10 V, I _E = 0 mA, f = 1MHz	_	3	6	pF

Q2 Electrical Characteristics (Ta = 25°C)

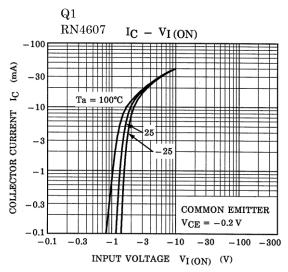
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	ICBO	-	$V_{CB} = 50 \text{ V}, \text{ I}_{E} = 0 \text{ mA}$	_	—	100	nA
	ICEO	-	VCE = 50 V, IB = 0 mA	-	_	500	
Emitter cut-off current	IEBO	-	VEB = 6 V, IC = 0 mA	0.081	_	0.15	mA
DC current gain	hFE	-	V _{CE} = 5 V, I _C = 10 mA	80	—	_	—
Collector-emitter saturation voltage	V _{CE (sat)}	-	$I_{C} = 5 \text{ mA}, I_{B} = 0.25 \text{ mA}$	_	0.1	0.3	V
Input voltage (ON)	VI (ON)	-	VCE = 0.2 V, IC = 5 mA	0.7	_	1.8	V
Input voltage (OFF)	VI (OFF)	-	VCE = 5 V, IC = 0.1 mA	0.5	_	1.0	V
Transition frequency	f⊤	-	VCE = 10 V, IC = 5 mA	-	250		MHz
Collector output capacitance	C _{ob}	_	V _{CB} = 10 V, I _E = 0 mA, f = 1 MHz	_	3	6	pF

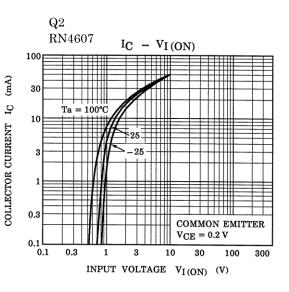
Q1, Q2 Common Electrical Characteristics (Ta = 25°C)

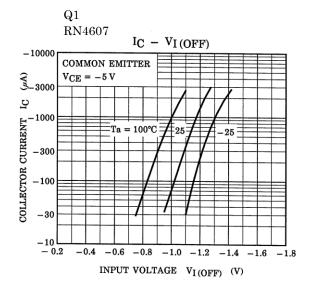
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input resistance	R1	_	—	7	10	13	kΩ
Resistance ratio	R1/R2	_	—	0.191	0.213	0.232	—

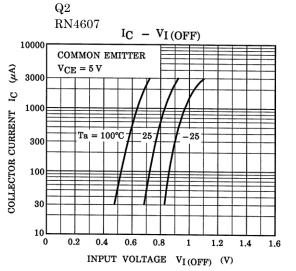
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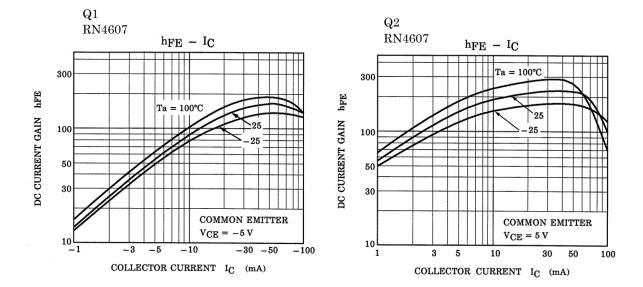
Q1,Q2 characteristics curves











The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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