

TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT Process)(Bias Resistor built-in Transistor)

RN2110MFV, RN2111MFV

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

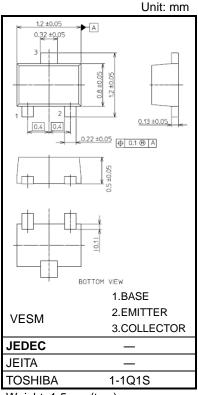
- Ultra-small package, suited to very high density mounting
- Incorporating a bias resistor into the transistor reduces the number of parts, so
 enabling the manufacture of ever more compact equipment and lowering assembly
 cost.
- A wide range of resistor values is available for use in various circuits.
- Complementary to the RN1110MFV, RN1111MFV

Equivalent Circuit

Note:

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Collector-base voltage	V _{CBO}	-50	V	
Collector-emitter voltage	VCEO	-50	٧	
Emitter-base voltage	VEBO	-5	٧	
Collector current	IC	-100	mA	
Collector power dissipation	Pc (Note 1)	150	mW	
Junction temperature	Tj	150	°C	
Storage temperature range	T _{stg}	−55 to 150	°C	

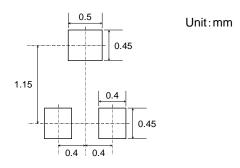


Weight: 1.5 mg (typ.)

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Mounted on an FR4 board (25.4 mm \times 25.4 mm \times 1.6 mm)

Land Pattern Dimensions (for reference only)



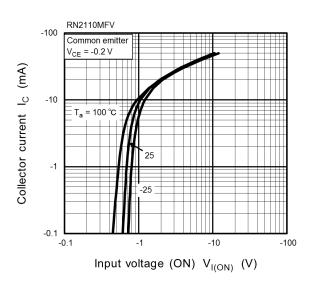
Start of commercial production 2005-02

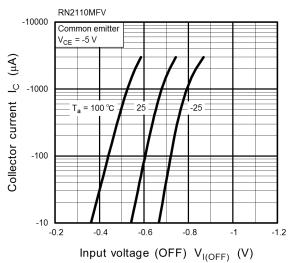


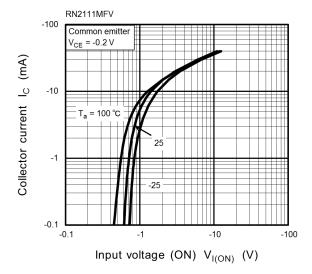
Electrical Characteristics (Ta = 25°C)

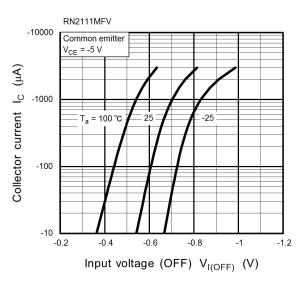
Charact	eristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cutoff curre	ent	I _{CBO}	V _{CB} = -50 V, I _E = 0 A	_	_	-100	nA
Emitter cutoff currer	nt	IEBO	VEB = -5 V, IC = 0 A	_	_	-100	nA
DC current gain		hFE	$V_{CE} = -5 \text{ V}, I_{C} = -1 \text{ mA}$	120	_	400	_
Collector-emitter sa	turation voltage	VCE (sat)	$I_C = -5 \text{ mA}, I_B = -0.5 \text{ mA}$	_	-0.1	-0.3	٧
Collector output cap	acitance	Cob	V _{CB} = −10 V, I _E = 0 A, f = 1 MHz	_	0.9	_	рF
Input resistor	RN2110MFV	- R1	_	3.29	4.7	6.11	kΩ
	RN2111MFV			7	10	13	



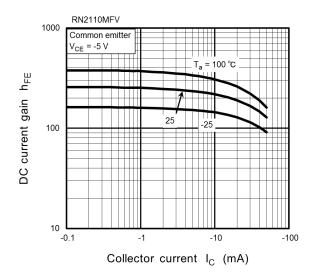


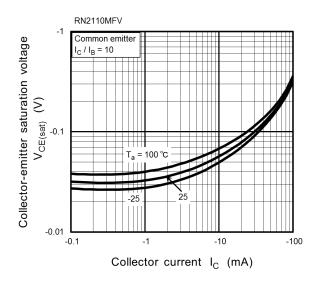


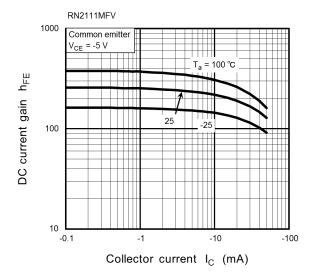


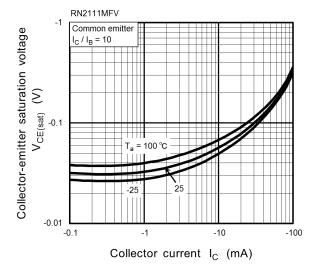














Marking

Type Name	Marking	
RN2110MFV	Type Name	
RN2111MFV	Type Name	



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