

ESD Protection Diodes Silicon Epitaxial Planar

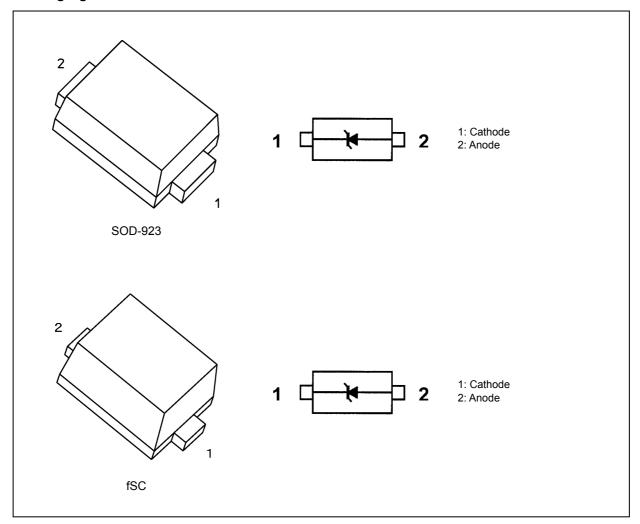
# DF2S20FS

## 1. Applications

· ESD Protection

Note: This product is designed for protection against electrostatic discharge (ESD) and is not intended for any other purpose, including, but not limited to, voltage regulation.

## 2. Packaging and Internal Circuit



Start of commercial production



## 3. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Electrostatic discharge voltage (IEC61000-4-2)(Contact)	V <sub>ESD</sub>	±12	kV
Junction temperature	Tj	150	°C
Storage temperature	T <sub>stg</sub>	-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### 4. Electrical Characteristics (Unless otherwise specified, T<sub>a</sub> = 25°C)

V<sub>RWM</sub>: Working peak reverse

voltage

Vz: Zener voltage

V<sub>BR</sub>: Reverse breakdown voltage

Z<sub>Z</sub>: Dynamic impedance

Iz: Zener current

I<sub>BR</sub>: Reverse breakdown current

I<sub>R</sub>: Reverse current V<sub>C</sub>: Clamp voltage I<sub>PP</sub>: Peak pulse current

R<sub>DYN</sub>: Dynamic resistance

I<sub>F</sub>: Forward current V<sub>F</sub>: Forward voltage

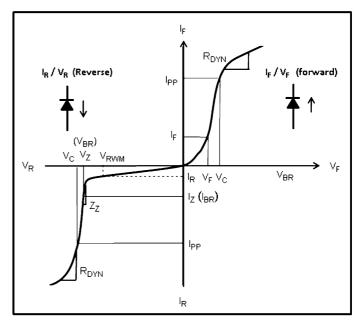


Fig. 4.1 Definitions of Electrical Characteristics

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Working peak reverse voltage	$V_{RWM}$		_	_	_	15	V
Zener voltage (Reverse breakdown voltage)	V <sub>Z</sub> (V <sub>BR</sub> )		$I_Z = 5 \text{ mA}$ $(I_{BR})$	18.8	20.0	21.2	V
Dynamic impedance	Z <sub>Z</sub>		$I_Z = 5 \text{ mA}$ $(I_{BR})$			50	Ω
Reverse current	I <sub>R</sub>		V <sub>RWM</sub> = 15 V	_	_	0.5	μА
Total capacitance	Ct		V <sub>R</sub> = 0 V, f = 1 MHz	_	9	_	pF

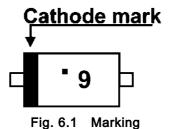


#### 5. Guaranteed ESD Protection (Note)

Test Condition	ESD Protection		
IEC61000-4-2 (Contact discharge)	±12 kV		

Note: Criterion: No damage to devices.

## 6. Marking



## 7. Land Pattern Dimensions (for reference only)

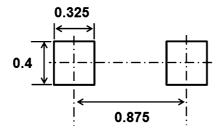


Fig. 7.1 SOD-923 (unit: mm)

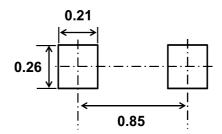
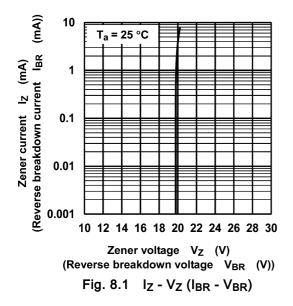


Fig. 7.2 fSC (unit: mm)



#### 8. Characteristics Curves (Note)



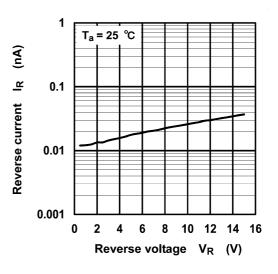
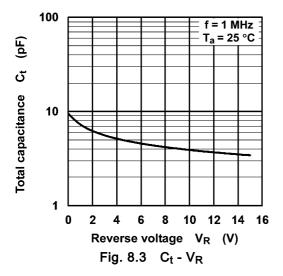


Fig. 8.2 I<sub>R</sub> - V<sub>R</sub>

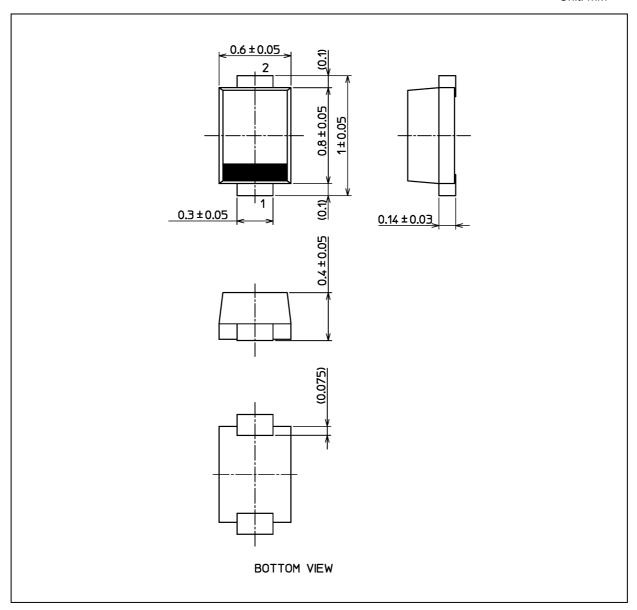


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



#### **Package Dimensions**

Unit: mm



The shapes and dimensions of the package vary, depending on the manufacturing plant. For details, contact the Toshiba sales representative.

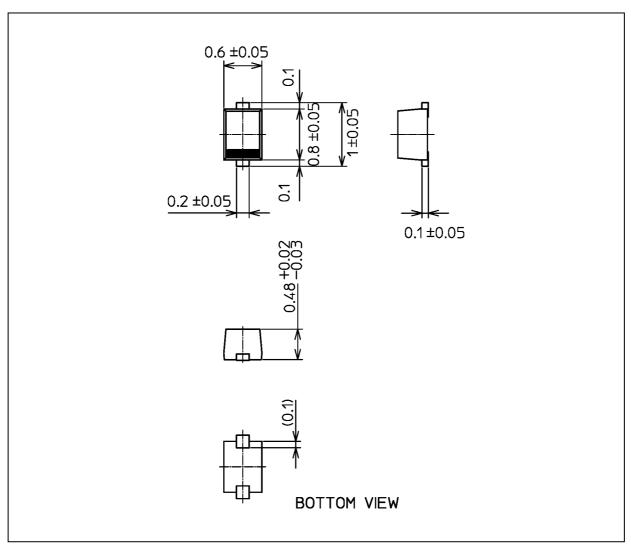
Weight: 0.55 mg (typ.)

Package Name(s)	
TOSHIBA: 1-1AH1A	
Nickname: SOD-923	



#### **Package Dimensions**

Unit: mm



The shapes and dimensions of the package vary, depending on the manufacturing plant. For details, contact the Toshiba sales representative.

#### Weight: 0.6 mg (typ.)

	Package Name(s)
TOSHIBA: 1-1L1S	
Nickname: fSC	



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