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Schottky Barrier Diode Silicon Epitaxial

# CUS15S30

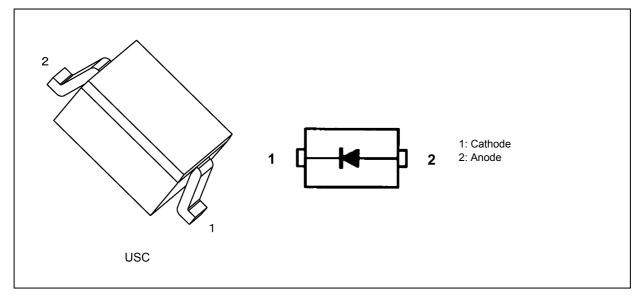
#### 1. Applications

High-Speed Switching

#### 2. Features

(1) Low forward voltage:  $V_F(1) = 0.33 V$  (typ.)

#### 3. Packaging and Internal Circuit



#### 4. Absolute Maximum Ratings (Note) (Unless otherwise specified, $T_a = 25^{\circ}$ C)

Characteristics	Symbol	Note	Rating	Unit
Peak reverse voltage	V <sub>RM</sub>		30	V
Reverse voltage	V <sub>R</sub>		20	
Average rectified current	I <sub>O</sub>	(Note 1)	1.5	А
Non-repetitive peak forward surge current	I <sub>FSM</sub>	(Note 2)	5	
Junction temperature	Тj		125	°C
Storage temperature	T <sub>stg</sub>		-55 to 125	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: Measured with a 10 ms pulse.

Note 1: Mounted on a FR4 board. (25.4 mm  $\times$  25.4 mm  $\times$  1.6 mm, Cu Pad: 645 mm<sup>2</sup>)

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### 5. Electrical Characteristics (Unless otherwise specified, $T_a = 25^{\circ}C$ )

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V <sub>F</sub> (1)	I <sub>F</sub> = 1 A (Pulse test)	_	0.33	0.40	V
	V <sub>F</sub> (2)	I <sub>F</sub> = 1.5 A (Pulse test)	_	0.39	—	
Reverse current	I <sub>R</sub>	V <sub>R</sub> = 30 V (Pulse test)		0.2	0.5	mA
Total capacitance	Ct	V <sub>R</sub> = 0 V, f = 1 MHz	_	200	_	pF

#### 6. Marking

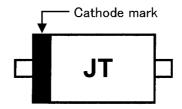


Fig. 6.1 Marking

Marking Code	Part Number
JT	CUS15S30

#### 7. Usage Considerations

• Schottky barrier diodes (SBDs) have reverse leakage greater than other types of diodes. This makes SBDs more susceptible to thermal runaway under high-temperature and high-voltage conditions. Thus, both forward and reverse power losses of SBDs should be considered for thermal and safety design.

#### 8. Land Pattern Dimensions for Reference Only

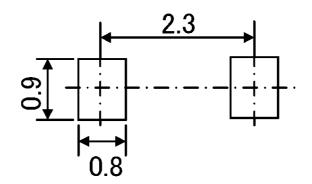
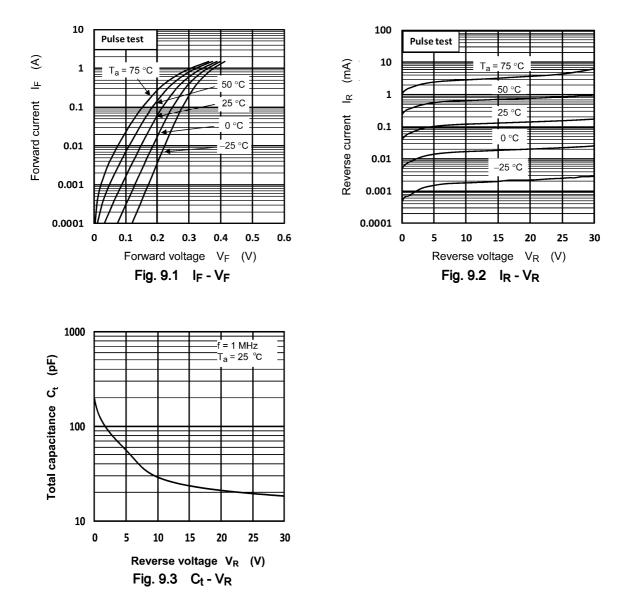


Fig. 8.1 Land Pattern Dimensions for Reference Only (Unit: mm)

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#### 9. Characteristics Curves (Note)

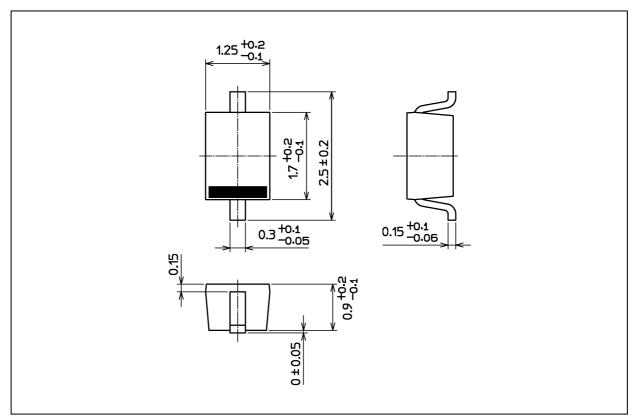


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



#### Package Dimensions

Unit: mm



#### Weight: 4.5 mg (typ.)

Package Name(s)		
TOSHIBA: 1-1E1S		
Nickname: USC		

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