

Schottky Barrier Diode Silicon Epitaxial

# **CTS520**

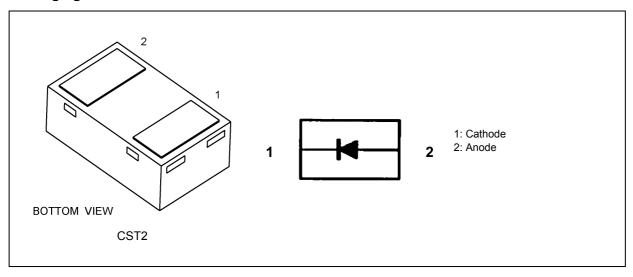
### 1. Applications

· High-Speed Switching

#### 2. Features

- (1) Low reverse current:  $I_{R(2)} = 5 \mu A \text{ (max)}$
- (2) Small chip scale package: Thickness = 0.40 mm (max)

#### 3. Packaging and Internal Circuit



## 4. Absolute Maximum Ratings (Note) (Unless otherwise specified, T<sub>a</sub> = 25°C)

Characteristics	Symbol	Note	Rating	Unit
Reverse voltage	V <sub>R</sub>		30	V
Peak forward current	I <sub>FM</sub>		300	mA
Average rectified current	I <sub>O</sub>		200	
Non-repetitive peak forward surge current	I <sub>FSM</sub>	(Note 1)	1	Α
Power dissipation	$P_{D}$	(Note 2)	150	mW
Junction temperature	Tj		125	°C
Storage temperature	T <sub>stg</sub>		-55 to 125	
Operating temperature	T <sub>opr</sub>		-40 to 100	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Measured with a 10 ms pulse.

Note 2: Mounted on a glass epoxy circuit board of 20 mm  $\times$  20 mm, Pad dimension of 4 mm  $\times$  4 mm.



## 5. Electrical Characteristics (Unless otherwise specified, T<sub>a</sub> = 25°C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V <sub>F(1)</sub>		I <sub>F</sub> = 1 mA	_	0.21	_	V
	V <sub>F(2)</sub>		I <sub>F</sub> = 10 mA	_	0.28	_	
	V <sub>F(3)</sub>		I <sub>F</sub> = 200 mA	_	0.52	0.6	
Reverse current	I <sub>R(1)</sub>		V <sub>R</sub> = 10 V	_	_	3	μА
	I <sub>R(2)</sub>		V <sub>R</sub> = 30 V	_	_	5	
Total capacitance	Ct		V <sub>R</sub> = 0 V, f = 1 MHz	_	16		pF

### 6. Marking

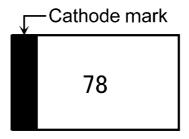


Fig. 6.1 Marking

Marking Code	Part Number		
78	CTS520		

### 7. Usage Considerations

• Schottky barrier diodes (SBDs) have reverse leakage greater than other types of diodes. This makes SBDs more susceptible to thermal runaway under high-temperature and high-voltage conditions. Thus, both forward and reverse power losses of SBDs should be considered for thermal and safety design.

## 8. Land Pattern Dimensions for Reference Only

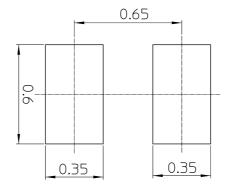
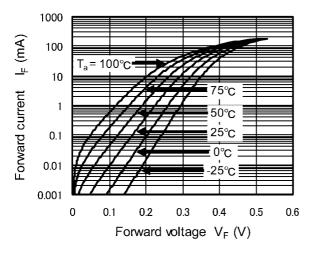


Fig. 8.1 Land Pattern Dimensions for Reference Only (Unit: mm)



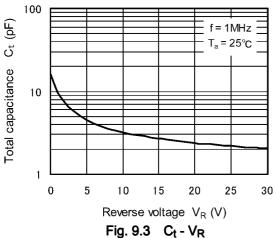
## 9. Characteristics Curves (Note)



1000 Reverse current IR (µA) T<sub>a</sub> = 100°C 100 75°C 10 50°C .25°C 1 0.1 0 10 15 20 25 30 Reverse voltage V<sub>R</sub> (V) Fig. 9.2  $I_R - V_R$ 

Fig. 9.1 I<sub>F</sub> - V<sub>F</sub>



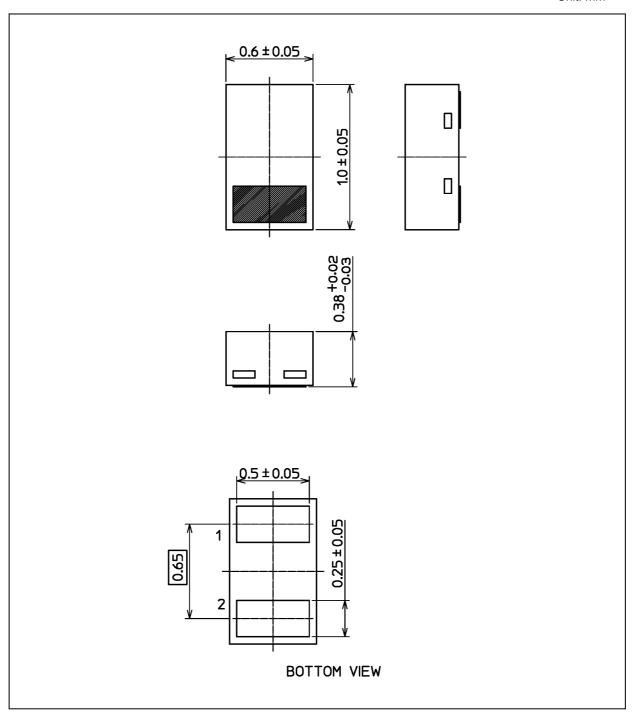


Note: The above characteristics curves are presented for reference only and not guaranteed by production test.



## **Package Dimensions**

Unit: mm



Weight: 0.7 mg (typ.)

	Package Name(s)	
Nickname: CST2		



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