Schottky Barrier Diode Silicon Epitaxial

CES521

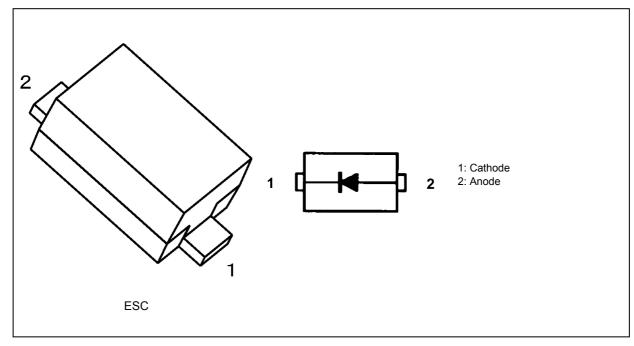
1. Applications

High-Speed Switching

2. Features

- (1) Low forward voltage: $V_{F(3)}$ = 0.5 V (max).
- (2) Small and compact ESC package, equivalent to SOD-523 and SC-79 packages.

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25°C)

Characteristics	Symbol	Note	Rating	Unit
Reverse voltage	V _R	—	30	V
Peak forward current	I _{FM}	_	300	mA
Average rectified current	Ι _Ο	_	200	
Non-repetitive peak forward surge current	I _{FSM}	(Note 1)	1	А
Power dissipation	PD	(Note 2)	150	mW
Junction temperature	Тj	_	125	°C
Storage temperature	T _{stg}	_	-55 to 125	
Operating temperature	T _{opr}	_	-40 to 100	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Measured with a 10ms pulse.

Note 2: Mounted on a glass-epoxy circuit board of 20 mm \times 20 mm, Pad dimension of 4 mm \times 4 mm.

5. Electrical Characteristics (Unless otherwise specified, Ta = 25°C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V _{F(1)}	_	I _F = 1 mA	_	0.20	_	V
	V _{F(2)}	_	I _F = 5 mA	_	0.24	_	
	V _{F(3)}	_	I _F = 200 mA	_	0.45	0.5	
Reverse current	I _{R(1)}	_	V _R = 10 V	_	—	20	μA
	I _{R(2)}	_	V _R = 30 V	_	—	30	
Total capacitance	Ct	_	V _R = 0 V, f = 1 MHz		26	_	pF

6. Marking

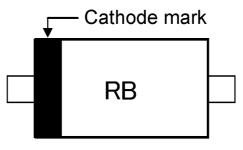


Fig. 6.1 Marking

Marking Code	Part Number		
RB	CES521		

7. Usage Considerations

• Schottky barrier diodes (SBDs) have reverse leakage greater than other types of diodes. This makes SBDs more susceptible to thermal runaway under high-temperature and high-voltage conditions. Thus, both forward and reverse power losses of SBDs should be considered for thermal and safety design.

8. Land pattern dimensions for reference only

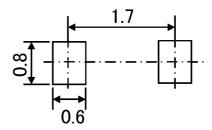


Fig. 8.1 Land pattern dimensions for reference only (Unit: mm)

9. Characteristics Curves (Note)

1

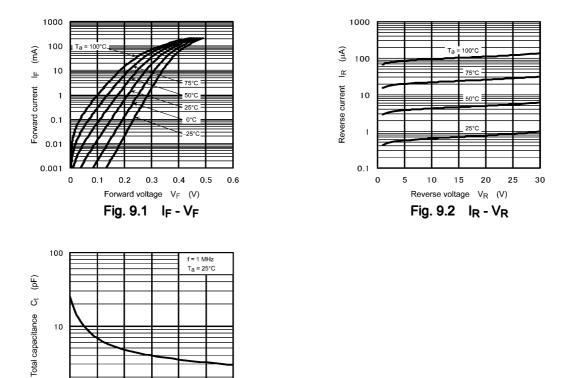
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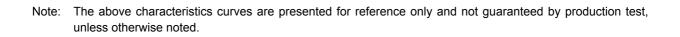
5

10

15 20 25

 $\label{eq:Fig.9.3} \begin{array}{c} \mathsf{Reverse voltage} \quad \mathsf{V}_{\mathsf{R}} \quad (\mathsf{V}) \\ \textbf{Fig. 9.3} \quad \textbf{C}_{t} \textbf{-} \textbf{V}_{\mathsf{R}} \end{array}$





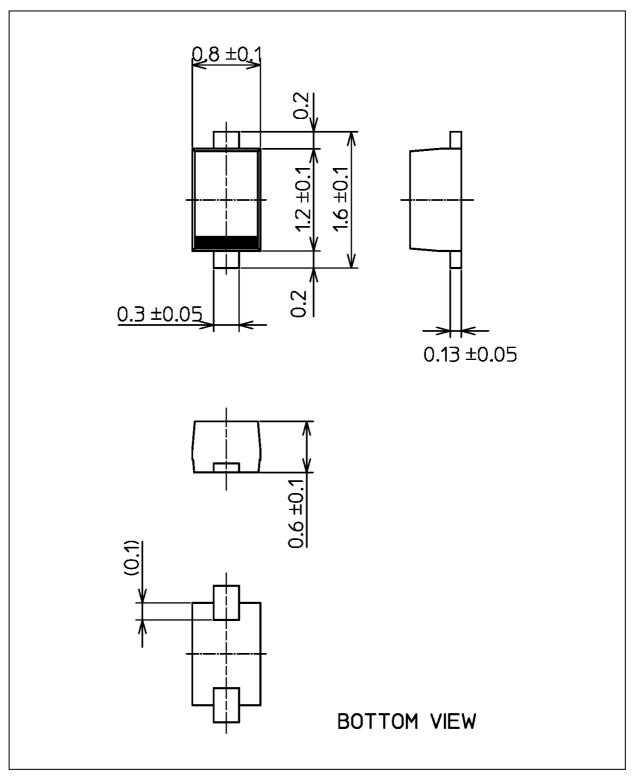
30



Package Dimensions

CES521

Unit: mm



Weight: 1.4 mg (typ.)

Package Name(s)

Nickname: ESC

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