

CMOS Digital Integrated Circuits Silicon Monolithic

# 7UL1G14FU

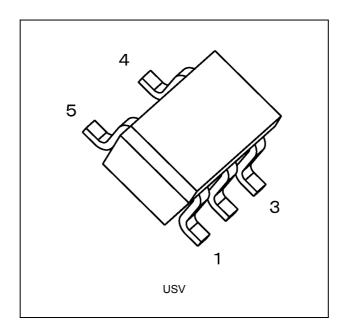
### 1. Functional Description

· Schmitt Inverter

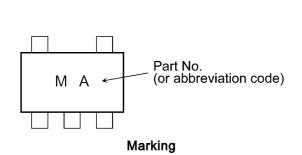
#### 2. Features

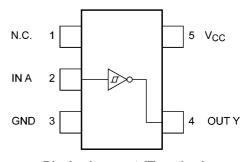
- (1) Wide operating temperature range:  $T_{opr} = -40$  to 125 °C
- (2) High output current:  $\pm 8.0$  mA (min) at  $V_{CC} = 3.0$  V
- (3) Super high speed operation:  $t_{pd} = 3.0$  ns (typ.) at  $V_{CC} = 3.3$  V,  $C_L = 15$  pF
- (4) Operating voltage range:  $V_{CC} = 0.9$  to 3.6 V
- (5) 3.6 V tolerant input
- (6) 3.6 V power down protection output

## 3. Packaging



### 4. Marking and Pin Assignment





Pin Assignment (Top view)

Start of commercial production

2021-04



#### 5. IEC Logic Symbol



#### 6. Truth Table

Input A	Output Y
L	Н
Н	L

## 7. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V <sub>CC</sub>		-0.5 to 4.6	V
Input voltage	V <sub>IN</sub>		-0.5 to 4.6	V
DC output voltage	V <sub>OUT</sub>	(Note 1)	-0.5 to 4.6	V
		(Note 2)	-0.5 to V <sub>CC</sub> + 0.5	
Input diode current	I <sub>IK</sub>		-20	mA
Output diode current	l <sub>ok</sub>	(Note 3)	-20	mA
DC output current	I <sub>OUT</sub>		±25	mA
V <sub>CC</sub> /ground current	I <sub>CC</sub>		±50	mA
Power dissipation	P <sub>D</sub>		200	mW
Storage temperature	T <sub>stg</sub>		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{CC} = 0 V$ 

Note 2: High (H) or Low (L) state. I<sub>OUT</sub> absolute maximum rating must be observed.

Note 3: V<sub>OUT</sub> < GND



# 8. Operating Ranges (Note)

Characteristics	Symbol	Note	Test Condition	Rating	Unit
Supply voltage	V <sub>CC</sub>		_	0.9 to 3.6	V
Input voltage	V <sub>IN</sub>		_	0 to 3.6	V
Output voltage	V <sub>OUT</sub>	(Note 1)	_	0 to 3.6	V
		(Note 2)	_	0 to V <sub>CC</sub>	
Output current	I <sub>OH</sub> ,I <sub>OL</sub>		V <sub>CC</sub> = 3.0 to 3.6 V	±8.0	mA
			V <sub>CC</sub> = 2.3 to 2.7 V	±4.0	
			V <sub>CC</sub> = 1.65 to 1.95 V	±3.0	
			V <sub>CC</sub> = 1.4 to 1.6 V	±1.7	
			V <sub>CC</sub> = 1.1 to 1.3 V	±0.3	
			V <sub>CC</sub> = 0.9 V	±0.02	
Operating temperature	T <sub>opr</sub>		_	-40 to 125	°C

Note: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either  $V_{CC}$  or GND.

Note 1:  $V_{CC} = 0 V$ 

Note 2: High (H) or Low (L) state.



### 9. Electrical Characteristics

# 9.1. DC Characteristics (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Тур.	Max	Unit
Positive threshold voltage	$V_P$	_		0.9	_	_	0.73	V
				1.1	_	-	0.86	
				1.4	_	_	1.07	
				1.65	_	_	1.23	
				2.3	_	_	1.66	
				3.0	_	_	2.14	
Negative threshold	V <sub>N</sub>	_		0.9	0.18	-	_	V
voltage				1.1	0.26	-	_	
				1.4	0.36	_	_	
				1.65	0.45	-	_	
				2.3	0.69	_	_	
				3.0	0.96	_	_	
Hysteresis voltage	V <sub>H</sub>	_		0.9	0.15	_	0.38	V
				1.1	0.18	-	0.41	
				1.4	0.20	_	0.48	
				1.65	0.22	_	0.60	
				2.3	0.35	_	0.76	
				3.0	0.45	_	0.93	
High-level output voltage	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OH</sub> = -0.02 mA	0.9	0.75	_	_	V
			I <sub>OH</sub> = -0.3 mA	1.1 to 1.3	V <sub>CC</sub> × 0.75	-	_	
			I <sub>OH</sub> = -1.7 mA	1.4 to 1.6	V <sub>CC</sub> × 0.75	_	_	
			I <sub>OH</sub> = -3.0 mA	1.65 to 1.95	V <sub>CC</sub> -0.45	_	_	
			I <sub>OH</sub> = -4.0 mA	2.3 to 2.7	2.0	_	_	
			I <sub>OH</sub> = -8.0 mA	3.0 to 3.6	2.48	_	_	
Low-level output voltage	V <sub>OL</sub>	$V_{IN} = V_{IH}$	I <sub>OL</sub> = 0.02 mA	0.9	_	_	0.1	V
			I <sub>OL</sub> = 0.3 mA	1.1 to 1.3	_	_	$V_{CC} \times 0.25$	
			I <sub>OL</sub> = 1.7 mA	1.4 to 1.6	_		$V_{CC} \times 0.25$	
			I <sub>OL</sub> = 3.0 mA	1.65 to 1.95	_	_	0.45	
			$I_{OL}$ = 4.0 mA	2.3 to 2.7	_		0.4	
			I <sub>OL</sub> = 8.0 mA	3.0 to 3.6	_		0.4	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 0 to 3.6 V		0 to 3.6	_	-	±0.1	μА
Power-OFF leakage current	I <sub>OFF</sub>	V <sub>IN</sub> = 0 to 3.6 V, V <sub>OUT</sub> = 0 to 3.6 V		0	_	_	1.0	μА
Quiescent supply current	I <sub>CC</sub>	$V_{IN} = V_{CC}$ or GND		3.6	_	_	1.0	μА

Rev.4.0



# 9.2. DC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C)

Characteristics	Symbol	Test Condition	on	V <sub>CC</sub> (V)	Min	Max	Unit
Positive threshold voltage	V <sub>P</sub>	_		0.9	_	0.73	V
				1.1	_	0.86	
				1.4	_	1.07	
				1.65	_	1.23	
				2.3	_	1.66	
				3.0	_	2.14	
Negative threshold voltage	V <sub>N</sub>	_		0.9	0.18	_	V
				1.1	0.26	_	
				1.4	0.36	_	
				1.65	0.45	_	
				2.3	0.69	_	
				3.0	0.96	_	
Hysteresis voltage	V <sub>H</sub>	_		0.9	0.15	0.38	V
				1.1	0.18	0.41	
				1.4	0.20	0.48	
				1.65	0.22	0.60	
				2.3	0.35	0.76	
				3.0	0.45	0.93	
High-level output voltage	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OH</sub> = -0.02 mA	0.9	0.75	_	V
			$I_{OH} = -0.3 \text{ mA}$	1.1 to 1.3	V <sub>CC</sub> × 0.75	_	
			I <sub>OH</sub> = -1.7 mA	1.4 to 1.6	V <sub>CC</sub> × 0.75	_	
			I <sub>OH</sub> = -3.0 mA	1.65 to 1.95	V <sub>CC</sub> -0.45	_	
			I <sub>OH</sub> = -4.0 mA	2.3 to 2.7	2.0	_	
			$I_{OH} = -8.0 \text{ mA}$	3.0 to 3.6	2.48	_	
Low-level output voltage	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub>	$I_{OL} = 0.02 \text{ mA}$	0.9	_	0.1	V
			$I_{OL} = 0.3 \text{ mA}$	1.1 to 1.3	_	V <sub>CC</sub> × 0.25	
			I <sub>OL</sub> = 1.7 mA	1.4 to 1.6	_	V <sub>CC</sub> × 0.25	
			I <sub>OL</sub> = 3.0 mA	1.65 to 1.95	_	0.45	
			I <sub>OL</sub> = 4.0 mA	2.3 to 2.7	_	0.4	
			I <sub>OL</sub> = 8.0 mA	3.0 to 3.6	_	0.4	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 0 to 3.6 V		0 to 3.6	_	±0.5	μА
Power-OFF leakage current	I <sub>OFF</sub>	V <sub>IN</sub> = 0 to 3.6 V, V <sub>OUT</sub> = 0 to 3.6 V		0	_	10.0	μА
Quiescent supply current	Icc	$V_{IN} = V_{CC}$ or GND		3.6		10.0	μΑ



# 9.3. DC Characteristics (Unless otherwise specified, T<sub>a</sub> = -40 to 125 °C)

Characteristics	Symbol	Test Condition	on	V <sub>CC</sub> (V)	Min	Max	Unit
Positive threshold voltage	V <sub>P</sub>	_		0.9	_	0.73	V
				1.1	_	0.86	
				1.4	_	1.07	
				1.65	_	1.23	
				2.3	_	1.66	
				3.0	_	2.14	V
Negative threshold voltage	V <sub>N</sub>	_		0.9	0.18	_	
				1.1	0.26	_	
				1.4	0.36	_	
				1.65	0.45	_	
				2.3	0.69	_	
				3.0	0.96	_	
Hysteresis voltage	V <sub>H</sub>	_		0.9	0.15	0.38	V
				1.1	0.18	0.41	
				1.4	0.20	0.48	
				1.65	0.22	0.60	
				2.3	0.35	0.76	
				3.0	0.45	0.93	
High-level output voltage	V <sub>OH</sub>	$V_{IN} = V_{IL}$	$I_{OH} = -0.02 \text{ mA}$	0.9	0.75	_	V
			$I_{OH}$ = -0.3 mA	1.1 to 1.3	$V_{CC} \times 0.73$	_	
			$I_{OH} = -1.7 \text{ mA}$	1.4 to 1.6	$V_{CC} \times 0.73$	_	
			$I_{OH}$ = -3.0 mA	1.65 to 1.95	V <sub>CC</sub> -0.5	_	
			$I_{OH}$ = -4.0 mA	2.3 to 2.7	1.95	_	
			$I_{OH}$ = -8.0 mA	3.0 to 3.6	2.4	_	
Low-level output voltage	V <sub>OL</sub>	$V_{IN} = V_{IH}$	$I_{OL} = 0.02 \text{ mA}$	0.9	_	0.1	V
			$I_{OL}$ = 0.3 mA	1.1 to 1.3	_	$V_{CC} \times 0.27$	
			$I_{OL}$ = 1.7 mA	1.4 to 1.6	_	V <sub>CC</sub> × 0.27	
			$I_{OL}$ = 3.0 mA	1.65 to 1.95	_	0.5	
			$I_{OL}$ = 4.0 mA	2.3 to 2.7	_	0.45	
			$I_{OL}$ = 8.0 mA	3.0 to 3.6	_	0.45	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 0 to 3.6 V		0 to 3.6	_	±2.0	μА
Power-OFF leakage current	l <sub>OFF</sub>	V <sub>IN</sub> = 0 to 3.6 V, V <sub>OUT</sub> = 0 to 3.6 V		0	_	80.0	μА
Quiescent supply current	I <sub>CC</sub>	$V_{IN} = V_{CC}$ or GND		3.6	_	80.0	μΑ



# 9.4. AC Characteristics (Unless otherwise specified, $T_a = 25$ °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Note	Test Condition	V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Тур.	Max	Unit
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>		$R_L = 1 M\Omega$	0.9	10	_	22.0	_	ns
			See Fig. 9.7.1, Table 9.7.1	1.1 to 1.3		_	11.1	22.6	
			Table 9.7.1	1.4 to 1.6		_	6.7	10.5	
				1.65 to 1.95		_	5.0	7.8	
				2.3 to 2.7		_	3.3	5.4	
				3.0 to 3.6			2.7	4.4	
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>		$R_L = 1 M\Omega$	0.9	15		24.2		ns
			See Fig. 9.7.1, Table 9.7.1	1.1 to 1.3		_	12.1	25.1	
			Table 9.7.1	1.4 to 1.6			7.3	11.5	
				1.65 to 1.95		_	5.5	8.4	
				2.3 to 2.7		_	3.7	5.7	
				3.0 to 3.6		_	3.0	4.6	
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>		$R_L = 1 M\Omega$	0.9	30	_	31.0	_	ns
			See Fig. 9.7.1, Table 9.7.1	1.1 to 1.3		_	15.7	35.7	
			Table 9.7.1	1.4 to 1.6		_	9.1	15.8	
				1.65 to 1.95		_	7.1	10.7	
				2.3 to 2.7		_	4.7	6.9	
				3.0 to 3.6		_	3.9	5.2	
Input capacitance	C <sub>IN</sub>		_	3.6	_	_	3	_	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 1)	_	0.9 to 3.6	_	_	9	_	pF

Note 1:  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.  $I_{CC(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

# 9.5. AC Characteristics (Unless otherwise specified, $T_a$ = -40 to 85 °C, Input: $t_r$ = $t_f$ = 3 ns)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Max	Unit
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>	$R_L = 1 M\Omega$	0.9	10	_	_	ns
		See Fig. 9.7.1, Table 9.7.1	1.1 to 1.3		1.0	35.9	
		Table 9.7.1	1.4 to 1.6		1.0	11.3	
			1.65 to 1.95		1.0	8.2	
			2.3 to 2.7	]	1.0	5.8	
			3.0 to 3.6		1.0	4.6	
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>	$R_L = 1 M\Omega$	0.9	15	_	_	ns
		See Fig. 9.7.1, Table 9.7.1	1.1 to 1.3	]	1.0	41.8	
		Table 9.7.1	1.4 to 1.6		1.0	12.6	
			1.65 to 1.95		1.0	8.7	
			2.3 to 2.7		1.0	6.1	
			3.0 to 3.6		1.0	5.0	
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>	$R_L = 1 M\Omega$	0.9	30	_	_	ns
		See Fig. 9.7.1, Table 9.7.1	1.1 to 1.3		1.0	58.1	
		Table 9.7.1	1.4 to 1.6		1.0	17.6	
			1.65 to 1.95	]	1.0	11.7	
			2.3 to 2.7	]	1.0	8.1	
			3.0 to 3.6		1.0	6.1	



# 9.6. AC Characteristics (Unless otherwise specified, $T_a$ = -40 to 125 °C, Input: $t_r$ = $t_f$ = 3 ns)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Max	Unit
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>	$R_L = 1 M\Omega$	0.9	10	_	_	ns
		See Fig. 9.7.1, Table 9.7.1	1.1 to 1.3		1.0	44.8	
		14016 9.7.1	1.4 to 1.6	]	1.0	11.9	
			1.65 to 1.95		1.0	8.5	
			2.3 to 2.7		1.0	6.1	
			3.0 to 3.6	]	1.0	4.8	
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>	$R_L = 1 M\Omega$	0.9	15	_	_	ns
		See Fig. 9.7.1, Table 9.7.1	1.1 to 1.3		1.0	53.0	
		Table 9.7.1	1.4 to 1.6		1.0	13.4	
			1.65 to 1.95		1.0	8.9	
			2.3 to 2.7		1.0	6.4	
			3.0 to 3.6	]	1.0	5.3	
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>	$R_L = 1 M\Omega$	0.9	30	_	_	ns
		See Fig. 9.7.1, Table 9.7.1	1.1 to 1.3		1.0	73.1	
		Table 5.7.1	1.4 to 1.6	]	1.0	18.8	
			1.65 to 1.95	1	1.0	12.4	
			2.3 to 2.7	]	1.0	8.9	
			3.0 to 3.6		1.0	6.7	

## 9.7. AC Waveform

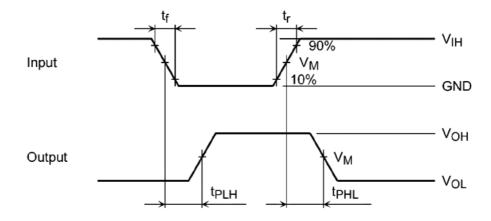


Fig. 9.7.1 t<sub>PLH</sub>, t<sub>PHL</sub>

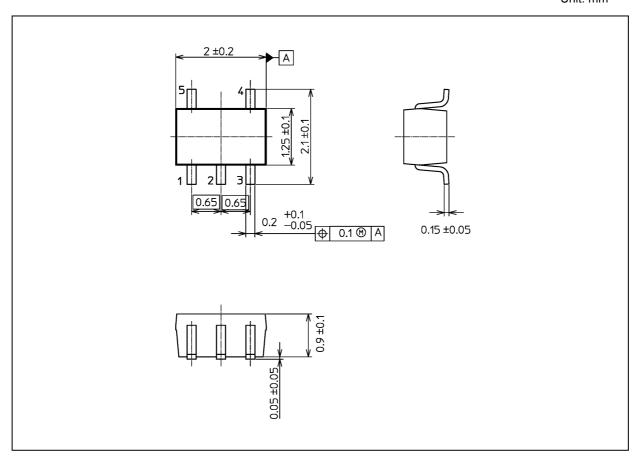
Table 9.7.1 AC Waveform Symbols

	Symbol	V <sub>CC</sub> = 3.3 ± 0.3 V	V <sub>CC</sub> = 2.5 ± 0.2 V	V <sub>CC</sub> = 1.8 ± 0.15 V	V <sub>CC</sub> = 1.5 ± 0.1 V	V <sub>CC</sub> = 1.2 ± 0.1 V	V <sub>CC</sub> = 0.9 V
Input	V <sub>IH</sub>	V <sub>CC</sub>	V <sub>CC</sub>	V <sub>CC</sub>	V <sub>CC</sub>	V <sub>CC</sub>	V <sub>CC</sub>
	V <sub>M</sub>	V <sub>CC</sub> /2	V <sub>CC</sub> /2	V <sub>CC</sub> /2	V <sub>CC</sub> /2	V <sub>CC</sub> /2	V <sub>CC</sub> /2
Output	V <sub>M</sub>	V <sub>CC</sub> /2	V <sub>CC</sub> /2	V <sub>CC</sub> /2	V <sub>CC</sub> /2	V <sub>CC</sub> /2	V <sub>CC</sub> /2



# **Package Dimensions**

Unit: mm



Weight: 6.2 mg (typ.)

	Package Name(s)
Nickname: USV	



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