

CMOS Digital Integrated Circuits Silicon Monolithic

74LCX32FT

1. Functional Description

· Low-Voltage Quad 2-Input OR Gate with 5-V Tolerant Inputs and Outputs

2. General

The 74LCX32FT is a high-performance CMOS 2-input OR gate. Designed for use in 3.3 V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

The device is designed for low-voltage $(3.3\ V)\ V_{CC}$ applications, but it could be used to interface to 5 V supply environment for inputs.

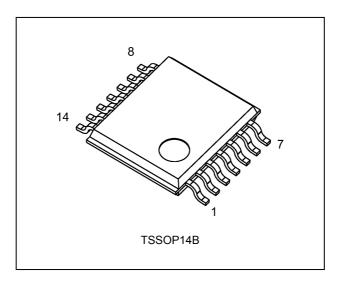
All inputs are equipped with protection circuits against static discharge.

3. Features

- (1) AEC-Q100 (Rev. H) (Note 1)
- (2) Wide operating temperature range: $T_{opr} = -40$ to 125 °C
- (3) Low-voltage operation: $V_{CC} = 1.65$ to 3.6 V
- (4) High-speed operation: $t_{pd} = 6.5 \text{ ns (max)} (V_{CC} = 3.3 \pm 0.3 \text{ V})$
- (5) Output current: $|I_{OH}|/I_{OL} = 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$
- (6) Power-down protection provided on all inputs and outputs
- (7) Pin and function compatible with the 74 series (74LVC/ALVC etc.) 32 type

Note 1: This device is compliant with the reliability requirements of AEC-Q100. For details, contact your Toshiba sales representative.

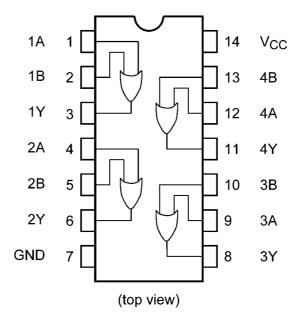
4. Packaging



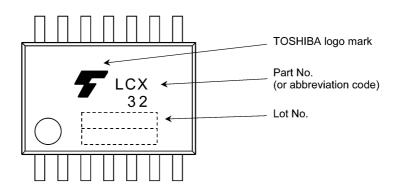
Start of commercial production



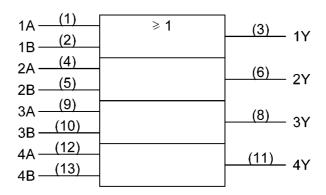
5. Pin Assignment



6. Marking



7. IEC Logic Symbol





8. Truth Table

Inputs A	Inputs B	Outputs Y
Ĺ	L	Ĺ
L	Н	Н
Н	L	Н
Н	Н	Н

9. Absolute Maximum Ratings (Note)

Characteristics	Symbol Note Rating		Unit	
Supply voltage	V _{CC}		-0.5 to 6.5	V
Input voltage	V _{IN}		-0.5 to 6.5	V
Output voltage	V _{OUT}	(Note 1)	-0.5 to 6.5	V
		(Note 2)	-0.5 to V _{CC} + 0.5	
Input diode current	I _{IK}		-50	mA
Output diode current	I _{OK}	(Note 3)	±50	mA
Output current	I _{OUT}		±50	mA
Power dissipation	P _D	(Note 4)	180	mW
V _{CC} /ground current	I _{CC} /I _{GND}		±100	mA
Storage temperature	T _{stg}		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0 V$

Note 2: High or low state. I_{OUT} absolute maximum rating must be observed.

Note 3: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$

Note 4: 180 mW in the range of T_a = -40 to 85 °C. From T_a = 85 to 125 °C a derating factor of -3.25 mW/°C shall be applied until 50 mW.



10. Operating Ranges (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CC}		1.65 to 3.6	V
		(Note 1)	1.5 to 3.6	
Input voltage	V _{IN}		0 to 5.5	V
Output voltage	V _{OUT}	(Note 2)	0 to 5.5	V
		(Note 3)	0 to V _{CC}	
Output current	I _{OH} ,I _{OL}	(Note 4)	±24	mA
		(Note 5)	±12	
Operating temperature	T _{opr}		-40 to 125	°C
Input rise and fall times	dt/dv	(Note 6)	0 to 10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either V_{CC} or GND.

Note 1: Data retention only

Note 2: $V_{CC} = 0 V$

Note 3: High or low state Note 4: V_{CC} = 3.0 to 3.6 V

Note 5: V_{CC} = 2.7 to 3.0 V

Note 6: V_{IN} = 0.8 to 2.0 V, V_{CC} = 3.0 V

11. Electrical Characteristics

11.1. DC Characteristics (Unless otherwise specified, Ta = -40 to 85 °C)

Characteristics	Symbol	Test Condition	า	V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	_		1.65 to 2.3	$V_{CC} \times 0.9$	_	V
				2.3 to 2.7	1.7	_]
				2.7 to 3.6	2.0	_]
Low-level input voltage	V _{IL}	_		1.65 to 2.3	_	V _{CC} × 0.1	V
				2.3 to 2.7	_	0.7]
				2.7 to 3.6	_	0.8	
High-level output voltage	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -100 μA	1.65 to 3.6	V _{CC} - 0.2	_	V
			I_{OH} = -4 mA	1.65	1.05		
			I_{OH} = -8 mA	2.3	1.7	_	
			I _{OH} = -12 mA	2.7	2.2	_	
			I _{OH} = -18 mA	3.0	2.4	_	
			I _{OH} = -24 mA	3.0	2.2	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IL}	I _{OL} = 100 μA	1.65 to 3.6	_	0.2	V
			I _{OL} = 4 mA	1.65	_	0.45	
			I _{OL} = 8 mA	2.3	_	0.7	
			I _{OL} = 12 mA	2.7	_	0.4]
			I _{OL} = 16 mA	3.0	_	0.4]
			I _{OL} = 24 mA	3.0	_	0.55	
Input leakage current	I _{IN}	V _{IN} = 0 to 5.5 V	•	1.65 to 3.6	_	±5.0	μА
Power-OFF leakage current	I _{OFF}	V _{IN} /V _{OUT} = 5.5 V		0		10.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		1.65 to 3.6		10.0	μА
		V _{IN} = 3.6 to 5.5 V	-	1.65 to 3.6	_	±10.0	
	Δl _{CC}	V _{IH} = V _{CC} - 0.6 V (per input)		2.7 to 3.6	_	500	μА



11.2. DC Characteristics (Unless otherwise specified, T_a = -40 to 125 °C)

Characteristics	Symbol	Test Condition	า	V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	_		1.65 to 2.3	$V_{CC} \times 0.9$	_	V
				2.3 to 2.7	1.7	_	
				2.7 to 3.6	2.0	_	
Low-level input voltage	V _{IL}	_		1.65 to 2.3	_	V _{CC} × 0.1	V
				2.3 to 2.7	_	0.7	
				2.7 to 3.6	_	0.8	
High-level output voltage	V _{OH}	$V_{IN} = V_{IH}$ or V_{IL}	I_{OH} = -100 μ A	1.65 to 3.6	V _{CC} - 0.2	_	V
			I_{OH} = -4 mA	1.65	0.9	_	
			I_{OH} = -8 mA	2.3	1.55	_	
			I _{OH} = -12 mA	2.7	2.0	_	
			I _{OH} = -18 mA	3.0	2.2	_	
			I _{OH} = -24 mA	3.0	2.0	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IL}	I _{OL} = 100 μA	1.65 to 3.6	_	0.2	V
			I _{OL} = 4 mA	1.65	_	0.65	
			I _{OL} = 8 mA	2.3	_	0.9	
			I _{OL} = 12 mA	2.7	_	0.6	
			I _{OL} = 16 mA	3.0	_	0.6	
			I _{OL} = 24 mA	3.0	_	0.75	
Input leakage current	I _{IN}	V _{IN} = 0 to 5.5 V		1.65 to 3.6	_	±20.0	μΑ
Power-OFF leakage current	I _{OFF}	V _{IN} /V _{OUT} = 5.5 V		0	_	40.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		1.65 to 3.6	_	40.0	μА
		V _{IN} = 3.6 to 5.5 V		1.65 to 3.6	_	±40.0	
	Δl _{CC}	$V_{IH} = V_{CC} - 0.6 V$ (per input)		2.7 to 3.6	_	5.0	mA

11.3. AC Characteristics (Unless otherwise specified, T_a = -40 to 85 °C)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}		See 11.7.1 AC Test Circuit,	1.8 ± 0.15	_	20.0	ns
			Fig. 11.8.1, Table 11.8.1	2.5 ± 0.2	_	7.2	
				2.7	_	6.2	
				3.3 ± 0.3	1.5	5.5	
Output skew	t _{osLH} ,t _{osHL}	(Note 1)	_	2.7	_	_	ns
				3.3 ± 0.3		1.0	

Note 1: Parameter guaranteed by design. ($t_{osLH} = |t_{PLH}m - t_{PLH}n|$, $t_{osHL} = |t_{PHL}m - t_{PHL}n|$)

11.4. AC Characteristics (Unless otherwise specified, T_a = -40 to 125 °C)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}		See 11.7.1 AC Test Circuit,	1.8 ± 0.15		22.0	ns
			Table 11.8.1, Fig. 11.8.1	2.5 ± 0.2		8.0	
				2.7		7.0	
				3.3 ± 0.3	1.5	6.5	
Output skew	t _{osLH} ,t _{osHL}	(Note 1)	_	2.7	_	_	ns
				3.3 ± 0.3		1.0	

Note 1: Parameter guaranteed by design. $(t_{osLH} = |t_{PLH}m-t_{PLH}n|, t_{osHL} = |t_{PHL}m-t_{PHL}n|)$



11.5. Dynamic Switching Characteristics (Unless otherwise specified, T_a = 25 °C, Input: t_r = t_f = 2.5 ns, C_L = 50 pF, R_L = 500 Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic V _{OL}	V _{OLP}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V
Quiet output minimum dynamic V _{OL}	V _{OLV}	V _{IH} = 3.3 V,V _{IL} = 0 V	3.3	0.8	V

11.6. Capacitive Characteristics (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}		_	3.3	7	pF
Output capacitance	C _{OUT}		_	0	8	pF
Power dissipation capacitance	C _{PD}	(Note 1)	f _{IN} =10 MHz	3.3	25	pF

Note 1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.

 $I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{IN} + I_{CC}/4$ (per 1 gate)

11.7. AC Test Circuit

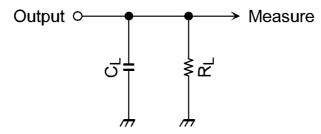


Fig. 11.7.1 AC Test Circuit

11.8. AC Waveform

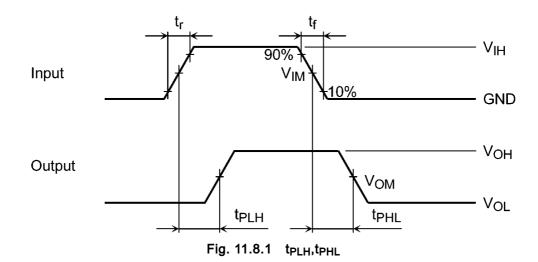


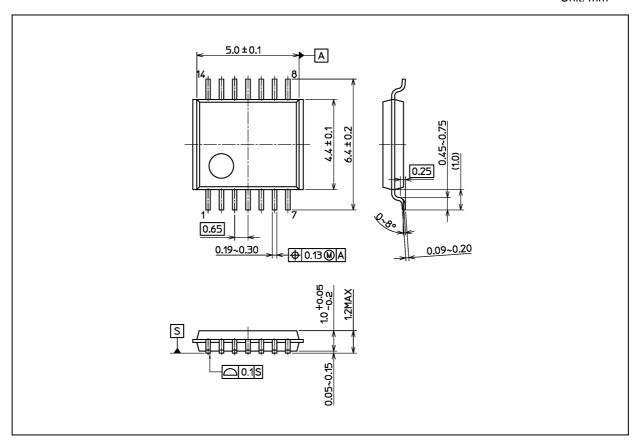
Table 11.8.1 AC Waveform Symbols

	Symbol	$V_{CC} = 3.3 \pm 0.3 \text{ V}$ $V_{CC} = 2.7 \text{ V}$	V_{CC} = 2.5 ± 0.2 V	V _{CC} = 1.8 ± 0.15 V
Input	V _{IH}	2.7 V	V _{CC}	V _{CC}
	V _{IM}	1.5 V	V _{CC} /2	V _{CC} /2
	t _r , t _f	2.5 ns	2.0 ns	2.0 ns
Output	V _{OM}	1.5 V	V _{OH} /2	V _{OH} /2
Load	C _L	50 pF	30 pF	30 pF
	R_L	500 Ω	500 Ω	1 kΩ



Package Dimensions

Unit: mm



Weight: 0.054 g (typ.)

Package Name(s)

Nickname: TSSOP14B



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