

# 74HC74D

## 1. Functional Description

- Dual D-Type Flip-Flop with Preset and Clear

## 2. General

The 74HC74D is a high speed CMOS D FLIP FLOP fabricated with silicon gate C<sup>2</sup>MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

The signal level applied to the D INPUT is transferred to Q OUTPUT during the positive going transition of the CLOCK pulse.

$\overline{\text{CLEAR}}$  and  $\overline{\text{PRESET}}$  are independent of the CLOCK and are accomplished by setting the appropriate input to an "L" level.

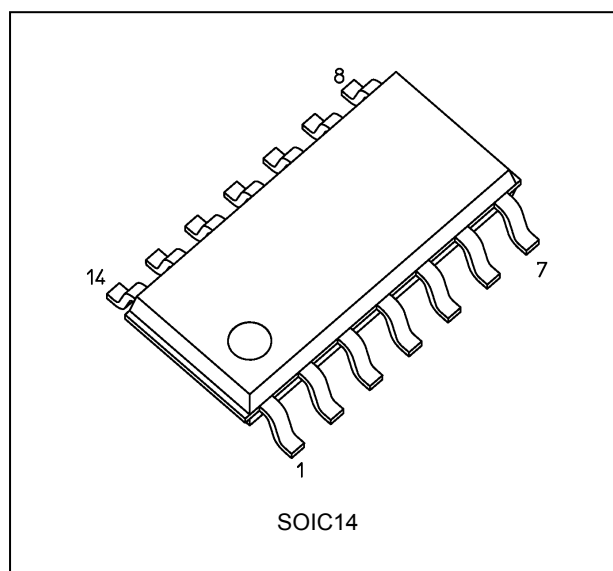
All inputs are equipped with protection circuits against static discharge or transient excess voltage.

## 3. Features

- (1) Wide operating temperature range:  $T_{opr} = -40$  to  $125^{\circ}\text{C}$  (Note 1)
- (2) High speed:  $f_{MAX} = 77$  MHz (typ.) at  $V_{CC} = 5$  V
- (3) Low power dissipation:  $I_{CC} = 2.0$   $\mu\text{A}$  (max) at  $T_a = 25^{\circ}\text{C}$
- (4) Balanced propagation delays:  $t_{PLH} \approx t_{PHL}$
- (5) Wide operating voltage range:  $V_{CC(opr)} = 2.0$  to  $6.0$  V

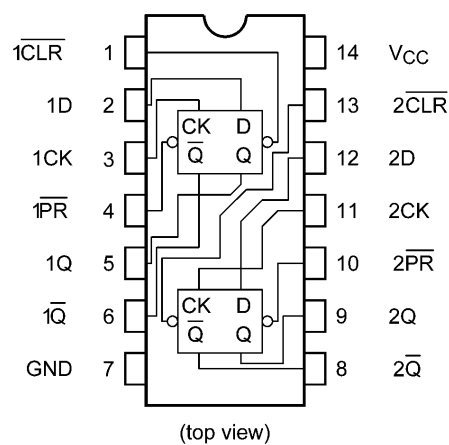
Note 1: Operating Range spec of  $T_{opr} = -40^{\circ}\text{C}$  to  $125^{\circ}\text{C}$  is applicable only for the products which manufactured after July 2020.

## 4. Packaging

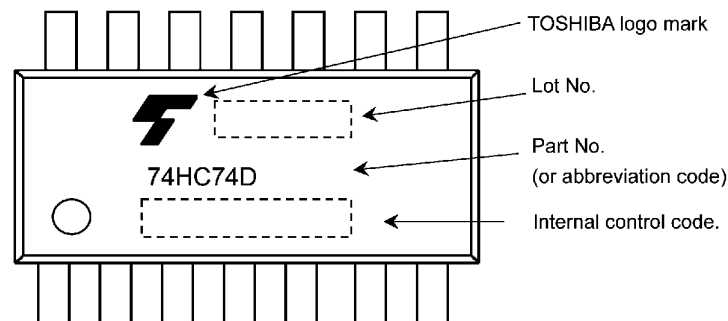


Start of commercial production  
2020-07

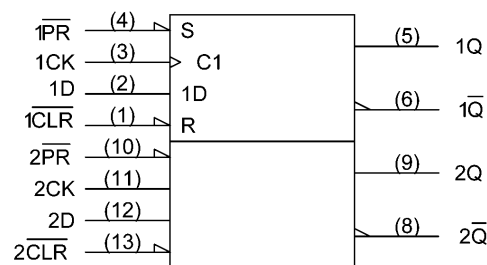
5. Pin Assignment



6. Marking



7. IEC Logic Symbol

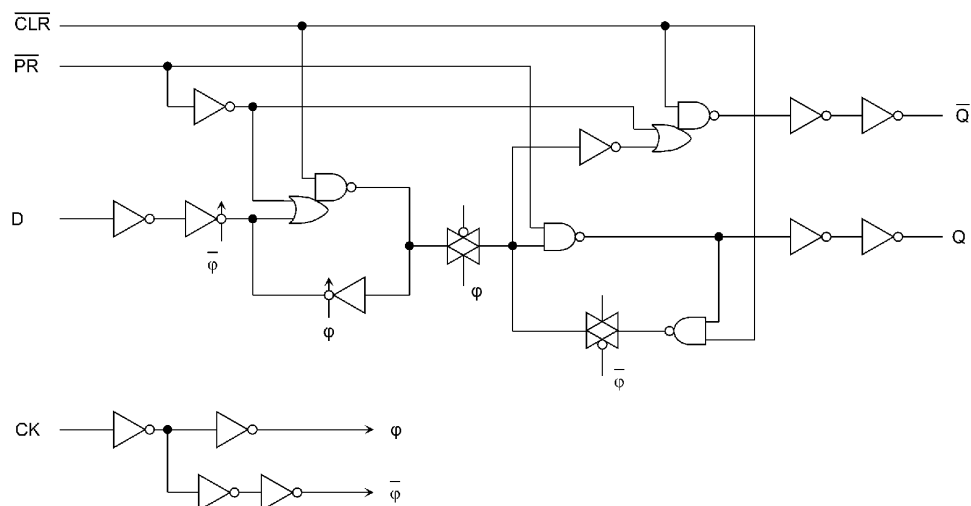


8. Truth Table

Inputs				Outputs		Function
CLR	PR	D	CK	Q	Q	
L	H	X	X	L	H	Clear
H	L	X	X	H	L	Preset
L	L	X	X	H	H	—
H	H	L	↑	L	H	—
H	H	H	↑	H	L	—
H	H	X	↓	Q <sub>n</sub>	Q <sub>n</sub>	No Change

X: Don't care

### 9. System Diagram



## 10. Absolute Maximum Ratings (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	$V_{CC}$		-0.5 to 7.0	V
Input voltage	$V_{IN}$		-0.5 to $V_{CC} + 0.5$	V
Output voltage	$V_{OUT}$		-0.5 to $V_{CC} + 0.5$	V
Input diode current	$I_{IK}$		$\pm 20$	mA
Output diode current	$I_{OK}$		$\pm 20$	mA
Output current	$I_{OUT}$		$\pm 25$	mA
$V_{CC}$ /ground current	$I_{CC}$		$\pm 50$	mA
Power dissipation	$P_D$	(Note 1)	500	mW
Storage temperature	$T_{stg}$		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $P_D$  derates linearly with -8 mW/°C above 85 °C

## 11. Operating Ranges (Note)

Characteristics	Symbol	Test Condition	Note	Rating	Unit
Supply voltage	$V_{CC}$			2.0 to 6.0	V
Input voltage	$V_{IN}$			0 to $V_{CC}$	V
Output voltage	$V_{OUT}$			0 to $V_{CC}$	V
Operating temperature	$T_{opr}$		(Note 1)	-40 to 125	°C
Input rise and fall times	$t_r, t_f$	$V_{CC} = 2.0 \text{ V}$		0 to 1000	ns
		$V_{CC} = 4.5 \text{ V}$		0 to 500	
		$V_{CC} = 6.0 \text{ V}$		0 to 400	

Note: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either  $V_{CC}$  or GND.

Note 1: Operating Range spec of  $T_{opr} = -40 \text{ °C}$  to  $125 \text{ °C}$  is applicable only for the products which manufactured after July 2020.

## 12. Electrical Characteristics

### 12.1. DC Characteristics (Unless otherwise specified, $T_a = 25\text{ }^{\circ}\text{C}$ )

Characteristics	Symbol	Test Condition		$V_{CC}$ (V)	Min	Typ.	Max	Unit
High-level input voltage	$V_{IH}$	—		2.0	1.50	—	—	V
				4.5	3.15	—	—	
				6.0	4.20	—	—	
Low-level input voltage	$V_{IL}$	—		2.0	—	—	0.50	V
				4.5	—	—	1.35	
				6.0	—	—	1.80	
High-level output voltage	$V_{OH}$	$V_{IN} = V_{IH}$ or $V_{IL}$	$I_{OH} = -20\text{ }\mu\text{A}$	2.0	1.9	2.0	—	V
				4.5	4.4	4.5	—	
				6.0	5.9	6.0	—	
			$I_{OH} = -4\text{ mA}$	4.5	4.18	4.31	—	
			$I_{OH} = -5.2\text{ mA}$	6.0	5.68	5.80	—	
Low-level output voltage	$V_{OL}$	$V_{IN} = V_{IH}$ or $V_{IL}$	$I_{OL} = 20\text{ }\mu\text{A}$	2.0	—	0.0	0.1	V
				4.5	—	0.0	0.1	
				6.0	—	0.0	0.1	
			$I_{OL} = 4\text{ mA}$	4.5	—	0.17	0.26	
			$I_{OL} = 5.2\text{ mA}$	6.0	—	0.18	0.26	
Input leakage current	$I_{IN}$	$V_{IN} = V_{CC}$ or GND		6.0	—	—	$\pm 0.1$	$\mu\text{A}$
Quiescent supply current	$I_{CC}$	$V_{IN} = V_{CC}$ or GND		6.0	—	—	2.0	$\mu\text{A}$

### 12.2. DC Characteristics (Unless otherwise specified, $T_a = -40\text{ to }85\text{ }^{\circ}\text{C}$ )

Characteristics	Symbol	Test Condition		$V_{CC}$ (V)	Min	Max	Unit
High-level input voltage	$V_{IH}$	—		2.0	1.50	—	V
				4.5	3.15	—	
				6.0	4.20	—	
Low-level input voltage	$V_{IL}$	—		2.0	—	0.50	V
				4.5	—	1.35	
				6.0	—	1.80	
High-level output voltage	$V_{OH}$	$V_{IN} = V_{IH}$ or $V_{IL}$	$I_{OH} = -20\text{ }\mu\text{A}$	2.0	1.9	—	V
				4.5	4.4	—	
				6.0	5.9	—	
			$I_{OH} = -4\text{ mA}$	4.5	4.13	—	
			$I_{OH} = -5.2\text{ mA}$	6.0	5.63	—	
Low-level output voltage	$V_{OL}$	$V_{IN} = V_{IH}$ or $V_{IL}$	$I_{OL} = 20\text{ }\mu\text{A}$	2.0	—	0.1	V
				4.5	—	0.1	
				6.0	—	0.1	
			$I_{OL} = 4\text{ mA}$	4.5	—	0.33	
			$I_{OL} = 5.2\text{ mA}$	6.0	—	0.33	
Input leakage current	$I_{IN}$	$V_{IN} = V_{CC}$ or GND		6.0	—	$\pm 1.0$	$\mu\text{A}$
Quiescent supply current	$I_{CC}$	$V_{IN} = V_{CC}$ or GND		6.0	—	20.0	$\mu\text{A}$

### 12.3. DC Characteristics (Note) (Unless otherwise specified, $T_a = -40$ to $125\text{ }^{\circ}\text{C}$ )

Characteristics	Symbol	Test Condition		$V_{CC}$ (V)	Min	Max	Unit
High-level input voltage	$V_{IH}$	—		2.0	1.50	—	V
				4.5	3.15	—	
				6.0	4.20	—	
Low-level input voltage	$V_{IL}$	—		2.0	—	0.50	V
				4.5	—	1.35	
				6.0	—	1.80	
High-level output voltage	$V_{OH}$	$V_{IN} = V_{IH}$ or $V_{IL}$	$I_{OH} = -20\text{ }\mu\text{A}$	2.0	1.9	—	V
				4.5	4.4	—	
				6.0	5.9	—	
			$I_{OH} = -4\text{ mA}$	4.5	3.7	—	
			$I_{OH} = -5.2\text{ mA}$	6.0	5.2	—	
Low-level output voltage	$V_{OL}$	$V_{IN} = V_{IH}$ or $V_{IL}$	$I_{OL} = 20\text{ }\mu\text{A}$	2.0	—	0.1	V
				4.5	—	0.1	
				6.0	—	0.1	
			$I_{OL} = 4\text{ mA}$	4.5	—	0.4	
			$I_{OL} = 5.2\text{ mA}$	6.0	—	0.4	
Input leakage current	$I_{IN}$	$V_{IN} = V_{CC}$ or GND		6.0	—	$\pm 1.0$	$\mu\text{A}$
Quiescent supply current	$I_{CC}$	$V_{IN} = V_{CC}$ or GND		6.0	—	40.0	$\mu\text{A}$

Note: Operating Range spec of  $T_{opr} = -40\text{ }^{\circ}\text{C}$  to  $125\text{ }^{\circ}\text{C}$  is applicable only for the products which manufactured after July 2020.

### 12.4. Timing Requirements (Unless otherwise specified, $T_a = 25^\circ\text{C}$ , Input: $t_r = t_f = 6$ ns)

Characteristics	Symbol	Test Condition	$V_{CC}$ (V)	Limit	Unit
Minimum pulse width (CK)	$t_{w(L)}, t_{w(H)}$	—	2.0	75	ns
			4.5	15	
			6.0	13	
Minimum pulse width (CLR, PR)	$t_{w(L)}$	—	2.0	75	ns
			4.5	15	
			6.0	13	
Minimum setup time	$t_s$	—	2.0	75	ns
			4.5	15	
			6.0	13	
Minimum hold time	$t_h$	—	2.0	0	ns
			4.5	0	
			6.0	0	
Minimum removal time (CLR, PR)	$t_{rem}$	—	2.0	25	ns
			4.5	5	
			6.0	4	
Clock frequency	f	—	2.0	6	MHz
			4.5	31	
			6.0	36	

### 12.5. Timing Requirements (Unless otherwise specified, $T_a = -40$ to $85^\circ\text{C}$ , Input: $t_r = t_f = 6$ ns)

Characteristics	Symbol	Test Condition	$V_{CC}$ (V)	Limit	Unit
Minimum pulse width (CK)	$t_{w(L)}, t_{w(H)}$	—	2.0	95	ns
			4.5	19	
			6.0	16	
Minimum pulse width (CLR, PR)	$t_{w(L)}$	—	2.0	95	ns
			4.5	19	
			6.0	16	
Minimum setup time	$t_s$	—	2.0	95	ns
			4.5	19	
			6.0	16	
Minimum hold time	$t_h$	—	2.0	0	ns
			4.5	0	
			6.0	0	
Minimum removal time (CLR, PR)	$t_{rem}$	—	2.0	30	ns
			4.5	6	
			6.0	5	
Clock frequency	f	—	2.0	5	MHz
			4.5	25	
			6.0	29	

### 12.6. Timing Requirements (Note) (Unless otherwise specified, $T_a = -40$ to $125\text{ }^{\circ}\text{C}$ , Input: $t_r = t_f = 6\text{ ns}$ )

Characteristics	Symbol	Test Condition	$V_{CC}$ (V)	Limit	Unit
Minimum pulse width (CK)	$t_{w(L)}, t_{w(H)}$	—	2.0	110	ns
			4.5	22	
			6.0	18	
Minimum pulse width (CLR, PR)	$t_{w(L)}$	—	2.0	110	ns
			4.5	22	
			6.0	18	
Minimum setup time	$t_s$	—	2.0	110	ns
			4.5	22	
			6.0	18	
Minimum hold time	$t_h$	—	2.0	0	ns
			4.5	0	
			6.0	0	
Minimum removal time (CLR, PR)	$t_{rem}$	—	2.0	34	ns
			4.5	7	
			6.0	6	
Clock frequency	f	—	2.0	4	MHz
			4.5	21	
			6.0	24	

Note: Operating Range spec of  $T_{opr} = -40\text{ }^{\circ}\text{C}$  to  $125\text{ }^{\circ}\text{C}$  is applicable only for the products which manufactured after July 2020.

### 12.7. AC Characteristics (Unless otherwise specified, $C_L = 15\text{ pF}$ , $V_{CC} = 5\text{ V}$ , $T_a = 25\text{ }^{\circ}\text{C}$ , Input: $t_r = t_f = 6\text{ ns}$ )

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Output transition time	$t_{TLH}, t_{THL}$	—	—	6	12	ns
Propagation delay time (CK-Qn, $\overline{Q}$ )	$t_{PLH}, t_{PHL}$	—	—	13	26	ns
Propagation delay time ( $\overline{\text{CLR}}$ , $\overline{\text{PR}}$ -Q, $\overline{Q}$ )	$t_{PLH}, t_{PHL}$	—	—	14	26	ns
Maximum clock frequency	$f_{MAX}$	—	36	77	—	MHz



### 12.8. AC Characteristics

(Unless otherwise specified,  $C_L = 50 \text{ pF}$ ,  $T_a = 25 \text{ }^\circ\text{C}$ , Input:  $t_r = t_f = 6 \text{ ns}$ )

Characteristics	Symbol	Note	$V_{CC} \text{ (V)}$	Min	Typ.	Max	Unit
Output transition time	$t_{TLH}, t_{THL}$		2.0	—	30	75	ns
			4.5	—	8	15	
			6.0	—	7	13	
Propagation delay time (CK-Q, $\bar{Q}$ )	$t_{PLH}, t_{PHL}$		2.0	—	48	150	ns
			4.5	—	16	30	
			6.0	—	13	26	
Propagation delay time (CLR, PR-Q, $\bar{Q}$ )	$t_{PLH}, t_{PHL}$		2.0	—	51	150	ns
			4.5	—	17	30	
			6.0	—	15	26	
Maximum clock frequency	$f_{MAX}$		2.0	6	21	—	MHz
			4.5	31	63	—	
			6.0	36	67	—	
Input capacitance	$C_{IN}$		—	—	5	—	pF
Power dissipation capacitance	$C_{PD}$	(Note 1)	—	—	34	—	pF

Note 1:  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

$$I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{IN} + I_{CC}/2 \text{ (per F/F)}$$

### 12.9. AC Characteristics

(Unless otherwise specified,  $C_L = 50 \text{ pF}$ ,  $T_a = -40 \text{ to } 85 \text{ }^\circ\text{C}$ , Input:  $t_r = t_f = 6 \text{ ns}$ )

Characteristics	Symbol	$V_{CC} \text{ (V)}$	Min	Max	Unit
Output transition time	$t_{TLH}, t_{THL}$	2.0	—	95	ns
		4.5	—	19	
		6.0	—	16	
Propagation delay time (CK-Q, $\bar{Q}$ )	$t_{PLH}, t_{PHL}$	2.0	—	190	ns
		4.5	—	38	
		6.0	—	32	
Propagation delay time (CLR, PR-Q, $\bar{Q}$ )	$t_{PLH}, t_{PHL}$	2.0	—	190	ns
		4.5	—	38	
		6.0	—	32	
Maximum clock frequency	$f_{MAX}$	2.0	5	—	MHz
		4.5	25	—	
		6.0	29	—	

### 12.10. AC Characteristics (Note)

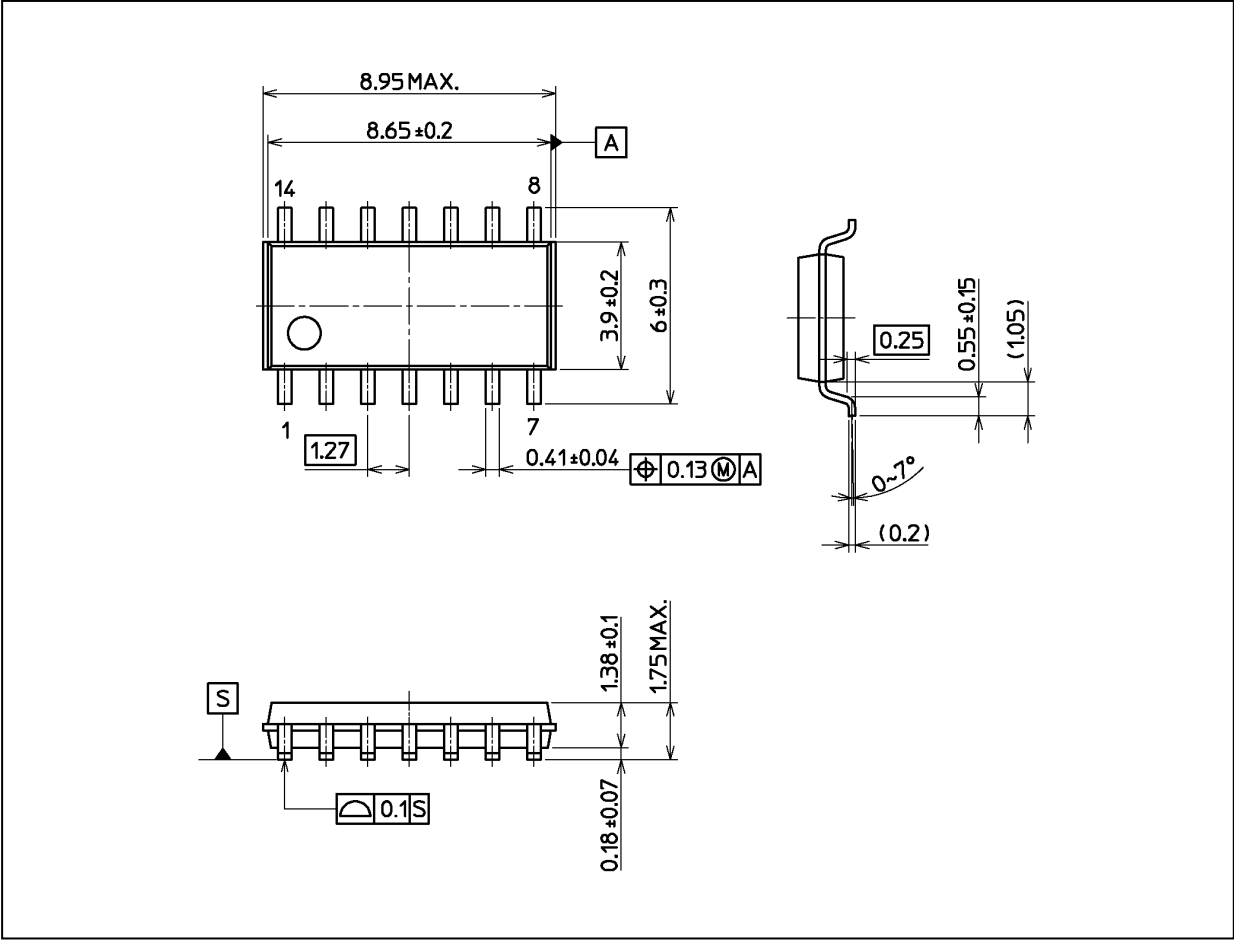
(Unless otherwise specified,  $C_L = 50 \text{ pF}$ ,  $T_a = -40 \text{ to } 125 \text{ }^\circ\text{C}$ , Input:  $t_r = t_f = 6 \text{ ns}$ )

Characteristics	Symbol	$V_{CC} \text{ (V)}$	Min	Max	Unit
Output transition time	$t_{TLH}, t_{THL}$	2.0	—	110	ns
		4.5	—	22	
		6.0	—	18	
Propagation delay time (CK-Q, $\bar{Q}$ )	$t_{PLH}, t_{PHL}$	2.0	—	220	ns
		4.5	—	44	
		6.0	—	36	
Propagation delay time (CLR, PR-Q, $\bar{Q}$ )	$t_{PLH}, t_{PHL}$	2.0	—	220	ns
		4.5	—	44	
		6.0	—	36	
Maximum clock frequency	$f_{MAX}$	2.0	4	—	MHz
		4.5	21	—	
		6.0	24	—	

Note: Operating Range spec of  $T_{opr} = -40 \text{ }^\circ\text{C}$  to  $125 \text{ }^\circ\text{C}$  is applicable only for the products which manufactured after July 2020.

Package Dimensions

Unit: mm



Weight: 0.13 g (typ.)

Package Name(s)
Nickname: SOIC14

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