CMOS Digital Integrated Circuits Silicon Monolithic

74HC238D

1. Functional Description

3-to-8 Line Decoder

2. General

The 74HC238D is a high speed CMOS 3-to-8 DECODER fabricated with silicon gate C²MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

When the device is enabled, 3 Binary Select inputs (A, B and C) determine which one of the outputs (Y0 - Y7) will go high.

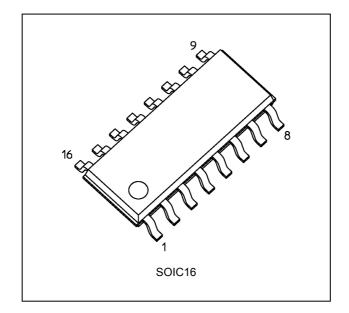
When enable input G1 is held low or either $\overline{G}2A$ or $\overline{G}2B$ is held high, decoding function is inhibited and all the outputs go low. G1, $\overline{G}2A$, and $\overline{G}2B$ inputs are provided to ease cascade connection and for use as an address decoder for memory systems.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

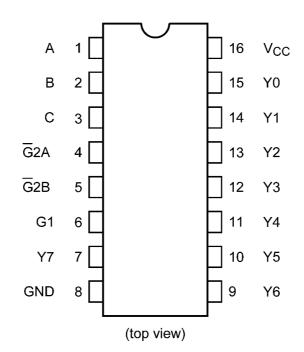
3. Features

- (1) High speed: t_{pd} = 14 ns (typ.) at V_{CC} = 5 V
- (2) Low power dissipation: I_{CC} = 4.0 μA (max) at T_a = 25 $^{\circ}\text{C}$
- (3) Balanced propagation delays: $t_{PLH} \approx t_{PHL}$
- (4) Wide operating voltage range: $V_{CC(opr)} = 2.0 \text{ V to } 6.0 \text{ V}$

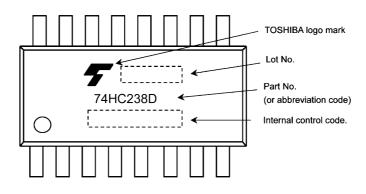
4. Packaging



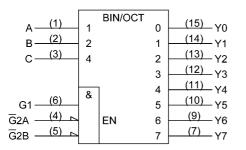
TOSHIBA 5. Pin Assignment



6. Marking



7. IEC Logic Symbol



| A (1) B (2) C (3) G1 (6) | 1 2 & | DMUX G ⁰ 7 | 0 1 2 3 4 5 | (15) Y0 (14) Y1 (13) Y2 (12) Y3 (11) Y4 (10) Y5 |
|-----------------------------------|-------------|--------------------------|----------------------------|--|
| G1 <u>(6)</u> | à | | 5 | <u>(10)</u> Y5 |
| G2A <u>(4)</u> | | | 6 | <u>(9)</u> Y6 |
| G2B <u>(5)</u> ⊳ | | | 7 | <u>(7)</u> Y7 |

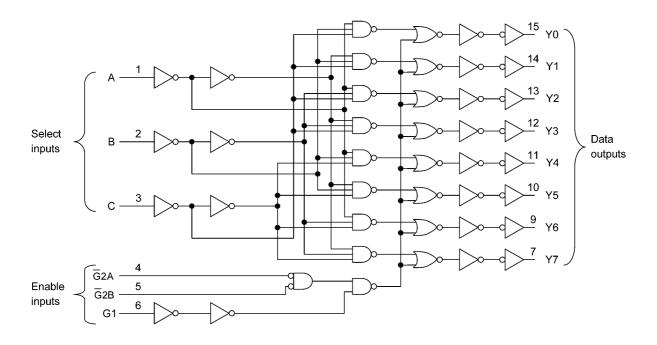
TOSHIBA

8. Truth Table

| | | Inp | uts | | | Outputs | | | | | | | | |
|----|--------|-----|-----|--------|---|---------|-----|-------|----|----|----|-----|----|--------------------|
| | Enable | | | Select | | YO | Y1 | Y2 | Y3 | Y4 | Y5 | Y6 | V7 | Selected Output |
| G1 | G2A | G2B | С | В | А | | Y I | Y1 Y2 | 13 | ¥4 | 15 | Ϋ́Ο | Y7 | |
| L | X | Х | Х | х | х | L | L | L | L | L | L | L | L | None |
| Х | н | Х | Х | Х | Х | L | L | L | L | L | L | L | L | None |
| Х | х | н | Х | х | Х | L | L | L | L | L | L | L | L | None |
| н | L | L | L | L | L | н | L | L | L | L | L | L | L | Y0 |
| н | L | L | L | L | н | L | н | L | L | L | L | L | L | Y1 |
| н | L | L | L | н | L | L | L | н | L | L | L | L | L | Y2 |
| н | L | L | L | н | н | L | L | L | н | L | L | L | L | Y3 |
| н | L | L | Н | L | L | L | L | L | L | н | L | L | L | Y4 |
| н | L | L | н | L | н | L | L | L | L | L | н | L | L | Y5 |
| н | L | L | н | н | L | L | L | L | L | L | L | н | L | Y6 |
| н | L | L | н | н | н | L | L | L | L | L | L | L | н | Y7 |

X: Don't care

9. Logic Diagram



10. Absolute Maximum Ratings (Note)

| Characteristics | Symbol | Note | Rating | Unit |
|---------------------------------|------------------|----------|-------------------------------|------|
| Supply voltage | V _{CC} | | -0.5 to 7.0 | V |
| Input voltage | V _{IN} | | -0.5 to V _{CC} + 0.5 | |
| Output voltage | V _{OUT} | | -0.5 to V _{CC} + 0.5 | |
| Input diode current | I _{IK} | | ±20 | mA |
| Output diode current | I _{ОК} | | ±20 | |
| Output current | I _{OUT} | | ±25 | |
| V _{CC} /ground current | I _{CC} | | ±50 | |
| Power dissipation | PD | (Note 1) | 500 | mW |
| Storage temperature | T _{stg} | | -65 to 150 | °C |

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: P_D derates linearly with -8 mW/°C above 85 °C

11. Operating Ranges (Note)

| Characteristics | Symbol | Test Condition | Rating | Unit |
|---------------------------|--------------------------------|----------------|----------------------|------|
| Supply voltage | V _{CC} | — | 2.0 to 6.0 | V |
| Input voltage | V _{IN} | — | 0 to V _{CC} | V |
| Output voltage | V _{OUT} | — | 0 to V _{CC} | V |
| Operating temperature | T _{opr} | — | -40 to 125 | °C |
| Input rise and fall times | t _r ,t _f | — | 0 to 50 | μS |

Note: The operating ranges are required to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

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12. Electrical Characteristics

12.1. DC Characteristics (Unless otherwise specified, $T_a = 25$ °C)

| Characteristics | Symbol | Test Condition | | V _{CC} (V) | Min | Тур. | Max | Unit |
|---------------------------|-----------------|--------------------------------------|---------------------------|---------------------|------|------|------|------|
| High-level input voltage | VIH | _ | | 2.0 | 1.50 | | _ | V |
| | | | | 4.5 | 3.15 | _ | _ |] |
| | | | | 6.0 | 4.20 | _ | _ | |
| Low-level input voltage | V _{IL} | — | | 2.0 | _ | _ | 0.50 | V |
| | | | | 4.5 | _ | — | 1.35 | |
| | | | | 6.0 | _ | — | 1.80 | |
| High-level output voltage | V _{OH} | $V_{IN} = V_{IH} \text{ or } V_{IL}$ | I _{OH} = -20 μA | 2.0 | 1.9 | 2.0 | _ | V |
| | | | | 4.5 | 4.4 | 4.5 | _ | |
| | | | | 6.0 | 5.9 | 6.0 | _ | |
| | | | I _{OH} = -4 mA | 4.5 | 4.18 | 4.31 | _ |] |
| | | | I _{OH} = -5.2 mA | 6.0 | 5.68 | 5.80 | _ | |
| Low-level output voltage | V _{OL} | $V_{IN} = V_{IH} \text{ or } V_{IL}$ | I _{OL} = 20 μA | 2.0 | | 0.0 | 0.1 | > |
| | | | | 4.5 | | 0.0 | 0.1 | |
| | | | | 6.0 | | 0.0 | 0.1 | |
| | | | I _{OL} = 4 mA | 4.5 | _ | 0.17 | 0.26 | |
| | | | I _{OL} = 5.2 mA | 6.0 | _ | 0.18 | 0.26 | |
| Input leakage current | I _{IN} | $V_{IN} = V_{CC}$ or GND | | 6.0 | | _ | ±0.1 | μA |
| Quiescent supply current | I _{CC} | $V_{IN} = V_{CC}$ or GND | | 6.0 | | _ | 4.0 | μA |

12.2. DC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C)

| Characteristics | Symbol | Test Condition | | V _{CC} (V) | Min | Max | Unit |
|---------------------------|-----------------|--------------------------------------|---------------------------|---------------------|------|------|------|
| High-level input voltage | V _{IH} | _ | | 2.0 | 1.50 | _ | V |
| | | | | 4.5 | 3.15 | _ | 1 |
| | | | | 6.0 | 4.20 | _ |] |
| Low-level input voltage | V _{IL} | — | | 2.0 | _ | 0.50 | V |
| | | | | 4.5 | _ | 1.35 |] |
| | | | | 6.0 | _ | 1.80 | |
| High-level output voltage | V _{OH} | $V_{IN} = V_{IH} \text{ or } V_{IL}$ | I _{OH} = -20 μA | 2.0 | 1.9 | — | V |
| | | | | 4.5 | 4.4 | — | |
| | | | | 6.0 | 5.9 | _ | |
| | | | I _{OH} = -4 mA | 4.5 | 4.13 | — | |
| | | | I _{OH} = -5.2 mA | 6.0 | 5.63 | — | |
| Low-level output voltage | V _{OL} | $V_{IN} = V_{IH} \text{ or } V_{IL}$ | I _{OL} = 20 μA | 2.0 | | 0.1 | V |
| | | | | 4.5 | _ | 0.1 | |
| | | | | 6.0 | | 0.1 | |
| | | | I _{OL} = 4 mA | 4.5 | _ | 0.33 | |
| | | | I _{OL} = 5.2 mA | 6.0 | _ | 0.33 | |
| Input leakage current | I _{IN} | $V_{IN} = V_{CC}$ or GND | | 6.0 | _ | ±1.0 | μA |
| Quiescent supply current | I _{CC} | $V_{IN} = V_{CC}$ or GND | | 6.0 | _ | 40.0 | μA |

12.3. DC Characteristics (Unless otherwise specified, $T_a = -40$ to 125 °C)

| Characteristics | Symbol | Test Condition | ı | V _{CC} (V) | Min | Max | Unit |
|---------------------------|-----------------|--------------------------------------|---------------------------|---------------------|------|-------|------|
| High-level input voltage | VIH | — | | 2.0 | 1.50 | — | V |
| | | | | 4.5 | 3.15 | _ | |
| | | | | 6.0 | 4.20 | _ | |
| Low-level input voltage | VIL | — | | 2.0 | _ | 0.50 | V |
| | | | | 4.5 | | 1.35 | |
| | | | | 6.0 | _ | 1.80 | |
| High-level output voltage | V _{OH} | $V_{IN} = V_{IH} \text{ or } V_{IL}$ | I _{OH} = -20 μA | 2.0 | 1.9 | _ | V |
| | | | | 4.5 | 4.4 | _ | |
| | | | | 6.0 | 5.9 | _ | |
| | | | I _{OH} = -4 mA | 4.5 | 3.7 | _ | |
| | | | I _{OH} = -5.2 mA | 6.0 | 5.2 | — | |
| Low-level output voltage | V _{OL} | $V_{IN} = V_{IH} \text{ or } V_{IL}$ | I _{OL} = 20 μA | 2.0 | _ | 0.1 | V |
| | | | | 4.5 | _ | 0.1 | |
| | | | | 6.0 | | 0.1 | |
| | | | I _{OL} = 4 mA | 4.5 | _ | 0.4 | |
| | | | I _{OL} = 5.2 mA | 6.0 | _ | 0.4 |] |
| Input leakage current | I _{IN} | $V_{IN} = V_{CC}$ or GND | | 6.0 | _ | ±1.0 | μA |
| Quiescent supply current | I _{CC} | $V_{IN} = V_{CC}$ or GND | | 6.0 | | 160.0 | μA |

12.4. AC Characteristics (Unless otherwise specified, C_L = 15 pF, V_{CC} = 5 V, T_a = 25 °C, Input: t_r = t_f = 6 ns)

| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|---|------------------------------------|----------------|-----|------|-----|------|
| Output transition time | t _{TLH} ,t _{THL} | — | — | 4 | 8 | ns |
| Propagation delay time (A, B, C - Y) | t _{PLH} ,t _{PHL} | _ | — | 14 | 26 | |
| Propagation delay time (G1, G2 - Y) | t _{PLH} ,t _{PHL} | _ | — | 14 | 26 | |

12.5. AC Characteristics (Unless otherwise specified, C_L = 50pF, T_a = 25 °C, Input: t_r = t_f = 6 ns)

| Characteristics | Symbol | Note | V _{CC} (V) | Min | Тур. | Max | Unit |
|---|------------------------------------|----------|---------------------|-----|------|-----|------|
| Output transition time | t _{TLH} ,t _{THL} | | 2.0 | _ | 30 | 75 | ns |
| | | | 4.5 | _ | 8 | 15 | |
| | | | 6.0 | _ | 7 | 13 | |
| Propagation delay time (A, B, C - Y) | t _{PLH} ,t _{PHL} | | 2.0 | — | 50 | 150 | ns |
| | | | 4.5 | _ | 17 | 30 | |
| | | | 6.0 | _ | 15 | 26 | |
| Propagation delay time | t _{PLH} ,t _{PHL} | | 2.0 | _ | 50 | 150 | ns |
| (G1, G 2 - Y) | | | 4.5 | _ | 17 | 30 | |
| | | | 6.0 | _ | 15 | 26 | |
| Input capacitance | C _{IN} | | _ | _ | 3 | _ | pF |
| Power dissipation capacitance | C _{PD} | (Note 1) | _ | — | 22 | — | pF |

Note 1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.

 $I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{IN} + I_{CC}$

12.6. AC Characteristics

(Unless otherwise specified, $C_L = 50 \text{ pF}$, $T_a = -40 \text{ to } 85 \text{ °C}$, Input: $t_f = t_f = 6 \text{ ns}$)

| Characteristics | Symbol | V _{CC} (V) | Min | Max | Unit |
|------------------------|------------------------------------|---------------------|-----|-----|------|
| Output transition time | t _{TLH} ,t _{THL} | 2.0 | _ | 95 | ns |
| | | 4.5 | _ | 19 | |
| | | 6.0 | _ | 16 | |
| Propagation delay time | t _{PLH} ,t _{PHL} | 2.0 | — | 190 | ns |
| (A, B, C -Y) | | 4.5 | _ | 38 | |
| | | 6.0 | _ | 32 | |
| Propagation delay time | t _{PLH} ,t _{PHL} | 2.0 | — | 190 | ns |
| (G1, <u>G</u> 2 - Y) | | 4.5 | _ | 38 | |
| | | 6.0 | _ | 32 | |

12.7. AC Characteristics

(Unless otherwise specified, $C_L = 50 \text{ pF}$, $T_a = -40 \text{ to } 125 \text{ °C}$, Input: $t_r = t_f = 6 \text{ ns}$)

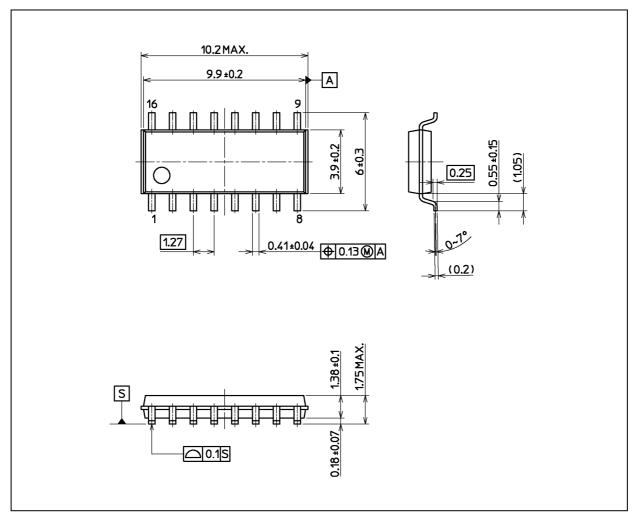
| Characteristics | Symbol | V _{CC} (V) | Min | Max | Unit |
|------------------------|------------------------------------|---------------------|-----|-----|------|
| Output transition time | t _{TLH} ,t _{THL} | 2.0 | — | 110 | ns |
| | | 4.5 | — | 22 | |
| | | 6.0 | — | 19 | |
| Propagation delay time | t _{PLH} ,t _{PHL} | 2.0 | — | 225 | ns |
| (A, B, C - Y) | | 4.5 | — | 45 | |
| | | 6.0 | _ | 38 | |
| Propagation delay time | t _{PLH} ,t _{PHL} | 2.0 | _ | 225 | ns |
| (G1, <u>G</u> 2 - Y) | | 4.5 | — | 45 | |
| | | 6.0 | — | 38 | |



Package Dimensions

74HC238D

Unit: mm



Weight: 0.15 g (typ.)

Package Name(s) Nickname: SOIC16

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