CMOS Digital Integrated Circuits Silicon Monolithic

74HC151D

1. Functional Description

· 8-Channel Multiplexer

2. General

The 74HC151D is a high speed CMOS 8-CHANNEL MULTIPLEXER fabricated with silicon gate C2MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

One of eight date input signals (D0-D7) is selected by decoding of the three-bit address input (A, B, C). The selected data appears on two outputs: non-inverting (Y) and inverting (W).

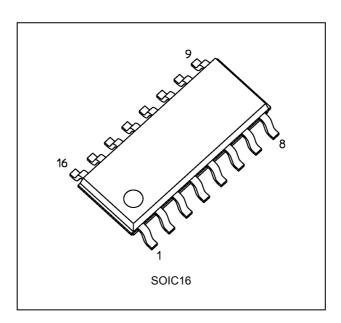
The strobe input provides two output conditions; a low level on the strobe input transfers the selected data to the outputs. A high level on the strobe input sets the Y output low and the W output high without regard to the data or select input conditions.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

3. Features

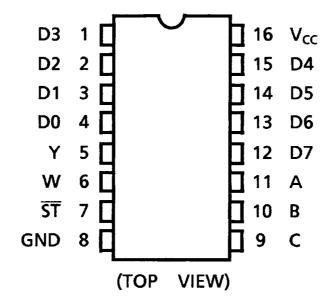
- (1) High speed: $t_{pd} = 15$ ns (typ.) at $V_{CC} = 5$ V
- (2) Low power dissipation: $I_{CC} = 4.0 \,\mu\text{A}$ (max) at $T_a = 25 \,^{\circ}\text{C}$
- (3) Balanced propagation delays: $t_{PLH} \approx t_{PHL}$
- (4) Wide operating voltage range: $V_{CC(opr)} = 2.0$ to 6.0 V

4. Packaging

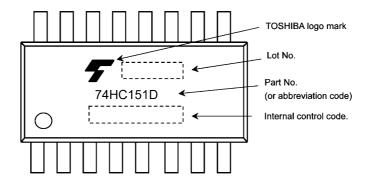




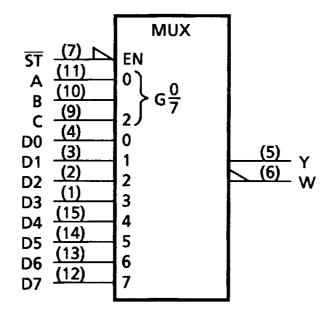
5. Pin Assignment



6. Marking



7. IEC Logic Symbol



Rev.3.0

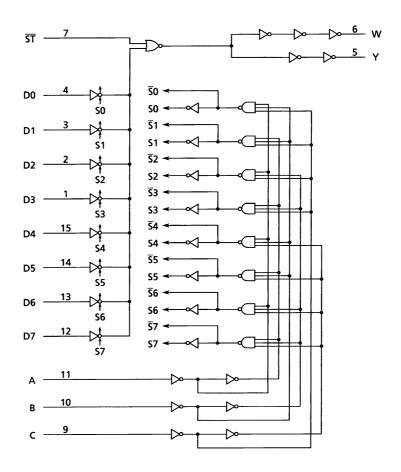


8. Truth Table

	1	Out	puts		
	Select		Strobe		10/
С	В	Α	ST	Y	W
Х	Х	Х	Н	L	Н
L	L	L	L	D0	D0
L	L	Н	L	D1	D1
L	Н	L	L	D2	D ₂
L	Н	Ι	L	D3	D3
Н	L	L	L	D4	D̄4
Н	L	Н	L	D5	D ₅
Н	Н	L	L	D6	D̄6
Н	Н	Η	L	D7	D7

X: Don't care

9. System Diagram





10. Absolute Maximum Ratings (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CC}		-0.5 to 7.0	V
Input voltage	V _{IN}		-0.5 to V _{CC} + 0.5	V
Output voltage	V _{OUT}		-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}		±20	mA
Output diode current	I _{OK}		±20	mA
Output current	l _{out}		±25	mA
V _{CC} /ground current	I _{CC}		±50	mA
Power dissipation	P _D	(Note 1)	500	mW
Storage temperature	T _{stg}		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: P_D derates linearly with -8 mW/°C above 85 °C

11. Operating Ranges (Note)

Characteristics	Symbol	Test Condition	Rating	Unit
Supply voltage	V _{CC}	_	2.0 to 6.0	V
Input voltage	V _{IN}	_	0 to V _{CC}	V
Output voltage	V _{OUT}	_	0 to V _{CC}	V
Operating temperature	T _{opr}	_	-40 to 125	°C
Input rise and fall times	t _r ,t _f	_	0 to 50	μS

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.



12. Electrical Characteristics

12.1. DC Characteristics (Unless otherwise specified, $T_a = 25$ °C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Тур.	Max	Unit
High-level input voltage	V _{IH}	_		2.0	1.50	_	_	V
				4.5	3.15	_	_	
				6.0	4.20	_	_	
Low-level input voltage	V _{IL}	_		2.0		_	0.50	V
				4.5		_	1.35	
				6.0		_	1.80	
High-level output voltage	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -20 μA	2.0	1.9	2.0	_	V
				4.5	4.4	4.5	_	
				6.0	5.9	6.0	_	
			I _{OH} = -4 mA	4.5	4.18	4.31	_	
			$I_{OH} = -5.2 \text{ mA}$	6.0	5.68	5.80	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 20 μA	2.0	_	0.0	0.1	V
				4.5	_	0.0	0.1	
				6.0	_	0.0	0.1	
			I _{OL} = 4 mA	4.5	_	0.17	0.26	
			I _{OL} = 5.2 mA	6.0	_	0.18	0.26	
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		6.0			±0.1	μА
Quiescent supply current	I _{CC}	$V_{IN} = V_{CC}$ or GND		6.0		_	4.0	μА

12.2. DC Characteristics (Unless otherwise specified, T_a = -40 to 85 °C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	_		2.0	1.50	_	V
				4.5	3.15	_	
				6.0	4.20	_	
Low-level input voltage	V _{IL}	_		2.0	_	0.50	V
				4.5	_	1.35	
				6.0	_	1.80	
High-level output voltage	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -20 μA	2.0	1.9	_	V
				4.5	4.4	_]
				6.0	5.9	_	
			I _{OH} = -4 mA	4.5	4.13	_	
			I _{OH} = -5.2 mA	6.0	5.63	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 20 μA	2.0	_	0.1	V
				4.5	_	0.1	
				6.0	_	0.1	
			I _{OL} = 4 mA	4.5	_	0.33	
			I _{OL} = 5.2 mA	6.0	_	0.33]
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		6.0	_	±1.0	μА
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		6.0	_	40.0	μА



12.3. DC Characteristics (Unless otherwise specified, T_a = -40 to 125 °C)

Characteristics	Symbol	Test Condition	1	V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	_		2.0	1.50	_	V
				4.5	3.15	_	
				6.0	4.20	_	1
Low-level input voltage	V _{IL}	_		2.0	_	0.50	V
				4.5	_	1.35	
				6.0	_	1.80	1
High-level output voltage	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -20 μA	2.0	1.9	_	V
				4.5	4.4	_	
				6.0	5.9	_	1
			I _{OH} = -4 mA	4.5	3.7	_	
			I _{OH} = -5.2 mA	6.0	5.2	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 20 μA	2.0	_	0.1	V
				4.5	_	0.1	
				6.0	_	0.1	
			I _{OL} = 4 mA	4.5	_	0.4	1
			I _{OL} = 5.2 mA	6.0	_	0.4	1 l
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND	•	6.0	_	±1.0	μА
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		6.0	_	160.0	μА

12.4. AC Characteristics (Unless otherwise specified, C_L = 15 pF, V_{CC} = 5 V, T_a = 25 °C, Input: t_r = t_f = 6 ns)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	t _{TLH} ,t _{THL}	_	_	4	8	ns
Propagation delay time (D-Y)	t _{PLH} ,t _{PHL}	_	_	15	24	ns
Propagation delay time (D-W)	t _{PLH} ,t _{PHL}	_	_	15	24	ns
Propagation delay time (ST-Y)	t _{PLH} ,t _{PHL}	_	_	10	17	ns
Propagation delay time (ST-W)	t _{PLH} ,t _{PHL}	_	_	10	17	ns
Propagation delay time (A, B, C-Y)	t _{PLH} ,t _{PHL}	_	_	19	31	ns
Propagation delay time (A, B, C-W)	t _{PLH} ,t _{PHL}	_	_	19	31	ns



12.5. AC Characteristics (Unless otherwise specified, $C_L = 50$ pF, $T_a = 25$ °C, Input: $t_r = t_f = 6$ ns)

Characteristics	Symbol	Note	V _{CC} (V)	Min	Тур.	Max	Unit
Output transition time	t _{TLH} ,t _{THL}		2.0	_	30	75	ns
			4.5	_	8	15	
			6.0	_	7	13	
Propagation delay time	t_{PLH}, t_{PHL}		2.0		65	140	ns
(D-Y)			4.5		18	28	
			6.0		15	24	
Propagation delay time	t_{PLH}, t_{PHL}		2.0		65	140	ns
(D-W)			4.5		18	28	1
			6.0	_	15	24	
Propagation delay time	t_{PLH}, t_{PHL}		2.0		36	100	ns
(ST -Y)			4.5		12	20]
			6.0	_	10	17	
Propagation delay time	t_{PLH}, t_{PHL}		2.0		36	100	ns
(ST -W)			4.5		12	20	
			6.0	_	10	17	
Propagation delay time	t_{PLH}, t_{PHL}		2.0	_	80	180	ns
(A, B, C-Y)			4.5	_	23	36	
			6.0	_	19	31	
Propagation delay time	t_{PLH}, t_{PHL}		2.0	_	80	180	ns
(A, B, C-W)			4.5	_	23	36	
			6.0		19	31	
Input capacitance	C _{IN}		_	_	3	_	pF
Power dissipation capacitance	C _{PD}	(Note 1)			15	_	pF

Note 1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.

 $I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{IN} + I_{CC}$



12.6. AC Characteristics (Unless otherwise specified, $C_L = 50$ pF, $T_a = -40$ to 85 °C, Input: $t_r = t_f = 6$ ns)

Characteristics	Symbol	V _{CC} (V)	Min	Max	Unit
Output transition time	t _{TLH} ,t _{THL}	2.0	_	95	ns
		4.5	_	19	
		6.0	_	16]
Propagation delay time	t _{PLH} ,t _{PHL}	2.0	_	175	ns
(D-Y)		4.5	_	35	
		6.0	_	30]
Propagation delay time	t _{PLH} ,t _{PHL}	2.0	_	175	ns
(D-W)		4.5		35	
		6.0		30	1
Propagation delay time	t _{PLH} ,t _{PHL}	2.0		125	ns
(ST -Y)		4.5		25	
		6.0		21	1
Propagation delay time	t _{PLH} ,t _{PHL}	2.0		125	ns
(ST-W)		4.5		25	
		6.0		21	1
Propagation delay time	t _{PLH} ,t _{PHL}	2.0		225	ns
(A, B, C-Y)		4.5		45	1 l
		6.0		38	1
Propagation delay time	t _{PLH} ,t _{PHL}	2.0		225	ns
(A, B, C-W)		4.5	_	45	1
		6.0		38	1

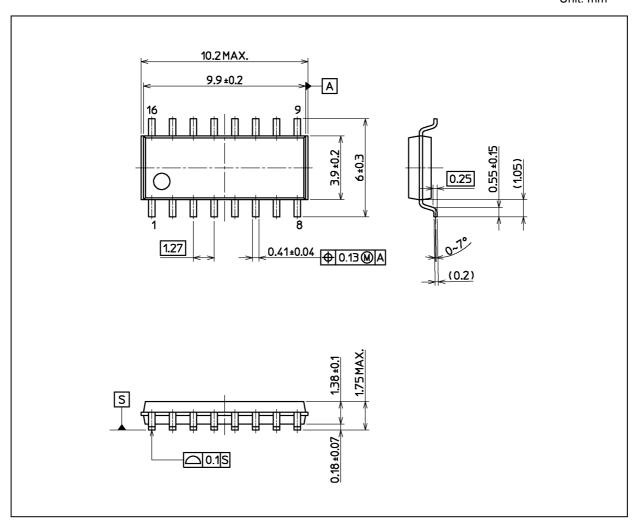
12.7. AC Characteristics (Unless otherwise specified, C_L = 50 pF, T_a = -40 to 125 °C, Input: t_r = t_f = 6 ns)

Characteristics	Symbol	V _{CC} (V)	Min	Max	Unit
Output transition time	t _{TLH} ,t _{THL}	2.0	_	110	ns
		4.5	_	22	
		6.0	_	19	
Propagation delay time	t _{PLH} ,t _{PHL}	2.0	-	210	ns
(D-Y)		4.5	_	42	
		6.0	_	36	
Propagation delay time	t _{PLH} ,t _{PHL}	2.0	_	210	ns
(D-W)		4.5	_	42	
		6.0	_	36]
Propagation delay time	t _{PLH} ,t _{PHL}	2.0	_	150	ns
(ST-Y)		4.5	_	30]
		6.0	_	26]
Propagation delay time	t _{PLH} ,t _{PHL}	2.0	_	150	ns
(ST -W)		4.5	_	30	
		6.5	_	26]
Propagation delay time	t _{PLH} ,t _{PHL}	2.0	_	270	ns
(A, B, C-Y)		4.5	_	54	
		6.5	_	46]
Propagation delay time	t _{PLH} ,t _{PHL}	2.0	_	270	ns
(A, B, C-W)		4.5	_	54]
		6.0	_	46]



Package Dimensions

Unit: mm



Weight: 0.15 g (typ.)

	Package Name(s)
Nickname: SOIC16	



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