CMOS Digital Integrated Circuits Silicon Monolithic

74HC02D

1. Functional Description

• Quad 2-Input NOR Gate

2. General

The 74HC02D is a high speed CMOS 2-INPUT NOR GATE fabricated with silicon gate C²MOS technology. It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

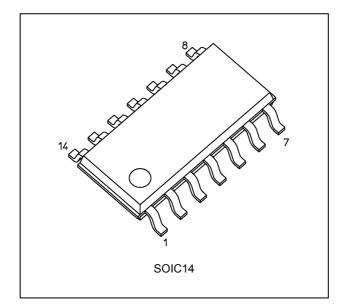
The internal circuit is composed of 3 stages, including a buffer output, which provide high noise immunity and stable output.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

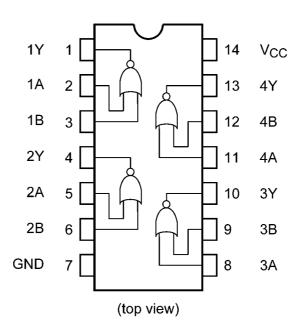
3. Features

- (1) Wide operating temperature range: T_{opr} = -40 to 125 °C (Note 1)
- (2) High speed: t_{pd} = 6 ns (typ.) at V_{CC} = 5 V
- (3) Low power dissipation: I_{CC} = 1.0 μA (max) at T_a = 25 °C
- (4) Balanced propagation delays: $t_{PLH} \approx t_{PHL}$
- (5) Wide operating voltage range: $V_{CC(opr)} = 2.0$ to 6.0 V
- Note 1: Operating Range spec of $T_{opr} = -40$ °C to 125 °C is applicable only for the products which manufactured after July 2020.

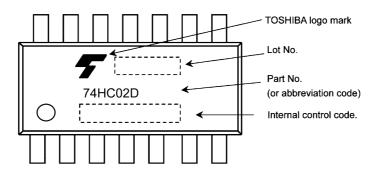
4. Packaging



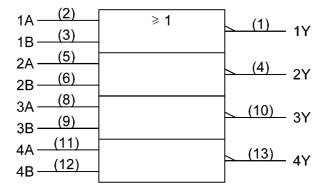
5. Pin Assignment



6. Marking



7. IEC Logic Symbol



8. Truth Table

А	В	Y
L	L	Н
L	Н	L
Н	L	L
Н	Н	L

9. Absolute Maximum Ratings (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CC}		-0.5 to 7.0	V
Input voltage	V _{IN}		-0.5 to V _{CC} + 0.5	V
Output voltage	V _{OUT}		-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}		±20	mA
Output diode current	I _{ОК}		±20	mA
Output current	I _{OUT}		±25	mA
V _{CC} /ground current	I _{CC}		±50	mA
Power dissipation	PD	(Note 1)	500	mW
Storage temperature	T _{stg}		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: P_D derates linearly with -8 mW/°C above 85 $^\circ C$

10. Operating Ranges (Note)

Characteristics	Symbol	Test Condition	Note	Rating	Unit
Supply voltage	V _{CC}	—		2.0 to 6.0	V
Input voltage	V _{IN}	—		0 to V _{CC}	V
Output voltage	V _{OUT}	—		0 to V _{CC}	V
Operating temperature	T _{opr}	—	(Note 1)	-40 to 125	ů
Input rise and fall times	t _r ,t _f	V _{CC} = 2.0 V		0 to 1000	ns
		V _{CC} = 4.5 V		0 to 500	
		V _{CC} = 6.0 V		0 to 400	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs and bus inputs must be tied to either V_{CC} or GND.

Note 1: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after July 2020.

11. Electrical Characteristics

11.1. DC Characteristics (Unless otherwise specified, $T_a = 25$ °C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Тур.	Мах	Unit
High-level input voltage	VIH	—		2.0	1.50	_	_	V
				4.5	3.15	_	_	
				6.0	4.20	_	—	
Low-level input voltage	V _{IL}	—		2.0	—		0.50	V
				4.5	—	_	1.35	
				6.0	_	_	1.80	
High-level output voltage	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -20 μA	2.0	1.9	2.0	_	V
				4.5	4.4	4.5	_	
				6.0	5.9	6.0	_	
			I _{OH} = -4 mA	4.5	4.18	4.31	—	
			I _{OH} = -5.2 mA	6.0	5.68	5.80	—	
Low-level output voltage	V _{OL}	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I _{OL} = 20 μA	2.0	_	0.0	0.1	V
				4.5	—	0.0	0.1	
				6.0	—	0.0	0.1	
			I _{OL} = 4 mA	4.5	_	0.17	0.26	
			I _{OL} = 5.2 mA	6.0	—	0.18	0.26	
Input leakage current	I _{IN}	$V_{IN} = V_{CC}$ or GND		6.0	_		±0.1	μA
Quiescent supply current	I _{CC}	$V_{IN} = V_{CC}$ or GND		6.0	_	_	1.0	μA

11.2. DC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	—		2.0	1.50	_	V
				4.5	3.15	_	1
				6.0	4.20	_	
Low-level input voltage	VIL	—		2.0	_	0.50	V
				4.5	_	1.35	
				6.0		1.80	
High-level output voltage	V _{OH}	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I _{OH} = -20 μA	2.0	1.9	_	V
				4.5	4.4	_	
				6.0	5.9	—	
			I _{OH} = -4 mA	4.5	4.13	_	
			I _{OH} = -5.2 mA	6.0	5.63	—	
Low-level output voltage	V _{OL}	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I _{OL} = 20 μA	2.0		0.1	V
				4.5	_	0.1	
				6.0	_	0.1	
			I _{OL} = 4 mA	4.5	_	0.33	
			I _{OL} = 5.2 mA	6.0	_	0.33	
Input leakage current	l _{iN}	$V_{IN} = V_{CC}$ or GND		6.0	_	±1.0	μA
Quiescent supply current	I _{CC}	$V_{IN} = V_{CC}$ or GND		6.0	_	10.0	μA

11.3. DC Characteristics (Note) (Unless otherwise specified, T_a = -40 to 125 °C)

Characteristics	Symbol	Test Condition	1	V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	—		2.0	1.50	_	V
				4.5	3.15	_	
				6.0	4.20	_	
Low-level input voltage	V _{IL}	_		2.0	_	0.50	V
				4.5	_	1.35	
				6.0	_	1.80	
High-level output voltage	V _{OH}	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I _{OH} = -20 μA	2.0	1.9	_	V
				4.5	4.4	_	
				6.0	5.9	_	
			I _{OH} = -4 mA	4.5	3.7	_	
			I _{OH} = -5.2 mA	6.0	5.2	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 20 μA	2.0	_	0.1	V
				4.5	_	0.1	
				6.0	_	0.1	
			I _{OL} = 4 mA	4.5	_	0.4	
			I _{OL} = 5.2 mA	6.0	_	0.4]
Input leakage current	I _{IN}	$V_{IN} = V_{CC}$ or GND		6.0	_	±1.0	μA
Quiescent supply current	I _{CC}	$V_{IN} = V_{CC}$ or GND		6.0		20.0	μA

Note: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after July 2020.

11.4. AC Characteristics

(Unless otherwise specified, CL = 15 pF, V_{CC} = 5 V, T_a = 25 °C, Input: t_r = t_f = 6 ns)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	t _{TLH} ,t _{THL}	—	_	4	8	ns
Propagation delay time	t _{PLH} ,t _{PHL}	—		6	12	ns

11.5. AC Characteristics

(Unless otherwise specified, $C_L = 50$ pF, $T_a = 25$ °C, Input: $t_r = t_f = 6$ ns)

Characteristics	Symbol	Note	V _{CC} (V)	Min	Тур.	Max	Unit
Output transition time	t _{TLH} ,t _{THL}		2.0	—	25	75	ns
			4.5	_	7	15	
			6.0	_	6	13	
Propagation delay time	t _{PLH} ,t _{PHL}		2.0	—	27	75	ns
			4.5	_	9	15	
			6.0	_	8	13	
Input capacitance	C _{IN}		_	_	5	_	pF
Power dissipation capacitance	C _{PD}	(Note 1)	_	_	21	_	pF

Note 1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.

 $I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{IN} + I_{CC}/4$ (per gate)

11.6. AC Characteristics (Unless otherwise specified, C_L = 50 pF, T_a = -40 to 85 °C, Input: t_r = t_f = 6 ns)

Characteristics	Symbol	V _{CC} (V)	Min	Max	Unit
Output transition time	t _{TLH} ,t _{THL}	2.0	_	95	ns
		4.5		19	
		6.0	_	16	
Propagation delay time	t _{PLH} ,t _{PHL}	2.0	—	95	ns
		4.5	_	19	
		6.0	_	16	

11.7. AC Characteristics (Note) (Unless otherwise specified, C_L = 50 pF, T_a = -40 to 125 °C, Input: t_r = t_f = 6 ns)

Characteristics	Symbol	V _{CC} (V)	Min	Max	Unit
Output transition time	t _{TLH} ,t _{THL}	2.0	_	110	ns
		4.5	_	22	
		6.0	_	18	
Propagation delay time	t _{PLH} ,t _{PHL}	2.0	_	110	ns
		4.5	_	22	
		6.0	_	18	

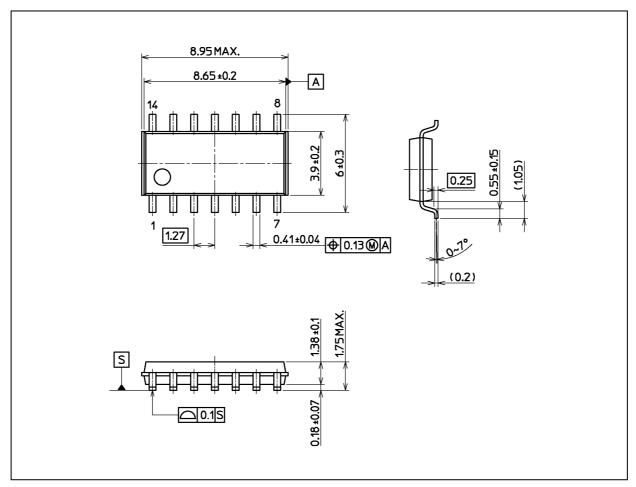
Note: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after July 2020.



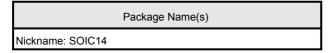
74HC02D

Package Dimensions

Unit: mm



Weight: 0.13 g (typ.)



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