TOSHIBA Photocoupler GaAs Ired & Photo-Triac

TLP161G

Triac Drive
Programmable Controllers
AC-Output Module
Solid State Relay

The TOSHIBA mini flat coupler TLP161G is a small outline coupler, suitable for surface mount assembly.

The TLP161G consists of a photo triac, optically coupled to a gallium arsenide infrared emitting diode.

• Zero-voltage crossing turn-on

• Peak off-state voltage: 400V(min.)

• Trigger LED current: 10mA(max.)

• On-state current: 70mA(max.)

• Isolation voltage: 2500Vrms(min.)

• UL recognized: UL1577, file no. E67349

Unit in mm 3 1 4 6 3.6 ± 0.2 4 7 7.0 ± 0.4 11-4C3

Weight: 0.09 g (typ.)

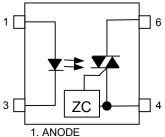
Trigger LED Current

Classi– fication*		Current (mA) Ta=25°C	Marking Of Classification
	Min.	Max.	Classification
(IFT5)	_	5	T5
(IFT7)	_	7	T5, T7
Standard	_	10	T5, T7, blank

*Ex. (IFT5); TLP161G(IFT5)

(Note) Application type name for certification test, please use standard product type name, i.e. TLP161G(IFT5): TLP161G

Pin Configurations



- 3. CATHODE
- 4. TERMINAL 1
- 6. TERMINAL 2

TLP161G



Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit	
	Forward current	lF	50	mA		
	Forward current derating (Ta	ΔI _F / °C	-0.7	mA / °C		
LED	Peak forward current (100µs p	Peak forward current (100µs pulse, 100pps)			Α	
	Reverse voltage		V _R	5	V	
	Junction temperature	Tj	125	°C		
	Off-state output terminal volta	V_{DRM}	400	V		
	On-state RMS current	Ta=25°C	IT(DMO)	70	mA	
_		Ta=70°C	I _{T(RMS)}	40		
Detector	On-state current derating (Ta	ΔI _T / °C	-0.67	mA / °C		
Det	Peak on-state current (100µs	I _{TP}	2	Α		
	Peak nonrepetitive surge curre (PW=10ms, DC=10%)	I _{TSM}	1.2	А		
	Junction temperature	Tj	115	°C		
Storage temperature range			T _{stg}	-55~125	°C	
Operating temperature range			T _{opr}	-40~100	°C	
Lead soldering temperature (10s)			T _{sol}	260	°C	
Isolation voltage (AC, 1min., R.H.≤ 60%) (Note)			BVS	2500	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note) Device considered a two terminal device: Pins 1 and 3 shorted together and pins 4 and 6 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	V_{AC}	_	_	120	Vac
Forward current	lF	15	20	25	mA
Peak on-state current	I _{TP}	_	_	1	Α
Operating temperature	T _{opr}	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

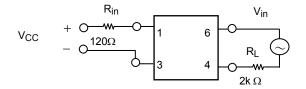
Individual Electrical Characteristics (Ta = 25°C)

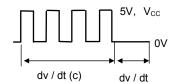
Characteristic		Symbol	Test Condition		Min.	Тур.	Max.	Unit
LED	Forward voltage	V _F	I _F =10mA		1.0	1.15	1.3	V
	Reverse current	I _R	V _R =5V		_	_	10	μA
	Capacitance	C _T	V=0, f=1MHz		-	30	_	pF
Detector	Peak off-state current	I _{DRM}	V _{DRM} =400V		_	10	1000	nA
	Peak on-state voltage	V _{TM}	I _{TM} =70 mA		_	1.7	2.8	V
	Holding current	lΗ	_			0.6	1	mA
	Critical rate of rise of off–state voltage	dv / dt	V _{in} =120Vrms, Ta=85°C	(Fig.1)	200	500	1	V / µs
	Critical rate of rise of commutating voltage	dv / dt(c)	V _{in} =30Vrms, I _T =15mA	(Fig.1)	_	0.2	_	V / µs

Coupled Electrical Characteristics (Ta = 25°C)

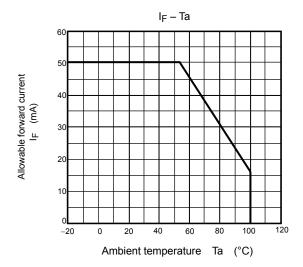
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Trigger LED current	I _{FT}	V _T =3V	_	5	10	mA
Inhibit voltage	V _{IH}	I _F =rated I _F T	_	_	40	V
Leakage in inhibited state	lін	I _F =rated I _{FT} V _T =rated V _{DRM}	_	100	300	μΑ
Capacitance (input to output)	CS	V _S =0, f=1MHz	_	0.8	_	pF
Isolation resistance	R _S	V _S =500V, R.H.≤ 60%	1×10 ¹²	10 ¹⁴	_	Ω
		AC, 1 minute	2500	_	_	Vrms
Isolation voltage	BVS	AC, 1 second, in oil	_	5000	_	
		DC, 1 minute, in oil	_	5000	_	Vdc

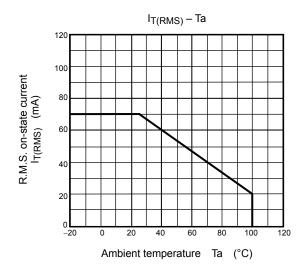
Fig.1 dv / dt test circuit

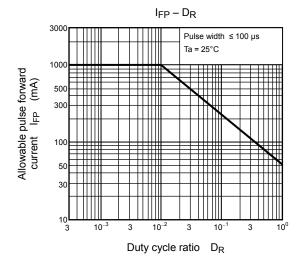


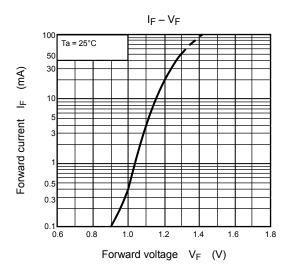


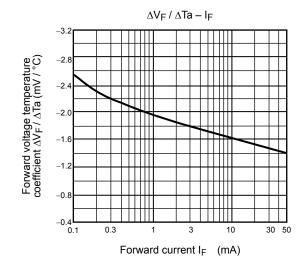
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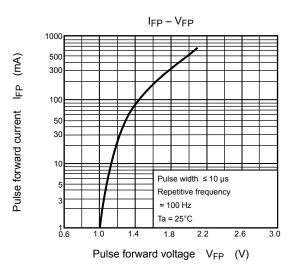




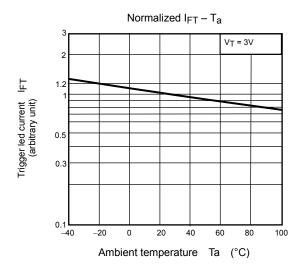


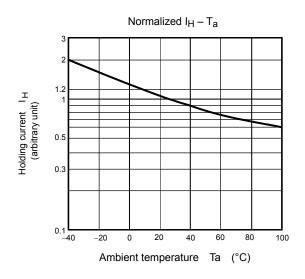


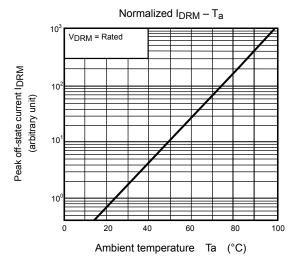


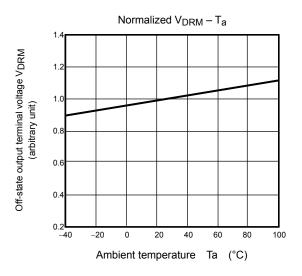


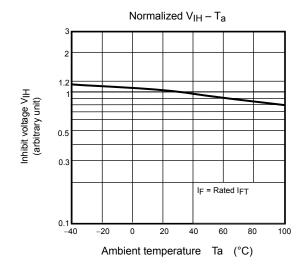
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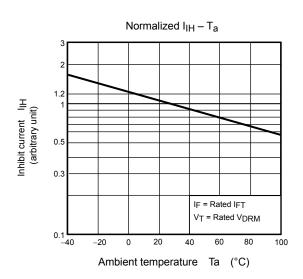












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