TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSVI)

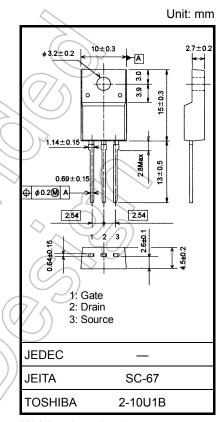
# 2SK3569

#### Switching Regulator Applications

- Low drain-source ON-resistance: R<sub>DS (ON)</sub> = 0.54 Ω (typ.)
- High forward transfer admittance: |Y<sub>fs</sub>| = 8.5 S (typ.)
- Low leakage current: I<sub>DSS</sub> = 100 μA (max) (V<sub>DS</sub> = 600 V)
- Enhancement mode:  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V <sub>DSS</sub>	600	V
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		V <sub>DGR</sub>	600	V
Gate-source voltage		V <sub>GSS</sub>	±30	> v
Drain current	DC (Note 1)	Ι <sub>D</sub>	10	
	Pulse (t = 1 ms) (Note 1)	I <sub>DP</sub>	40	A
Drain power dissipation (Tc = 25°C)		PD	45	$\langle \langle w \rangle \rangle$
Single pulse avalanche energy (Note 2)		EAS	363	mJ
Avalanche current		TAR	10	A
Repetitive avalanche energy (Note 3)		EAR	4.5	Lw
Channel temperature		7 Ich	150	°C
Storage temperature range		T <sub>stg</sub>	-55 to 150	∽°C



Weight : 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

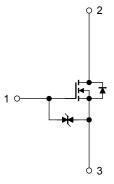
Characteristics	Symbol	Мах	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.78	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 90 \text{ V}, \text{ T}_{ch} = 25^{\circ}\text{C}$  (initial), L = 6.36 mH, I<sub>AR</sub> = 10 A, R<sub>G</sub> = 25  $\Omega$ 

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.



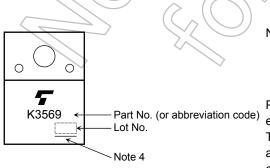
**Electrical Characteristics (Ta = 25°C)** 

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rent	I <sub>GSS</sub>	$V_{GS}=\pm 25~V,~V_{DS}=0~V$			±10	μA
Gate-source brea	akdown voltage	V (BR) GSS	$I_G=\pm 10~\mu A,~V_{DS}=0~V$	±30			V
Drain cut-off curr	ent	I <sub>DSS</sub>	$V_{DS} = 600 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$		—	100	μA
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	600		_	V
Gate threshold v	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.0	)}_	4.0	V
Drain-source ON	-resistance	R <sub>DS (ON)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}$		0.54	0.75	Ω
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = 10 V, I_D = 5 A$	2.4	8.5		S
Input capacitance	9	C <sub>iss</sub>			1500		
Reverse transfer	capacitance	C <sub>rss</sub>	$V_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$	7 —	15		pF
Output capacitance		C <sub>oss</sub>			180		
Switching time	Rise time	tr	$V_{GS}$ $U_D = 5 $ $V_{OUT}$	- (	22	>	
	Turn-on time	t <sub>on</sub>	$\begin{bmatrix} 0 & 0 \\ 0 & 0 \\ 50 & 0 \\ 0 & 0 \end{bmatrix} \neq \begin{bmatrix} R_L = \\ 40 & 0 \end{bmatrix}$		50	) —	20
	Fall time	t <sub>f</sub>			36		ns
	Turn-off time	t <sub>off</sub>	Duty $\leq$ 1%, t <sub>w</sub> = 10 $\mu$ s		180		
Total gate charge	9	Qg		) —	42		
Gate-source cha	rge	Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ A}$		23		nC
Gate-drain charge Q <sub>gd</sub>		Qgd			19		

### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)			_	_	10	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	<u> </u>	_	_	40	А
Forward voltage (diode)	VDSF	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V,	_	1300	_	ns
Reverse recovery charge	Qrr	dI <sub>DR</sub> /dt = 100 A/μs	_	16	_	μC

#### Marking

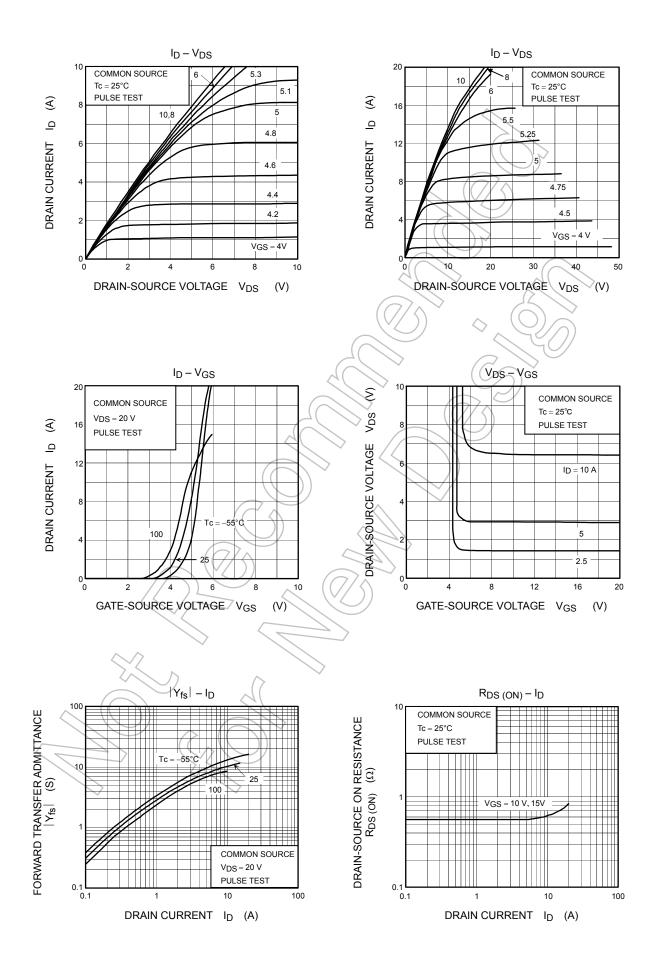


Note 4: A line under a Lot No. identifies the indication of product Labels.

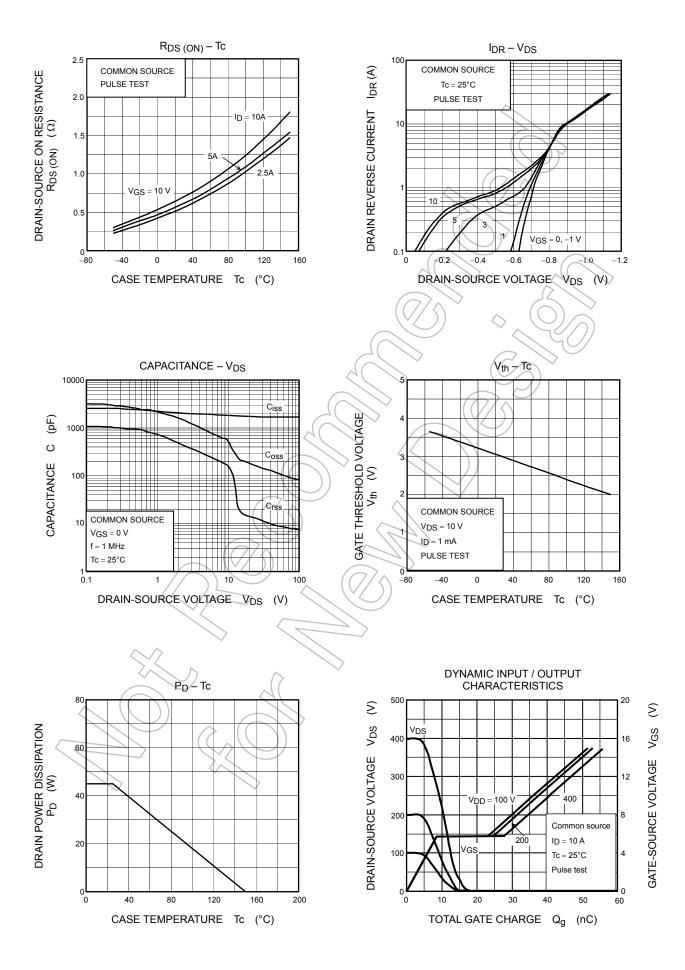
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

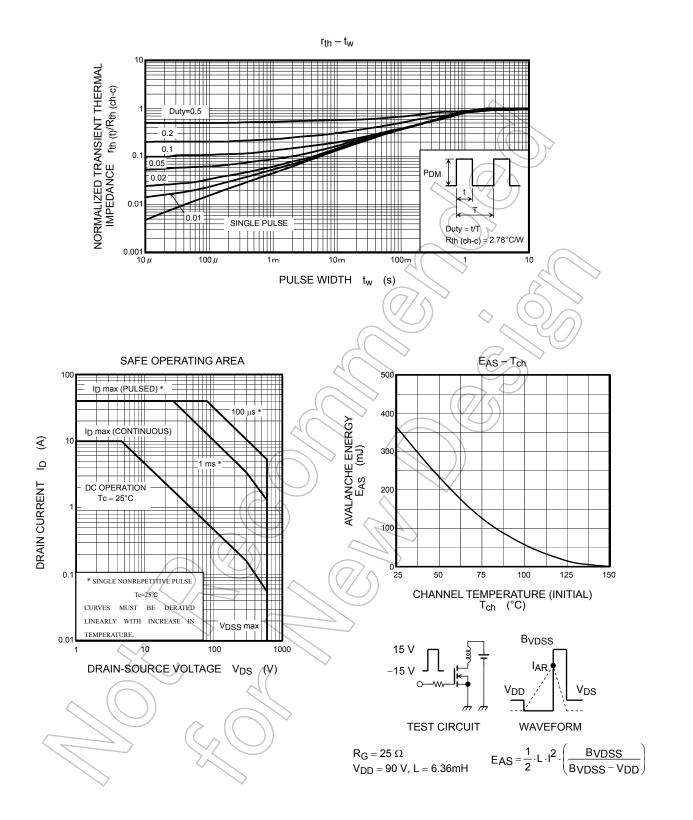
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