TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSVI)

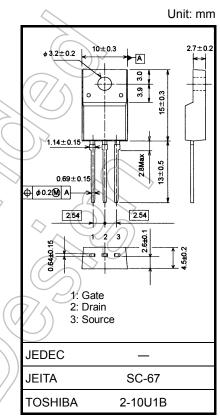
2SK3567

Switching Regulator Applications

- Low drain-source ON resistance: R_{DS (ON)} = 1.7 Ω (typ.)
- High forward transfer admittance: |Y_{fs}| = 2.5 S (typ.)
- Low leakage current: I_{DSS} = 100 μ A (max) (V_{DS} = 600 V)
- Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	600	V
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	600	V
Gate-source voltage		V _{GSS}	±30	> v
Drain current	DC (Note 1)	I _D	3.5	
	Pulse (t = 1 ms) (Note 1)	I _{DP}	14	A
Drain power dissipation (Tc = 25°C)		PD	35	$\langle \langle w \rangle \rangle$
Single pulse avalanche energy (Note 2)		EAS	201	mJ
Avalanche current		TAR	3.5	A
Repetitive avalanche energy (Note 3)		EAR	3.5	Lw
Channel temperature		Tch	150	°C
Storage temperature range		T _{stg}	-55 to 150	∽°c



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

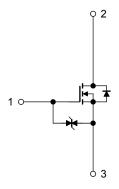
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	3.57	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 90 \text{ V}, \text{ T}_{ch} = 25^{\circ}\text{C}(\text{initial}), \text{ L} = 28.8 \text{ mH}, \text{ I}_{AR} = 3.5 \text{ A}, \text{ R}_{G} = 25 \Omega$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.



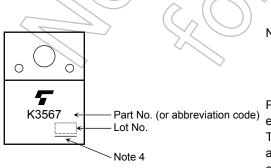
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rent	I _{GSS}	$V_{GS}=\pm 25~V,~V_{DS}=0~V$			±10	μA
Gate-source brea	akdown voltage	V (BR) GSS	$I_G=\pm 10~\mu A,~V_{DS}=0~V$	±30			V
Drain cut-off curr	ent	I _{DSS}	$V_{DS} = 600 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$		—	100	μA
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	600		_	V
Gate threshold v	oltage	V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.0)}_	4.0	V
Drain-source ON	resistance	R _{DS (ON)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 1.8 \text{ A}$		1.7	2.2	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 1.8 A	0.7	2.5		S
Input capacitance	e	C _{iss}			550		
Reverse transfer	capacitance	C _{rss}	$V_{DS} = 25 V$, $V_{GS} = 0 V$, f = 1 MHz	7 —	6		pF
Output capacitance		C _{oss}			60	1	
Switching time	Rise time	tr	V_{GS}	- (12	>	
	Turn-on time	t _{on}	$\begin{array}{c} 0 \\ 0 \\ 0 \\ 50 \\ \Omega \\ \end{array} $		45) —	20
	Fall time	t _f	$\begin{array}{c} \begin{array}{c} \begin{array}{c} \end{array} \\ \end{array} \\ \end{array} \\ \end{array} \\ \begin{array}{c} \end{array} \\ \end{array} \\ \end{array} \\ \end{array} \\ \begin{array}{c} \end{array} \\ \end{array} $		13		ns
	Turn-off time	t _{off}	Duty \leq 1%, t _w = 10 μ s		80		
Total gate charge	9	Qg) —	16	_	
Gate-source cha	rge	Qgs	$V_{DD}\simeq 400$ V, $V_{GS}=10$ V, $I_{D}=3.5$ A		10		nC
Gate-drain charge Q _{gd}		Qgd			6		

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)			_	_	3.5	A
Pulse drain reverse current (Note 1)	I _{DRP}	<u> </u>	_		14	А
Forward voltage (diode)	VDSF	I _{DR} = 3.5 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	trr	I _{DR} = 3.5 A, V _{GS} = 0 V,	_	1400	_	ns
Reverse recovery charge	Qrr	dI _{DR} /dt = 100 A/μs	_	9.0	_	μC

Marking

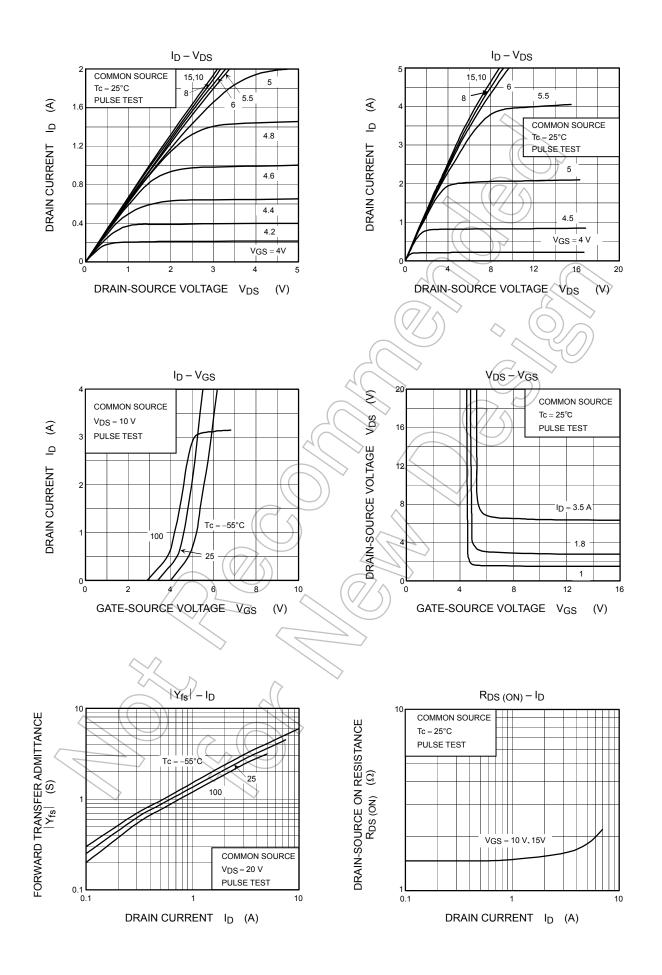


Note 4: A line under a Lot No. identifies the indication of product Labels.

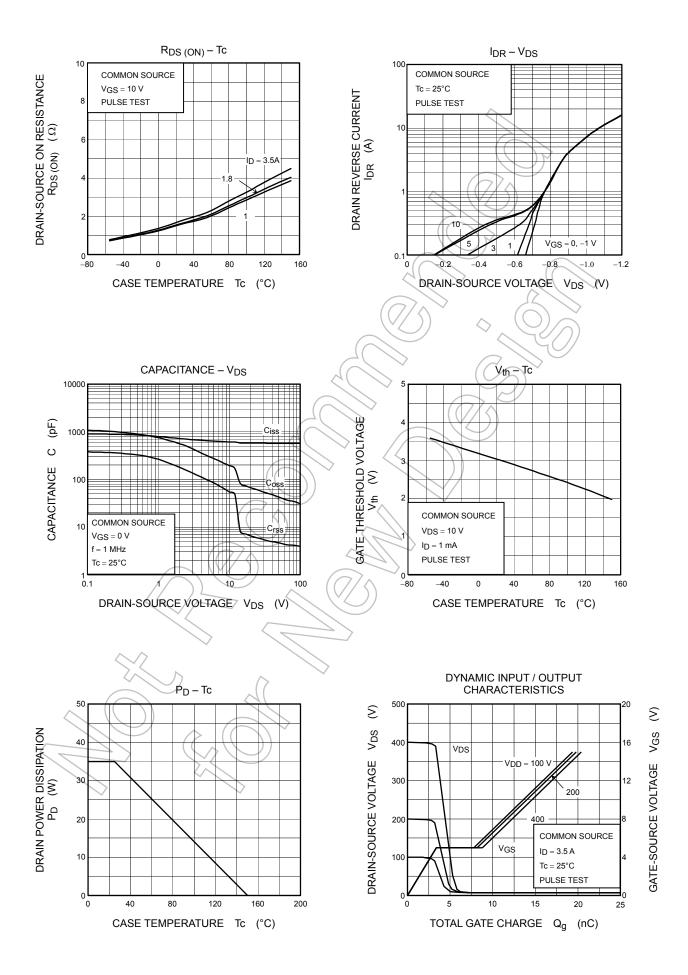
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

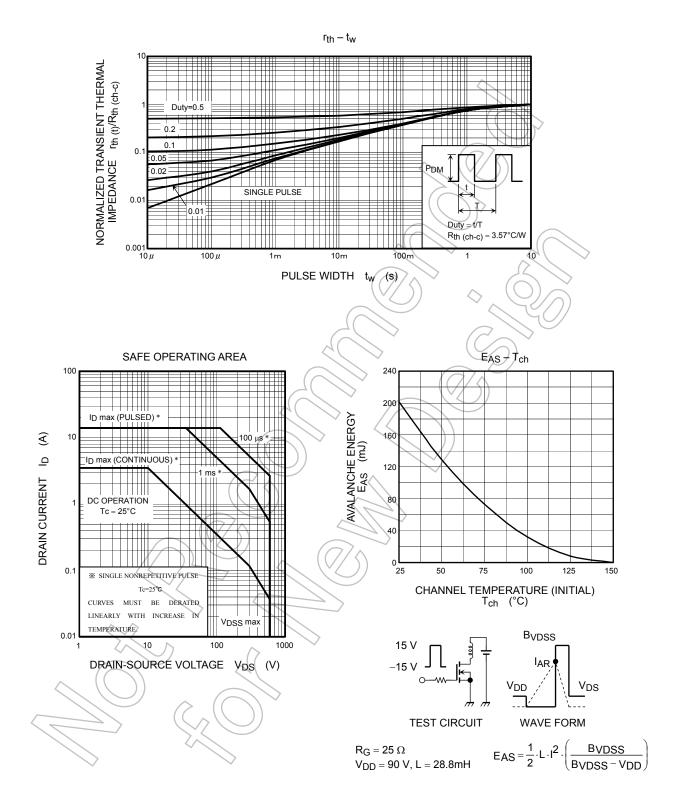
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

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